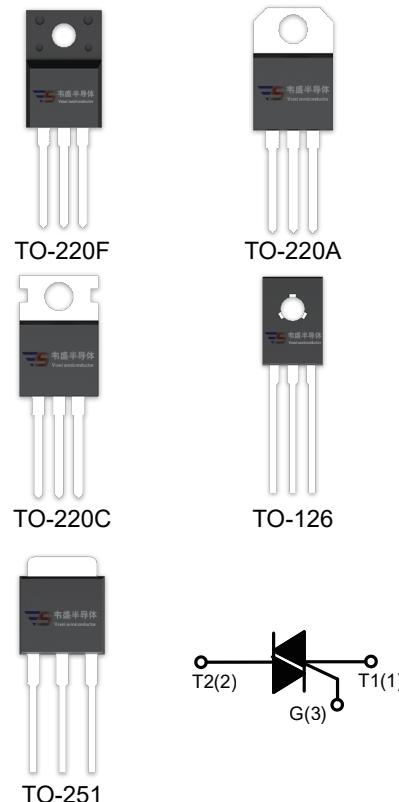


DESCRIPTION:

The BT234X-800E SCR series with the parallel resistor between Gate and Cathode are especially recommended for use on straight hair, igniter, anion generator, etc.



MAIN FEATURES

Symbol	Value	Unit
$I_T(\text{RMS})$	4	A
$V_{\text{DRM}}/V_{\text{RRM}}$	600/800	V

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	°C
Operating junction temperature range	T_j	-40-125	°C
Repetitive peak off-state voltage($T_j=25^\circ\text{C}$)	V_{DRM}	600/800	V
Repetitive peak reverse voltage($T_j=25^\circ\text{C}$)	V_{RRM}	600/800	V
Non repetitive surge peak Off-state voltage	V_{DSM}	$V_{\text{DRM}} + 100$	V
Non repetitive peak reverse voltage	V_{RSM}	$V_{\text{RRM}} + 100$	V
RMS on-state current	I_{TRMS}	4	A

Non repetitive surge peak on-state current (full cycle, F=50Hz)		I _{TSM}	35	A
I ² t value for fusing (tp=10ms)		I ² t	6.1	A ² s
Critical rate of rise of on-state current (I _G =2×I _{GT})	I - II - III	dI/dt	50	A/μs
	IV		10	
Peak gate current		I _{GM}	2	A
Average gate power dissipation		P _{G(AV)}	0.5	W
Peak gate power		P _{GM}	5	W

ELECTRICAL CHARACTERISTICS (T_j=25°C unless otherwise specified)

Symbol	Test Condition	Quadrant		Value				Unit
				T	D	E	F	
I _{GT}	V _D =12V	I - II - III	MAX	5	5	10	25	mA
		IV		5	10	25	70	
V _{GT}		ALL	MAX	1.3				V
V _{GD}	V _D =V _{DRM} T _j =125°C R _L =3.3KΩ	ALL	MIN	0.2				V
I _L	I _G =1.2I _{GT}	I - III	MAX	10	20	30	40	mA
		II - IV		15	35	45	60	
I _H	I _T =100mA		MAX	5	15	25	30	mA
dV/dt	V _D =2/3V _{DRM} Gate Open T _j =125°C		MIN	20	50	100	150	V/μs
(dV/dt)c	(dI/dt)c=1.7A/ms T _j =125°C		MIN	0.1	0.1	0.5	5	V/μs

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX)	Unit
V _{TM}	I _{TM} =5.5A tp=380μs		T _j =25°C	1.6
I _{DRM}	V _D =V _{DRM} V _R =V _{RRM}		T _j =25°C	5
I _{RRM}			T _j =125°C	0.5
				mA



THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case(AC)	TO-251	2.8
		TO-220A(Ins)	3.5
		TO-220A(Non-Ins)/ TO-220C	2.5
		TO-220F(Ins)	3.3
		TO-126/SOT-82	3.7
		TO-202-3	3.9

FIG.1: Maximum power dissipation versus RMS on-state current

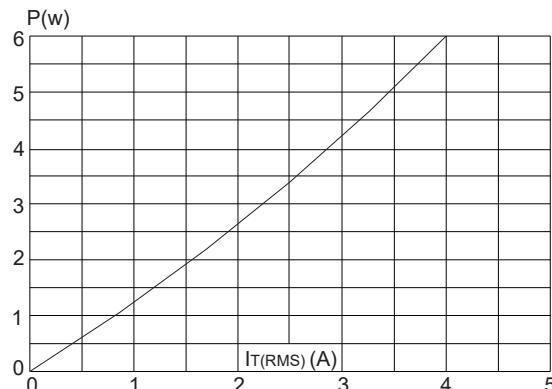


FIG.3: Surge peak on-state current versus number of cycles

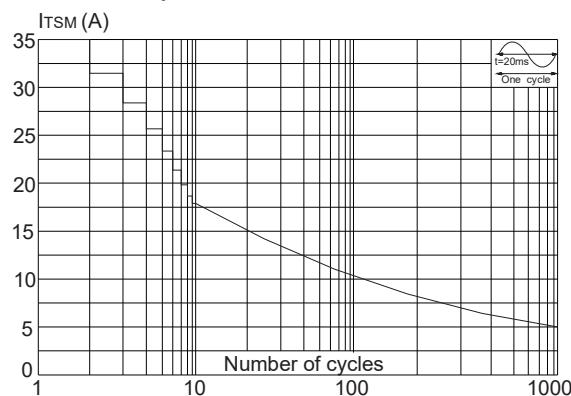


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$ and corresponding value of I^2t (I - II - III: $dI/dt < 50\text{A}/\mu\text{s}$; IV: $dI/dt < 10\text{A}/\mu\text{s}$)

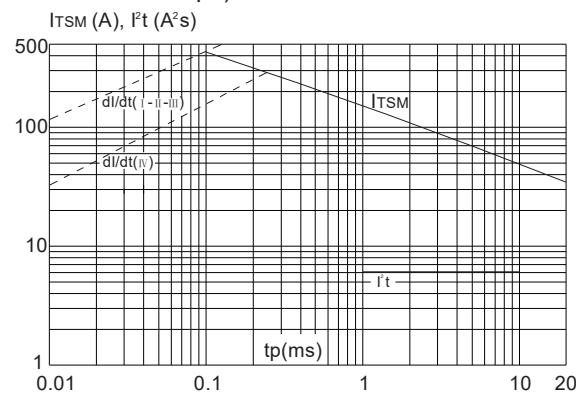


FIG.2: RMS on-state current versus case temperature

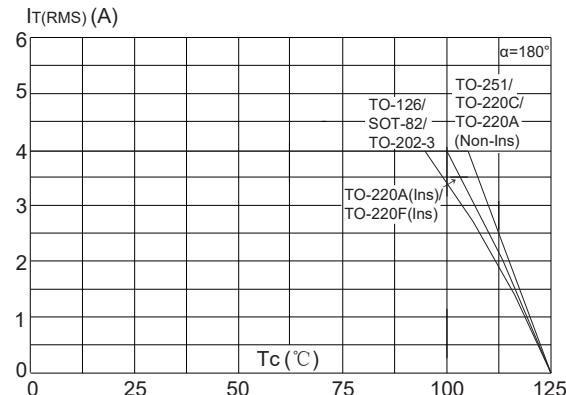


FIG.4: On-state characteristics (maximum values)

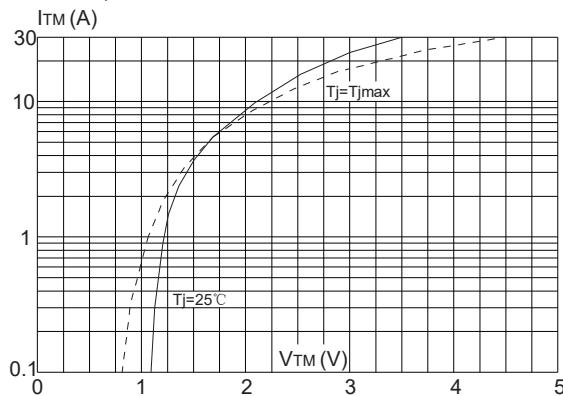


FIG.6: Relative variations of gate trigger current versus junction temperature

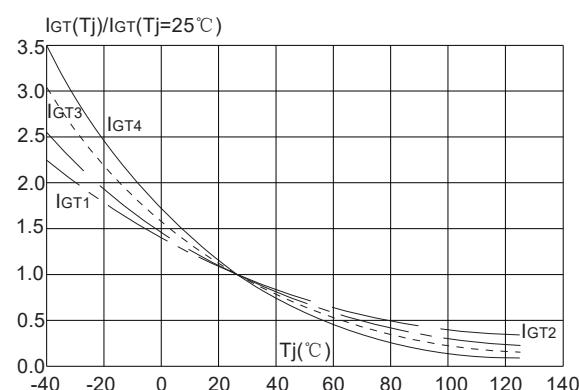


FIG.7: Relative variations of holding current versus junction temperature

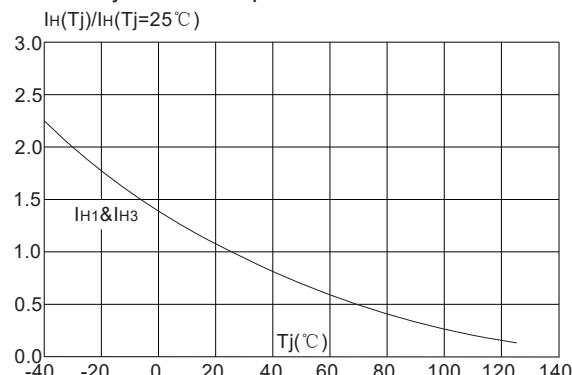


FIG.8: Relative variations of latching current versus junction temperature

