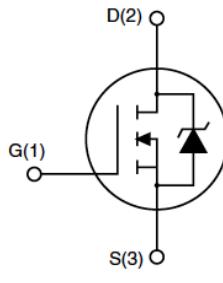


Description

Features <ul style="list-style-type: none"> ● 600V,20A ● $R_{DS(ON)} = 0.3\Omega$ (Typ.) @ $V_{GS} = 10V$, $I_D = 10A$ ● Fast Switching ● Improved dv/dt Capability ● 100% Avalanche Tested 	Application <ul style="list-style-type: none"> ● Switch Mode Power Supply(SMPS) ● Uninterruptible Power Supply(UPS) ● Power Factor Correction (PFC)
 TO-3P  TO-247  TO-220F	 Schematic Diagram

Absolute Maximum Ratings ($T_c=25^\circ C$ unless otherwise specified)

Symbol	Parameter	Max.		Units
		TO-3P/TO-247	TO-220F	
V_{DSS}	Drain-Source Voltage	600		V
V_{GSS}	Gate-Source Voltage	± 30		V
I_D	Continuous Drain Current	20		A
		13		A
I_{DM}	Pulsed Drain Current ^{note1}	80		A
E_{AS}	Single Pulsed Avalanche Energy ^{note2}	1350		mJ
P_D	Power Dissipation	416	167	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.3	0.75	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	60	90	$^\circ C/W$
T_J , T_{STG}	Operating and Storage Temperature Range	-55 to +150		$^\circ C$

Electrical Characteristics ($T_C=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristics						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	600	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 600\text{V}, V_{GS} = 0\text{V}, T_J = 25^\circ\text{C}$	-	-	1	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 30\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D=250\mu\text{A}$	2	3	4	V
$R_{DS(\text{on})}$	Static Drain-Source on-Resistance note3	$V_{GS} = 10\text{V}, I_D = 10\text{A}$	-	0.3	0.45	Ω
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS} = 25\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$	-	2980	-	pF
C_{oss}	Output Capacitance		-	291	-	pF
C_{rss}	Reverse Transfer Capacitance		-	40	-	pF
Q_g	Total Gate Charge	$V_{DD} = 480\text{V}, I_D = 20\text{A}, V_{GS} = 10\text{V}$	-	80	-	nC
Q_{gs}	Gate-Source Charge		-	12	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	34	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD} = 250\text{V}, I_D = 20\text{A}, R_G = 25\Omega$	-	37	-	ns
t_r	Turn-on Rise Time		-	66	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	175	-	ns
t_f	Turn-off Fall Time		-	84	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_s	Maximum Continuous Drain to Source Diode Forward Current	-	-	20	A	
I_{sM}	Maximum Pulsed Drain to Source Diode Forward Current	-	-	80	A	
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_{SD} = 20\text{A}$	-	-	1.4	V
t_{rr}	Reverse Recovery Time	$V_{GS} = 0\text{V}, I_s = 20\text{A}, di/dt = 100\text{A}/\mu\text{s}$	-	450	-	ns
Q_{rr}	Reverse Recovery Charge		-	7.1	-	μC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. $I_{AS} = 16\text{A}, V_{DD} = 50\text{V}, R_G = 25 \Omega, \text{Starting } T_J = 25^\circ\text{C}$

3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 1\%$

Typical Performance Characteristics

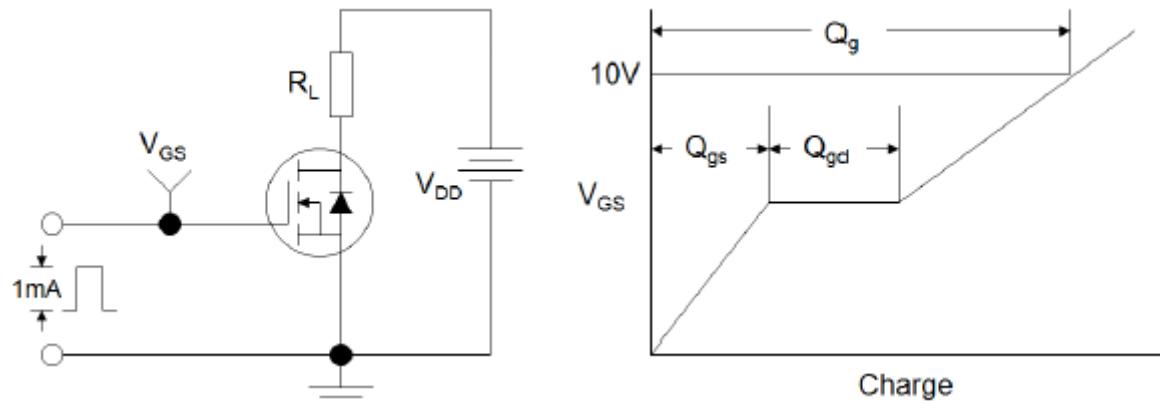


Figure1:Gate Charge Test Circuit & Waveform

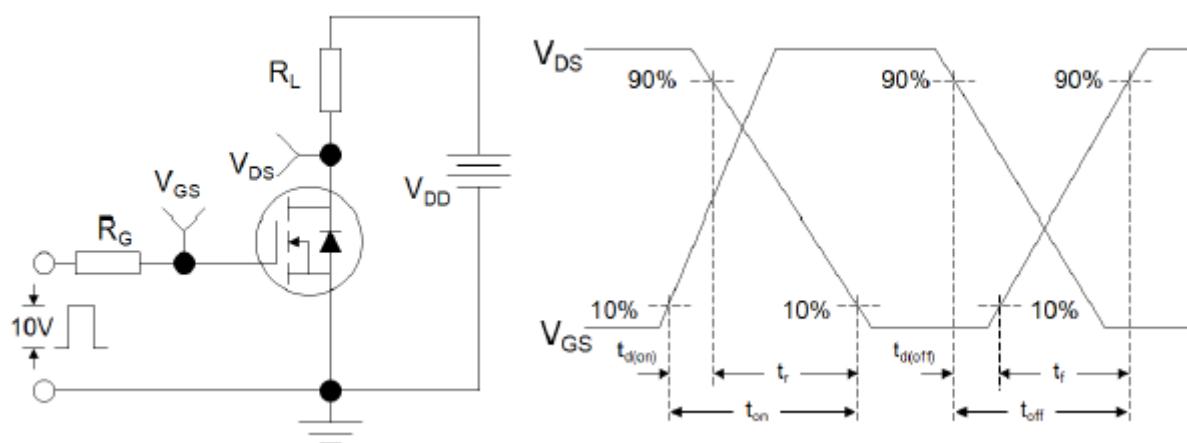


Figure 2: Resistive Switching Test Circuit & Waveforms

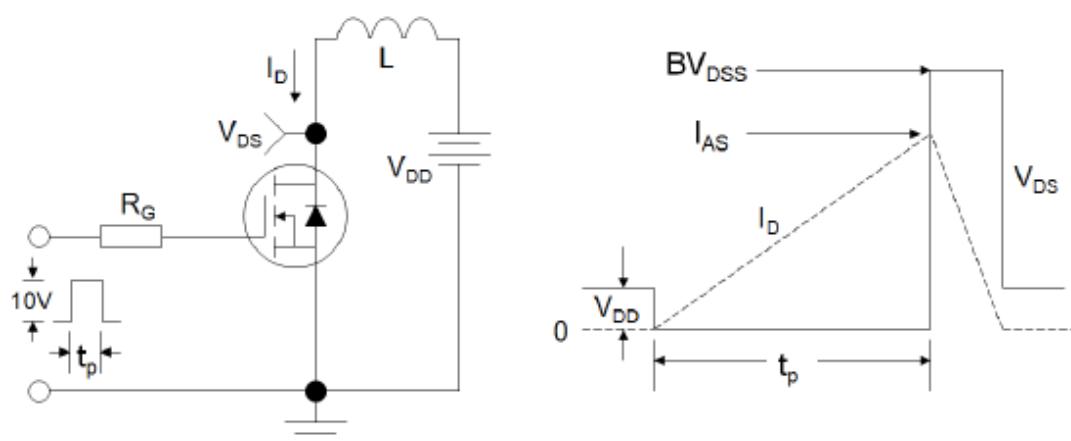


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms

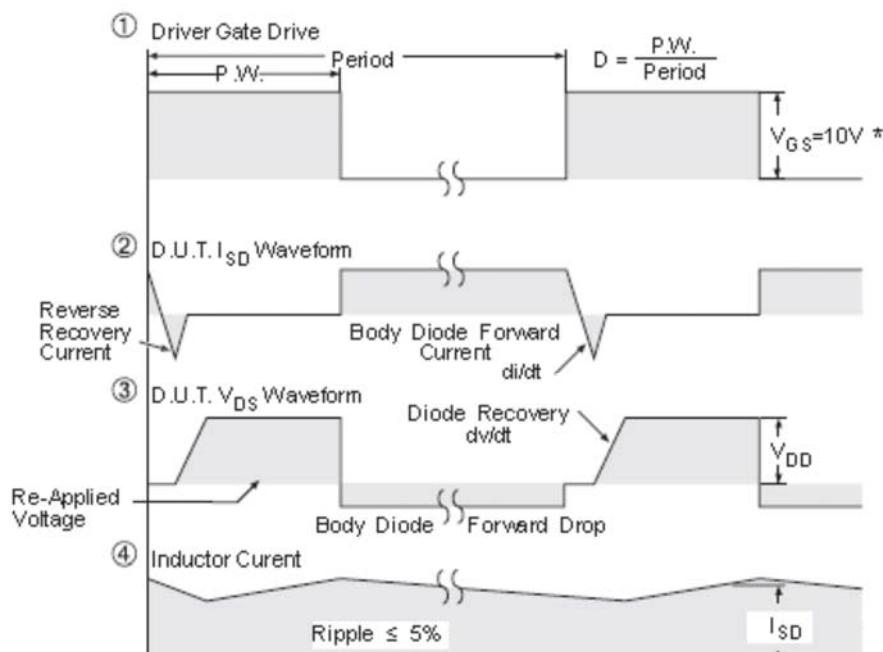
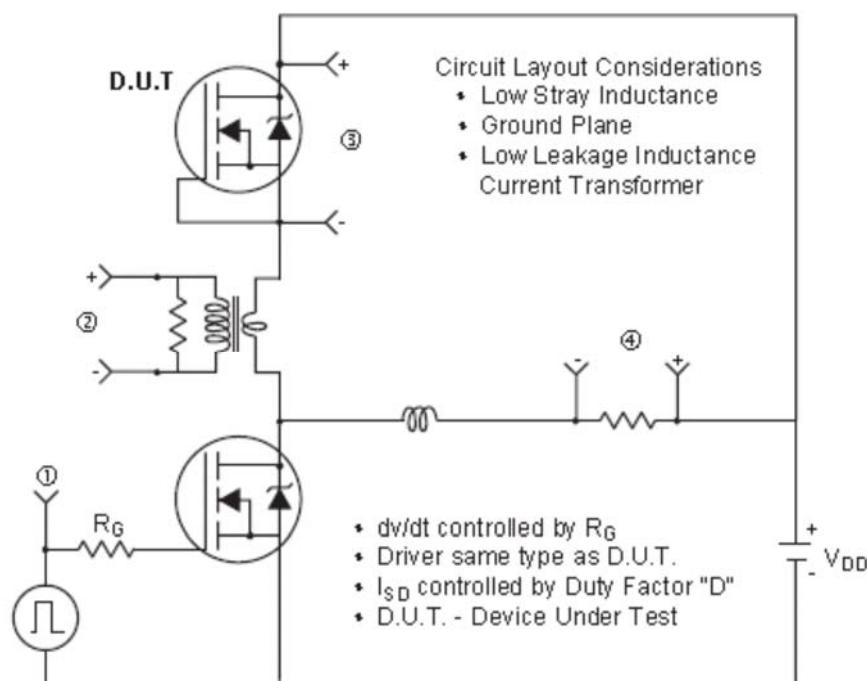


Figure 4:Peak Diode Recovery dv/dt Test Circuit & Waveforms (For N-channel)