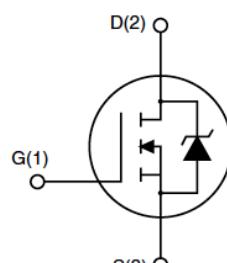


## Description

<p><b>Features</b></p> <ul style="list-style-type: none"> <li>● 900V, 9A</li> <li>● <math>R_{DS(ON)} = 1.0 \Omega</math> (Typ.) @ <math>V_{GS} = 10V</math>, <math>I_D = 4.5A</math></li> <li>● Fast Switching</li> <li>● 100% Avalanche Tested</li> <li>● Improved dv/dt Capability</li> </ul>	<p><b>Application</b></p> <ul style="list-style-type: none"> <li>● Switch Mode Power Supply (SMPS)</li> <li>● Uninterruptible Power Supply (UPS)</li> <li>● Power Factor Correction (PFC)</li> </ul>
 TO-220F  TO-247	 Schematic Diagram

## Absolute Maximum Ratings ( $T_C=25^\circ C$ unless otherwise specified)

Symbol	Parameter	Max.		Units
		TO-220F	TO-247	
$V_{DSS}$	Drain-Source Voltage	900		V
$V_{GSS}$	Gate-Source Voltage		$\pm 30$	V
$I_D$	Continuous Drain Current	$T_C = 25^\circ C$	9	A
		$T_C = 100^\circ C$	5.4	A
$I_{DM}$	Pulsed Drain Current <sup>note1</sup>		36	A
$E_{AS}$	Single Pulsed Avalanche Energy <sup>note2</sup>		562	mJ
$P_D$	Power Dissipation	$T_C = 25^\circ C$	65	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		1.92	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient		62.5	$^\circ C/W$
$T_J, T_{STG}$	Operating and Storage Temperature Range		-55 to +150	$^\circ C$

## Electrical Characteristics ( $T_C=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	900	-	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS} = 900\text{V}, V_{GS} = 0\text{V}, T_J = 25^\circ\text{C}$	-	-	1	$\mu\text{A}$
		$V_{DS} = 720\text{V}, V_{GS} = 0\text{V}, T_J = 125^\circ\text{C}$			100	
$I_{GSS}$	Gate to Body Leakage Current	$V_{GS} = \pm 30\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	3.0	-	4.0	V
$R_{DS(\text{on})}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{V}, I_D = 4.5\text{A}$	-	1	1.2	$\Omega$
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS} = 25\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$	-	1979	-	pF
$C_{oss}$	Output Capacitance		-	233	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	53	-	pF
$Q_g$	Total Gate Charge	$V_{DD} = 720\text{V}, I_D = 9\text{A}, V_{GS} = 10\text{V}$	-	83	-	nC
$Q_{gs}$	Gate-Source Charge		-	9	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	49	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 450\text{V}, I_D = 9\text{A}, R_G = 25\Omega$	-	23	-	ns
$t_r$	Turn-On Rise Time		-	15	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	90	-	ns
$t_f$	Turn-Off Fall Time		-	30	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Drain to Source Diode Forward Current		-	-	9	A
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current		-	-	36	A
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_{SD} = 9\text{A}, T_J = 25^\circ\text{C}$	-	-	1.4	V
$t_{rr}$	Reverse Recovery Time	$V_{GS} = 0\text{V}, I_S = 9\text{A}, di/dt = 100\text{A}/\mu\text{s}$	-	320	-	ns
$Q_{rr}$	Reverse Recovery Charge		-	4.2	-	$\mu\text{C}$

Notes: 1. Repetitive Rating: Pulse width limited by maximum junction temperature

2.  $I_{AS} = 7.5\text{A}, V_{DD} = 50\text{V}$ , Starting  $T_J = 25^\circ\text{C}$

3. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 1\%$

## Typical Performance Characteristics

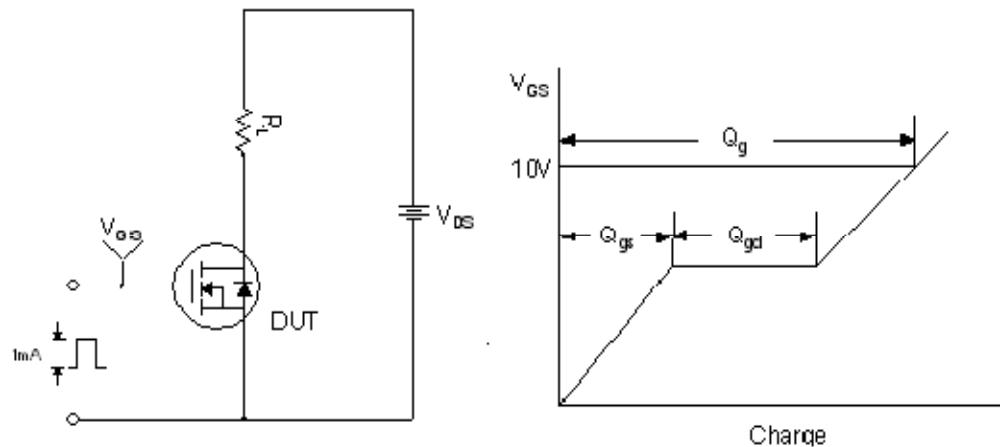


Figure 1. Gate Charge Test Circuit & Waveform

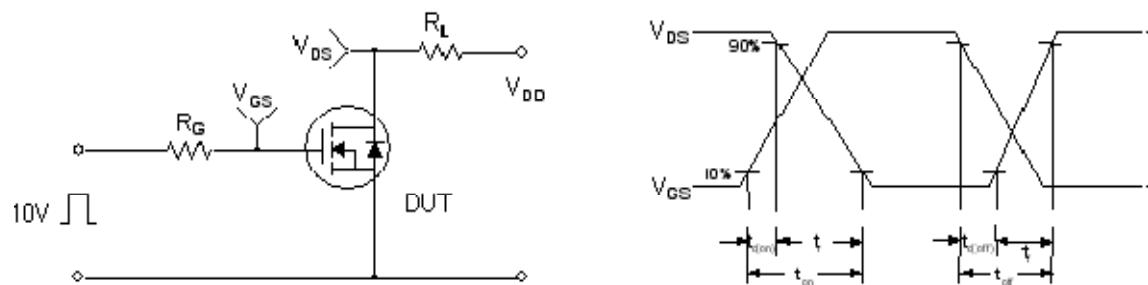


Figure 2. Resistive Switching Test Circuit & Waveforms

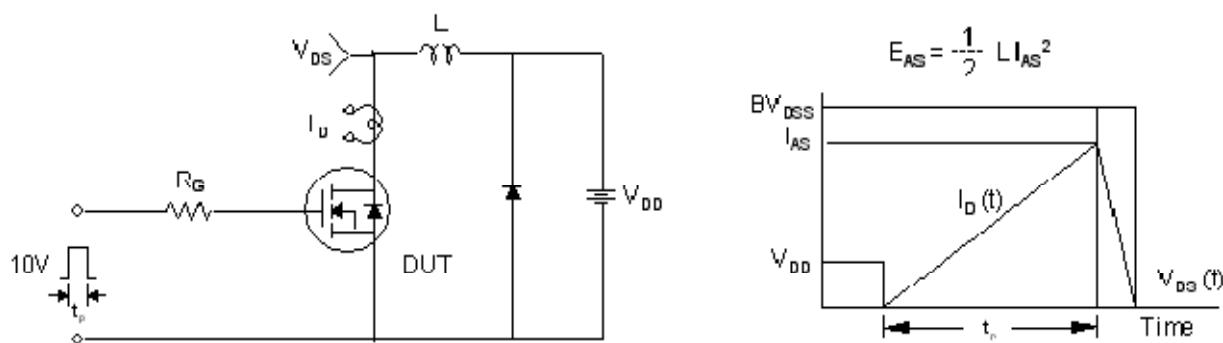
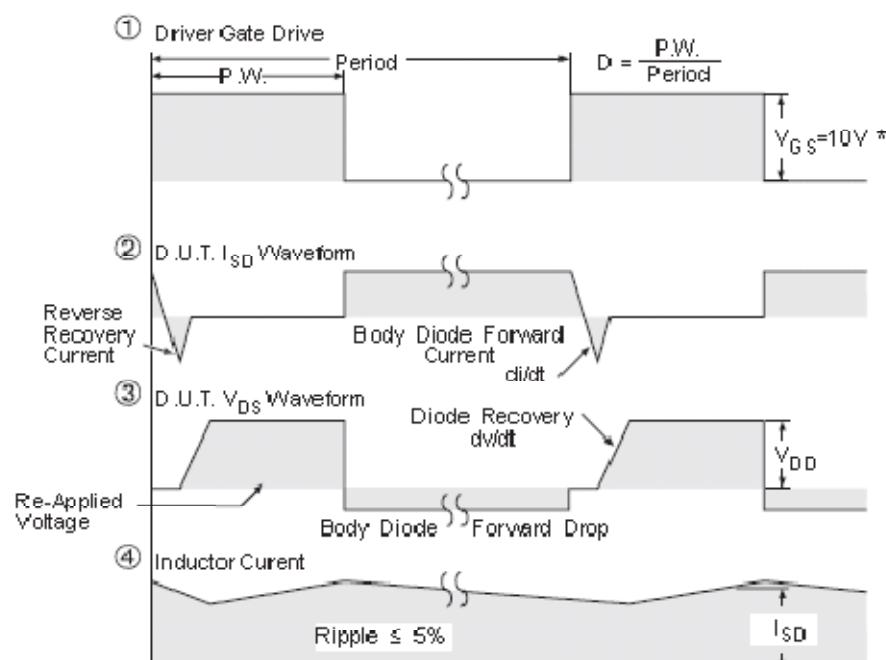
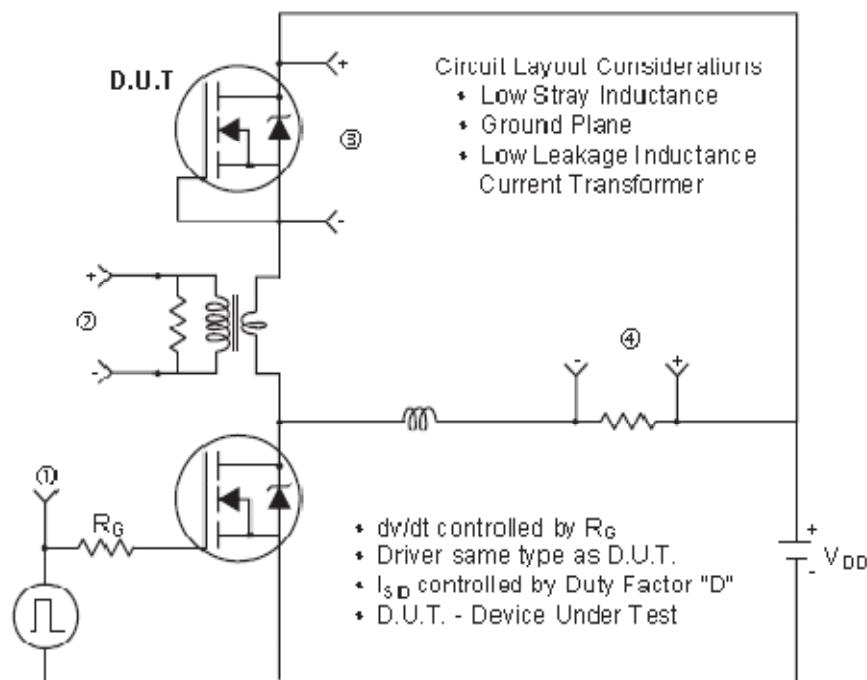


Figure 3. Unclamped Inductive Switching Test Circuit & Waveforms



\*  $V_{GS} = 5V$  for Logic Level Devices

Figure 4. Peak Diode Recovery  $dv/dt$  Test Circuit & Waveforms (For N-channel)