

Description

The VSM30P15 uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

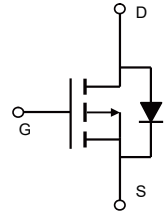
- $V_{DS} = -150V, I_D = -30A$
 $R_{DS(ON)} < 90m\Omega @ V_{GS} = -10V$ (Typ: 82m Ω)
- Super high dense cell design
- Advanced trench process technology
- Reliable and rugged
- High density cell design for ultra low On-Resistance
- Pb-free lead plating
- 100% UIS tested
- 100% ΔV_{ds} tested

Application

- Portable equipment and battery powered systems



TO-252



Schematic

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
VSM30P15	VSM30P15-T2	TO-252	-	-	-

Absolute Maximum Ratings ($T_c = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-150	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	-30	A
Drain Current-Continuous($T_c = 100^\circ C$)	$I_D(100^\circ C)$	-21.2	A
Pulsed Drain Current	I_{DM}	-120	A
Maximum Power Dissipation	P_D	187	W
Single pulse avalanche energy (Note 5)	E_{AS}	353	mJ
Derating factor		1.25	W/ $^\circ C$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	$^\circ C$

Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	$R_{\theta JC}$	0.8	$^\circ C/W$
Thermal Resistance, Junction-to-Ambient (Note 4)	$R_{\theta JA}$	50	$^\circ C/W$

Electrical Characteristics (T_c=25°C unless otherwise noted)

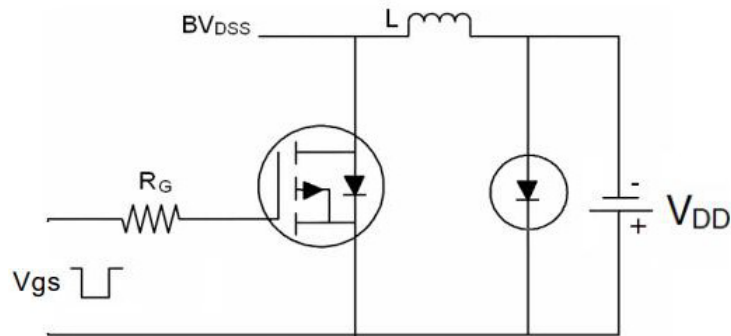
Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250μA	-150	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-150V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-2	-3	-4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-20A	-	82	90	mΩ
Forward Transconductance	g _{FS}	V _{DS} =-10V, I _D =-20A	-	22	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{iss}	V _{DS} =-75V, V _{GS} =0V, F=1.0MHz	-	5448	-	pF
Output Capacitance	C _{oss}		-	117	-	pF
Reverse Transfer Capacitance	C _{rss}		-	73	-	pF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =-75V, I _D =-20A V _{GS} =-10V, R _{GEN} =9.1Ω	-	17	-	nS
Turn-on Rise Time	t _r		-	80	-	nS
Turn-Off Delay Time	t _{d(off)}		-	45	-	nS
Turn-Off Fall Time	t _f		-	65	-	nS
Total Gate Charge	Q _g	V _{DS} =-75V, I _D =-20A, V _{GS} =-10V	-	111.5	-	nC
Gate-Source Charge	Q _{gs}		-	25.7	-	nC
Gate-Drain Charge	Q _{gd}		-	24.8	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V, I _S =-20A	-	-	-1.2	V
Diode Forward Current (Note 2)	I _S	-	-	-	-30	A
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F =-20A di/dt = 100A/μs ^(Note3)	-	90	-	nS
Reverse Recovery Charge	Q _{rr}		-	145	-	nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

Notes:

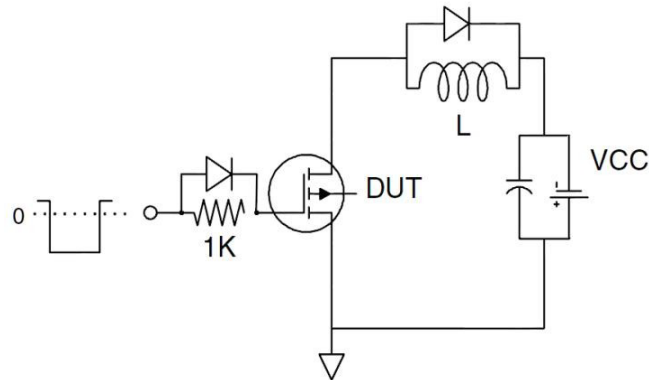
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. The value of R_{θJA} is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A =25° C. The value in any given application depends on the user's specific board design, and the maximum temperature of 150° C may be used if the PCB allows it.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production
5. EAS condition: T_J=25°C, V_{DD}=-50V, V_G=-10V, L=0.5mH, R_g=25Ω

Test Circuit

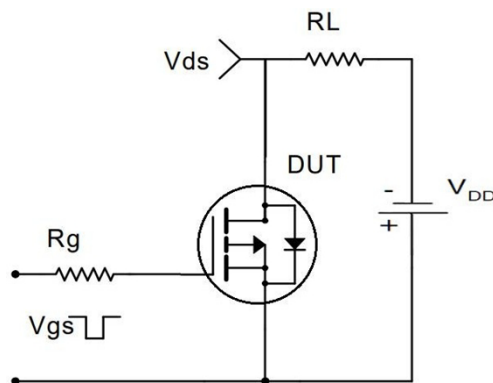
1) E_{AS} test Circuit



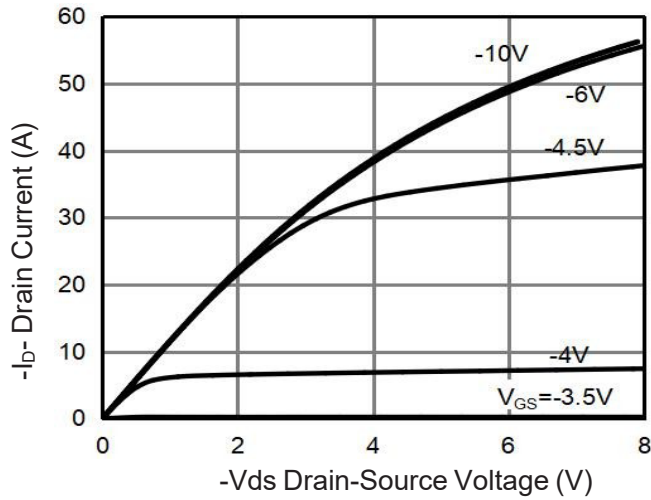
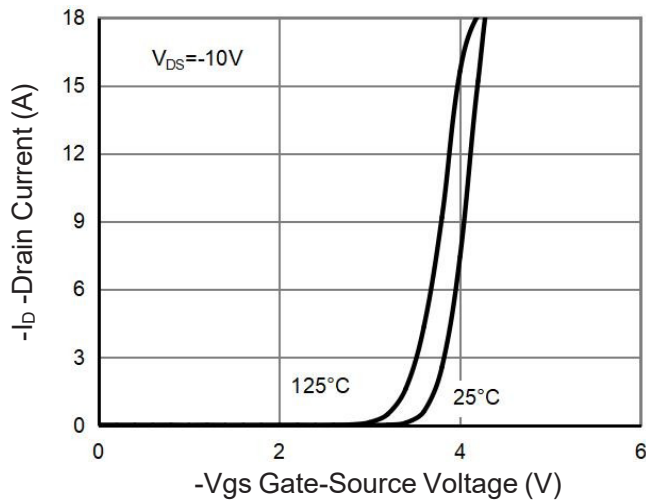
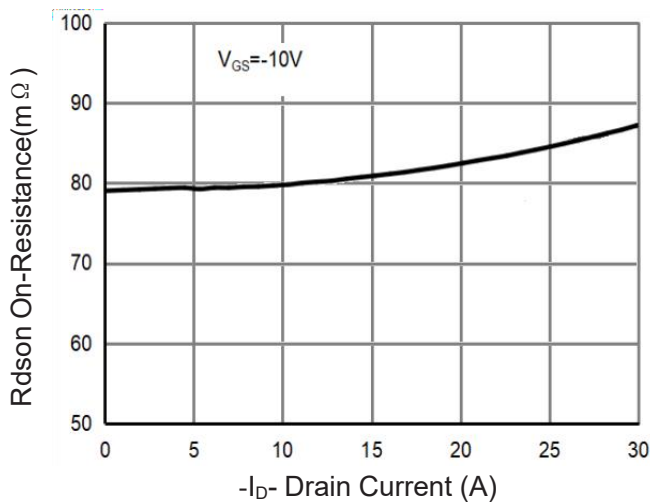
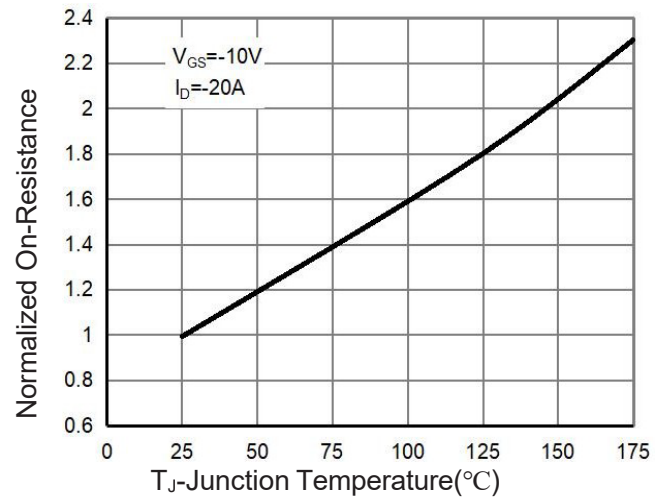
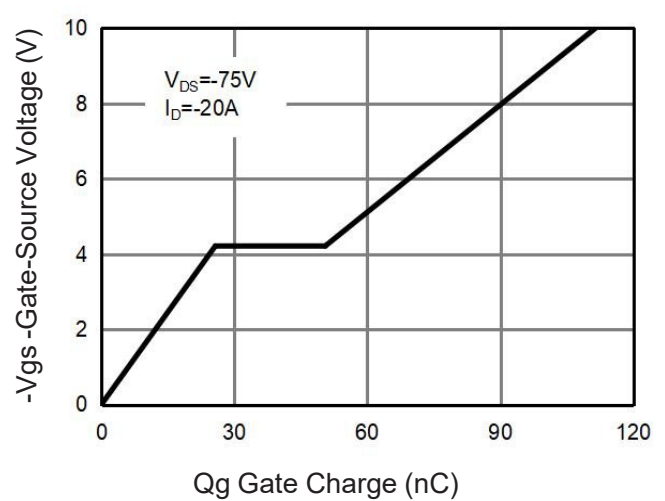
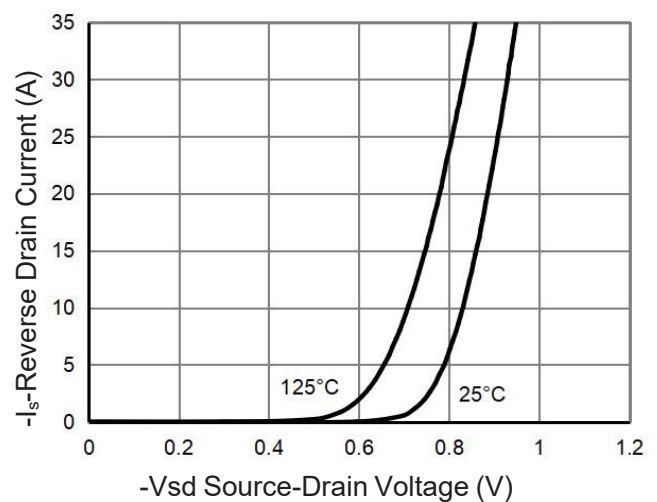
2) Gate charge test Circuit

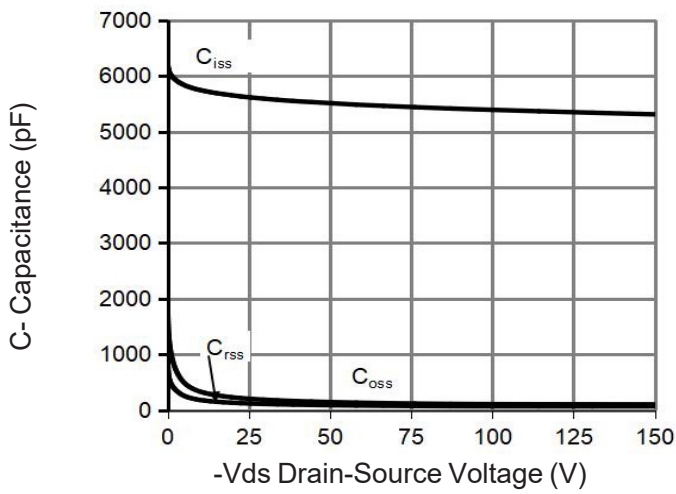
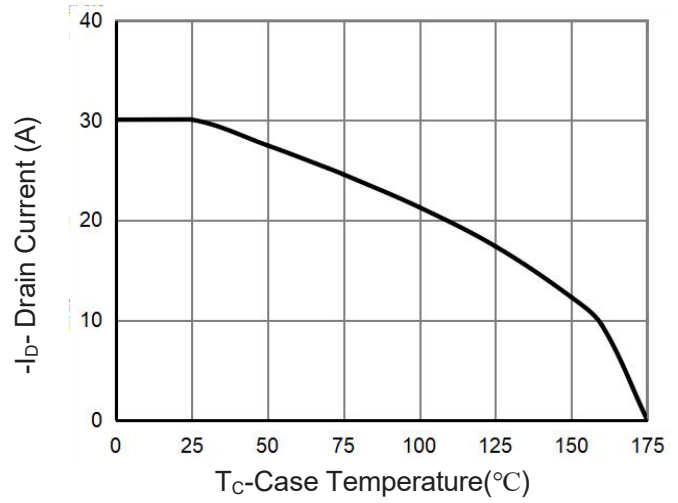
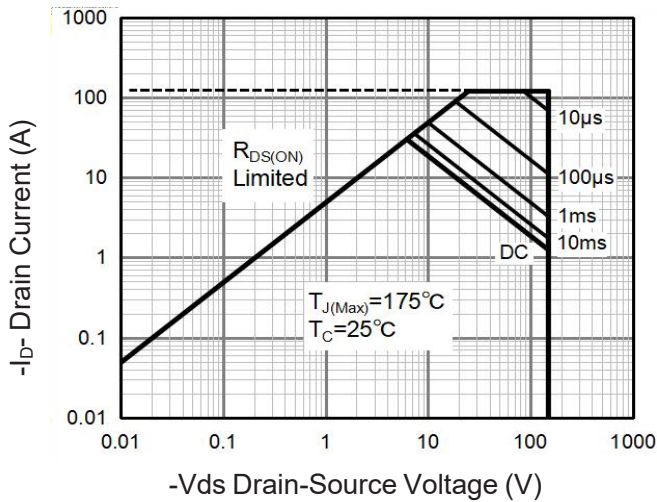
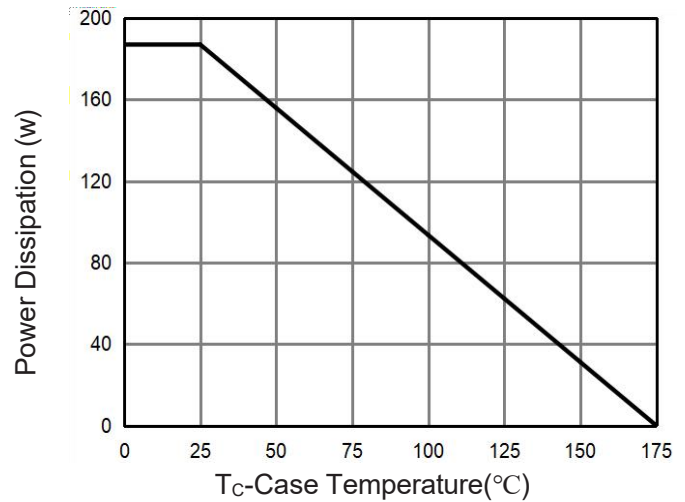
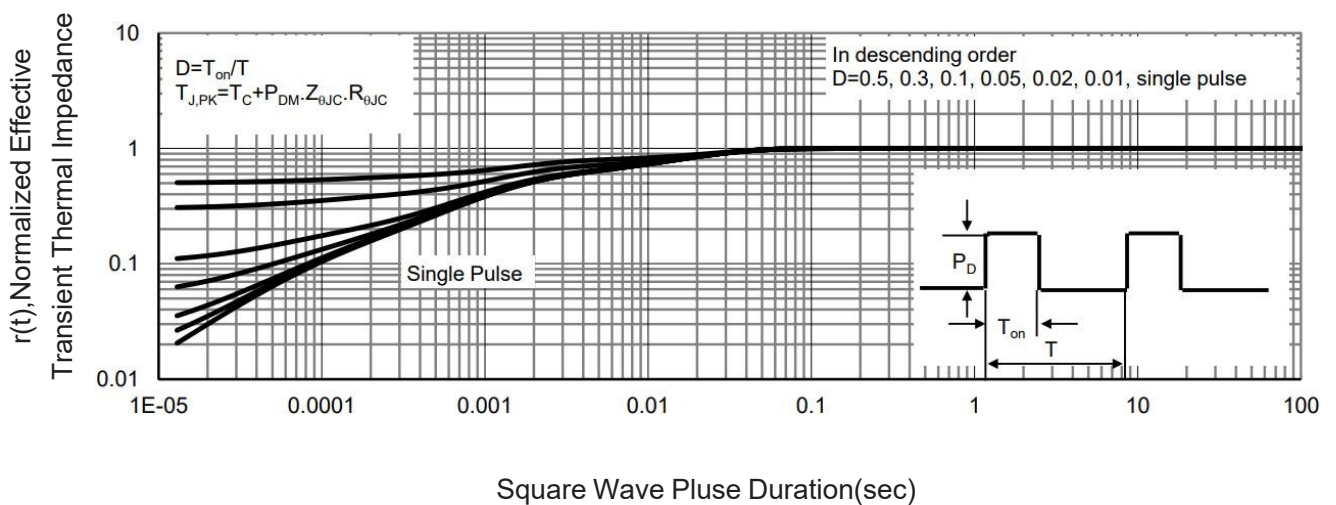


3) Switch Time Test Circuit



Typical Electrical and Thermal Characteristics (Curves)


Figure 1 Output Characteristics

Figure 2 Transfer Characteristics

Figure 3 Rdson- Drain Current

Figure 4 Rdson-Junction Temperature

Figure 5 Gate Charge

Figure 6 Source- Drain Diode Forward


Figure 7 Capacitance vs Vds

Figure 9 Drain Current vs Case Temperature

Figure 8 Safe Operation Area

Figure 10 Power De-rating

Figure 11 Normalized Maximum Transient Thermal Impedance