

Description

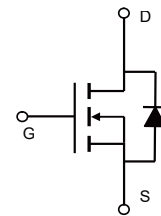
The series of devices uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

General Features

- $V_{DS} = 100V, I_D = 185A$
 $R_{DS(ON)} = 2.6m\Omega$, typical (TO-220) @ $V_{GS} = 10V$
 $R_{DS(ON)} = 2.4m\Omega$, typical (TO-263) @ $V_{GS} = 10V$
- Excellent gate charge x $R_{DS(on)}$ product(FOM)
- Very low on-resistance $R_{DS(on)}$
- 175 °C operating temperature
- Pb-free lead plating



Schematic

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
VST10N020	VST10N020-T3	TO-263	-	-	-

Absolute Maximum Ratings ($T_c = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous($T_c = 25^\circ C$)	I_D	185	A
Drain Current-Continuous($T_c = 100^\circ C$)	$I_D (100^\circ C)$	130	A
Pulsed Drain Current ($T_c = 25^\circ C$)	I_{DM}	740	A
Maximum Power Dissipation($T_c = 25^\circ C$)	P_D	275	W
Derating factor		1.83	W/ $^\circ C$
Single pulse avalanche energy ^(Note 1)	E_{AS}	1692	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	$^\circ C$

Thermal Characteristic

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.55	$^{\circ}\text{C/W}$
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Electrical Characteristics ($T_c=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	100		-	V	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V	-	-	1	μA	
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA	
On Characteristics							
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	2.0	3.0	4.0	V	
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =92.5A	TO-220	-	2.6	2.9	mΩ
			TO-263		2.4	2.9	mΩ
Gate resistance	R _G		-	1.9	-	Ω	
Forward Transconductance	g _{FS}	V _{DS} =5V, I _D =92.5A	90	-	-	S	
Dynamic Characteristics							
Input Capacitance	C _{iss}	V _{DS} =50V, V _{GS} =0V, F=1.0MHz	-	13700	-	PF	
Output Capacitance	C _{oss}		-	1050	-	PF	
Reverse Transfer Capacitance	C _{rss}		-	41	-	PF	
Switching Characteristics (Note 2)							
Turn-on Delay Time	t _{d(on)}	V _{DD} =50V, I _D =92.5A V _{GS} =10V, R _G =1.6Ω	-	34	-	nS	
Turn-on Rise Time	t _r		-	38	-	nS	
Turn-Off Delay Time	t _{d(off)}		-	84	-	nS	
Turn-Off Fall Time	t _f		-	28	-	nS	
Total Gate Charge	Q _g	V _{DS} =50V, I _D =92.5A, V _{GS} =10V	-	185	-	nC	
Gate-Source Charge	Q _{gs}		-	54		nC	
Gate-Drain Charge	Q _{gd}		-	48		nC	
Drain-Source Diode Characteristics							
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =92.5A	-		1.2	V	
Diode Forward Current	I _S		-	-	185	A	
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F = 185A	-	86	-	nS	
Reverse Recovery Charge	Q _{rr}	di/dt = 100A/μs	-	210	-	nC	

Notes:

1. EAS condition : $T_J=25^{\circ}\text{C}, V_{DD}=50V, V_G=10V, L=0.5mH, R_g=25\Omega$
2. Guaranteed by design, not subject to production
3. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of $T_J(MAX)=175^{\circ}\text{C}$. The SOA curve provides a single pulse rating.

Typical Electrical and Thermal Characteristics

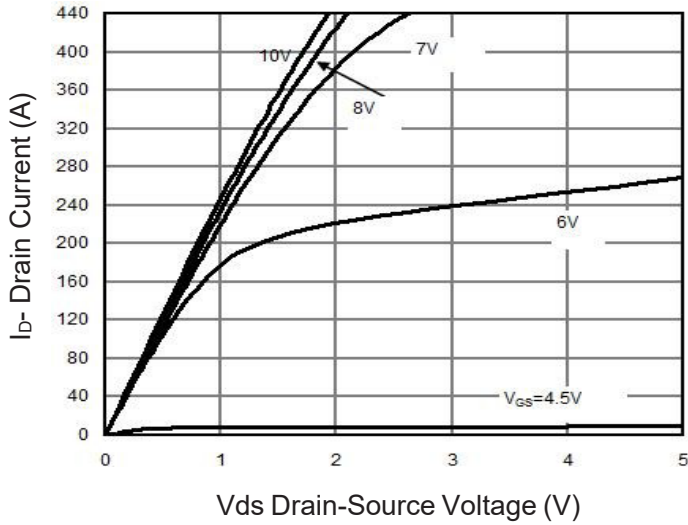


Figure 1 Output Characteristics

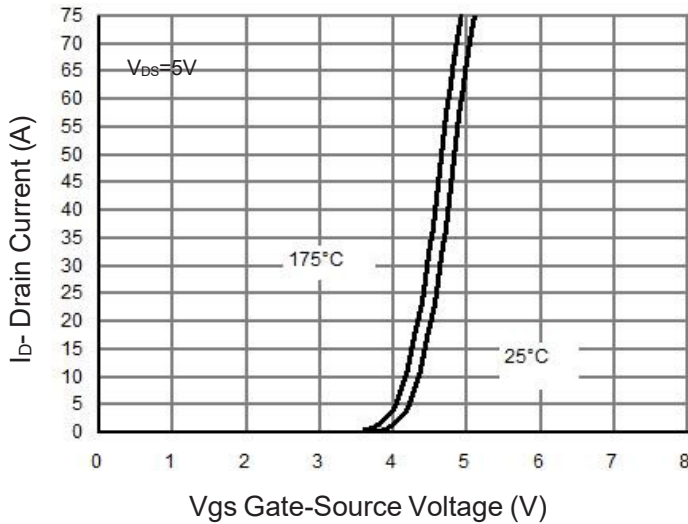


Figure 2 Transfer Characteristics

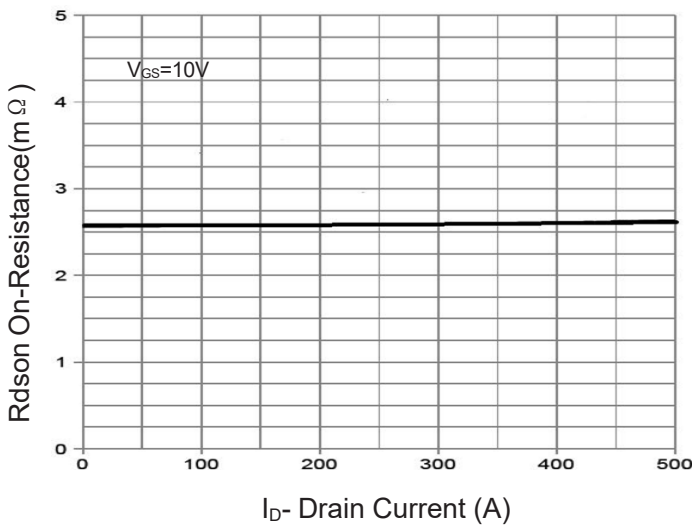


Figure 3 Rdson- Drain Current

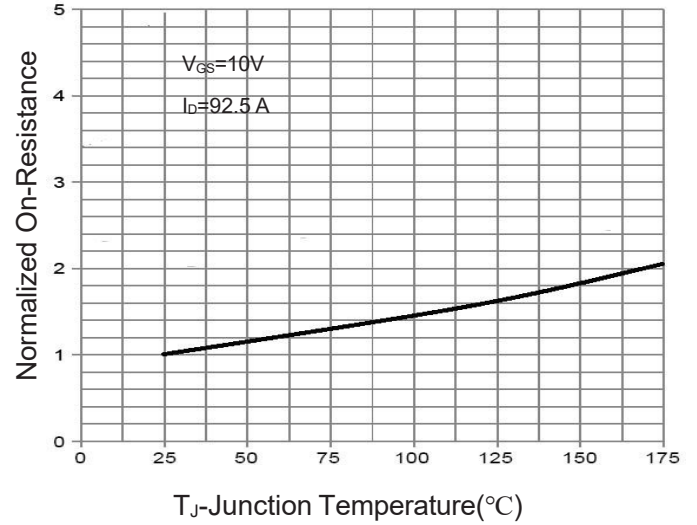


Figure 4 Rdson-Junction Temperature

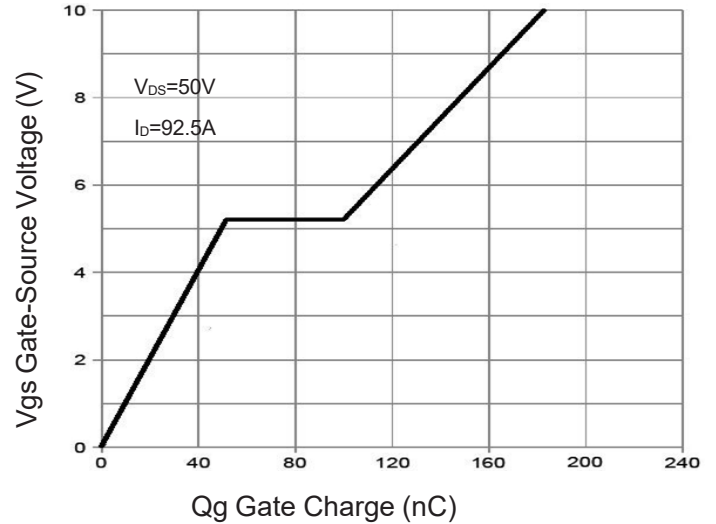


Figure 5 Gate Charge

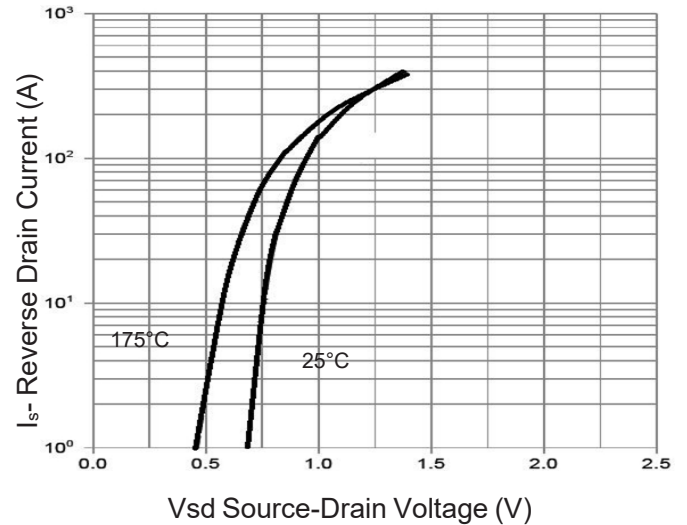
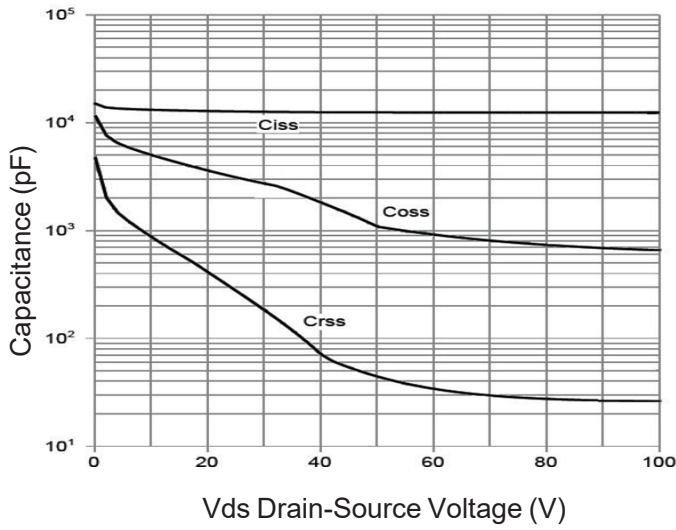
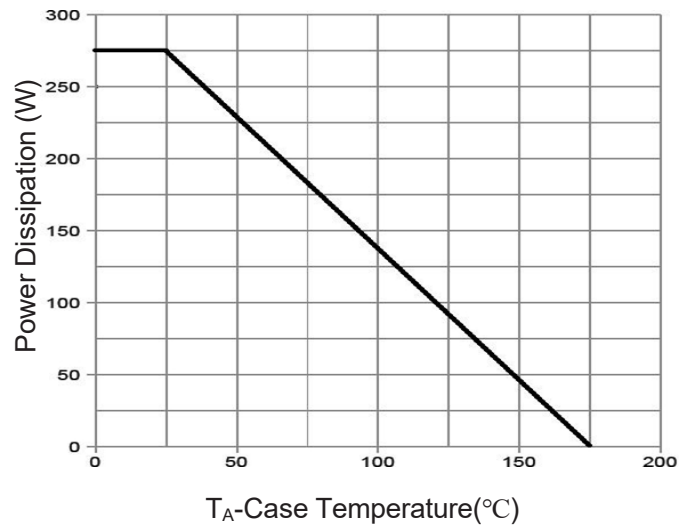
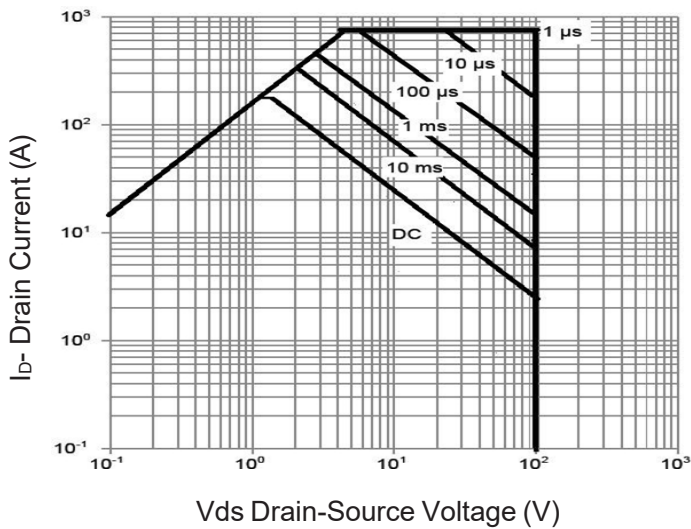
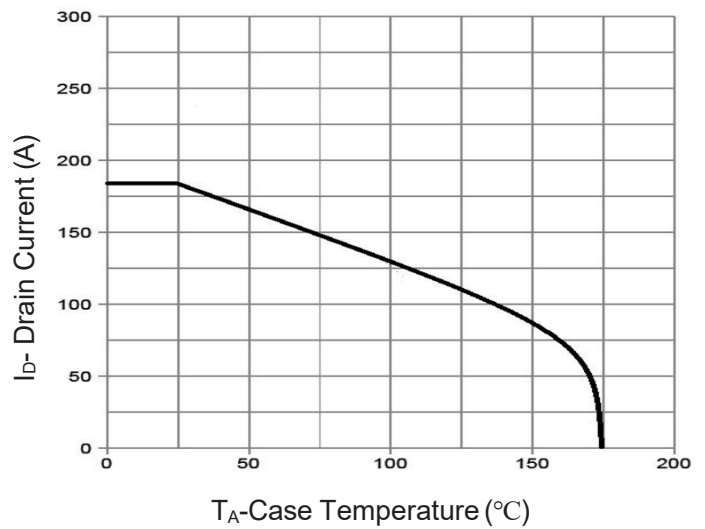
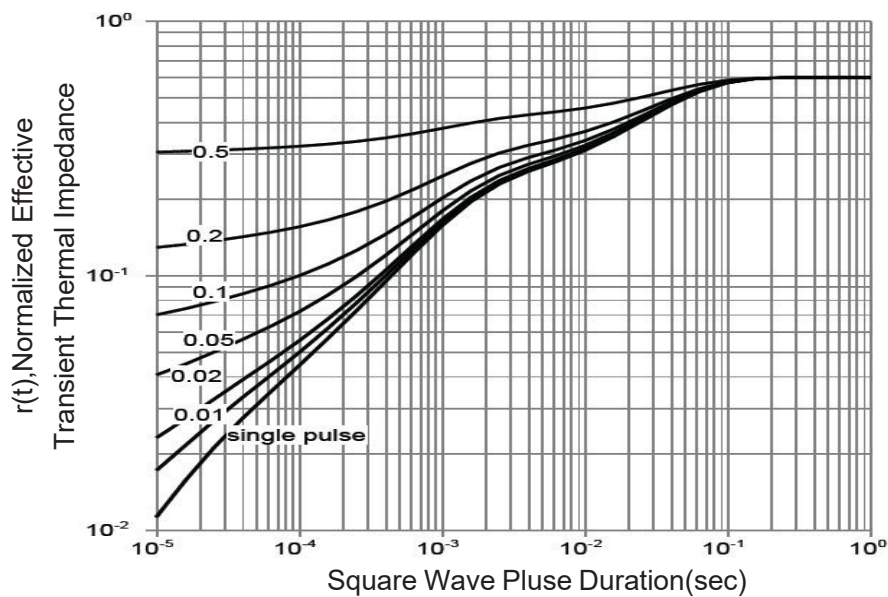


Figure 6 Source- Drain Diode Forward


Figure 7 Capacitance vs Vds

Figure 9 Power De-rating

Figure 8 Safe Operation Area (Note 3)

Figure 10 Current De-rating

Figure 11 Normalized Maximum Transient Thermal Impedance