

General Description

The series of devices use advanced trench gate super junction technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

Features

- Optimized body diode reverse recovery performance
- Low on-resistance and low conduction losses
- Small package
- Ultra Low Gate Charge cause lower driving requirements
- 100% Avalanche Tested
- ROHS compliant

Application

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)
- LLC Half-bridge



TO-263



Schematic

Parameter	Symbol	Value	Unit
Drain-Source Voltage (V _{GS} =0V)	V _{DS}	650	V
Gate-Source Voltage (V _{DS} =0V) AC (f>1 Hz)	V _{GS}	±30	V
Gate-Source Voltage (V _{DS} =0V) DC	V _{GS}	±20	V
Continuous Drain Current at T _c =25°C	I _{D (DC)}	11	A
Continuous Drain Current at T _c =100°C	I _{D (DC)}	7.7	A
Pulsed drain current (Note 1)	I _{DM (pluse)}	44	A
Maximum Power Dissipation(T _c =25°C)	P _D	107	W
Derate above 25°C		0.71	W/°C
Avalanche current (Note 2)	I _{AS}	3	A
Drain Source voltage slope, V _{DS} ≤480 V,	dv/dt	50	V/ns
Reverse diode dv/dt, V _{DS} ≤480 V, I _{SD} <I _D	dv/dt	15	V/ns
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55...+175	°C

* limited by maximum junction temperature

Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	R_{thJC}	1.4	$^{\circ}\text{C}/\text{W}$
Thermal Resistance, Junction-to-Ambient (Maximum)	R_{thJA}	62	$^{\circ}\text{C}/\text{W}$

Table 3. Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
On/off states						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	650			V
Zero Gate Voltage Drain Current(Tc=25℃)	I _{DSS}	V _{DS} =650V, V _{GS} =0V			1	μA
Zero Gate Voltage Drain Current(Tc=125℃)	I _{DSS}	V _{DS} =650V, V _{GS} =0V			100	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V			±200	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	3	3.5	4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =5.5A		300	330	mΩ
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =50V, V _{GS} =0V, F=1.0MHz		847		pF
Output Capacitance	C _{oss}			31		pF
Reverse Transfer Capacitance	C _{rss}			4		pF
Total Gate Charge	Q _g	V _{DS} =480V, I _D =5.5A, V _{GS} =10V		17		nC
Gate-Source Charge	Q _{gs}			4.4		nC
Gate-Drain Charge	Q _{gd}			4.9		nC
Gate plateau voltage	V _{gp}			5.4		V
Intrinsic gate resistance	R _G	f = 1 MHz open drain		18		Ω
Switching times						
Turn-on Delay Time	t _{d(on)}	V _{DD} =480V, I _D =5.5A, R _G =1.7Ω, V _{GS} =10V		10		nS
Turn-on Rise Time	t _r			7		nS
Turn-Off Delay Time	t _{d(off)}			55		nS
Turn-Off Fall Time	t _f			8		nS
Source- Drain Diode Characteristics						
Source-drain current(Body Diode)	I _{SD}	T _c =25℃			11	A
Pulsed Source-drain current(Body Diode)	I _{SDM}				44	A
Forward On Voltage	V _{SD}	T _j =25℃, I _{SD} =11A, V _{GS} =0V		0.9	1.2	V
Reverse Recovery Time	t _{rr}	T _j =25℃, I _F =5.5A, di/dt=100A/μs		200		nS
Reverse Recovery Charge	Q _{rr}			1.6		uC
Peak Reverse Recovery Current	I _{rrm}			16		A

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature

2. $T_J=25^{\circ}\text{C}, V_{DD}=50V, V_G=10V, R_G=25\Omega$

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)

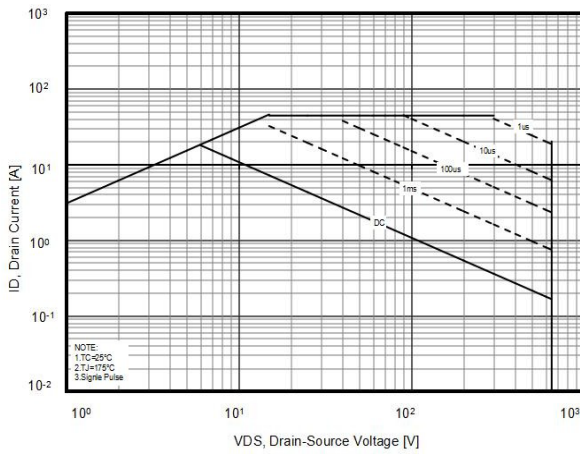
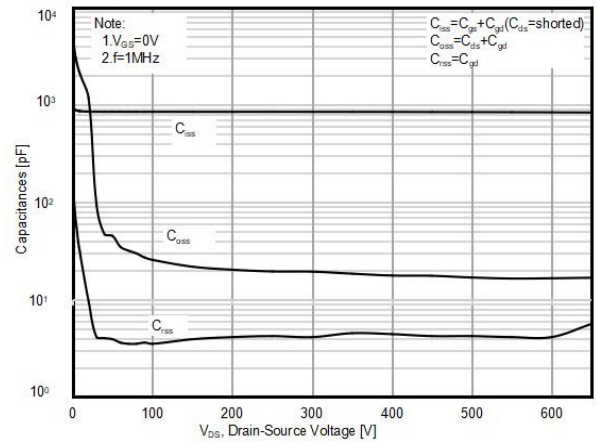
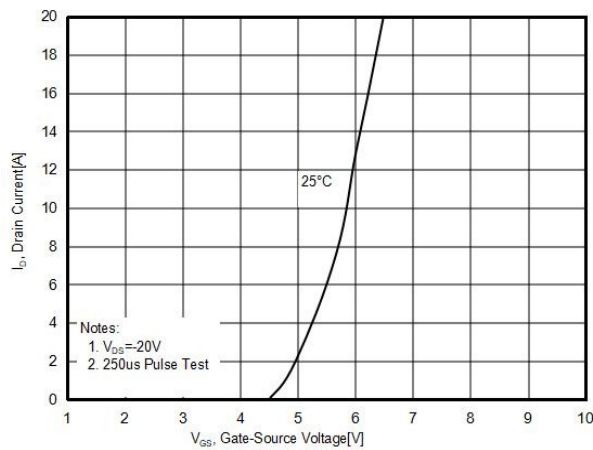
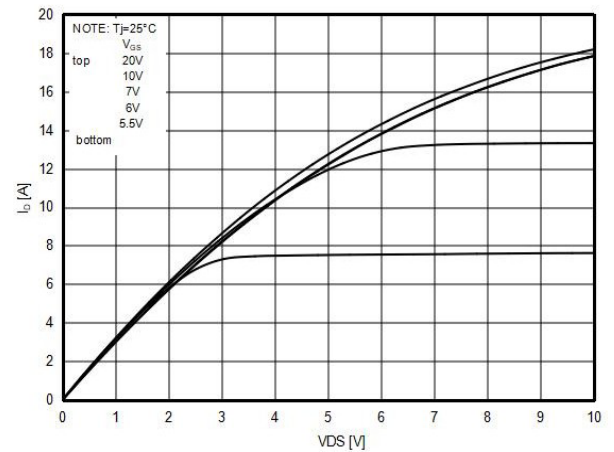
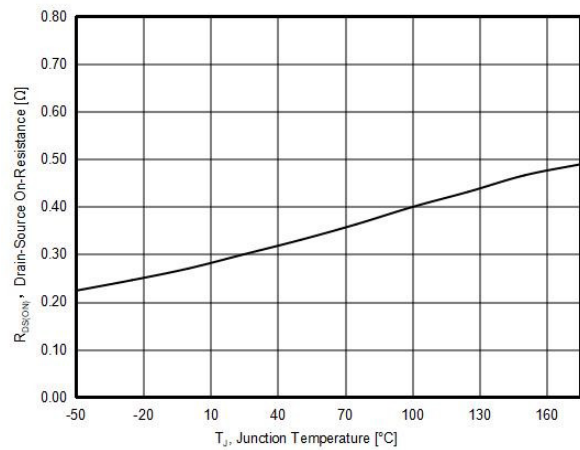
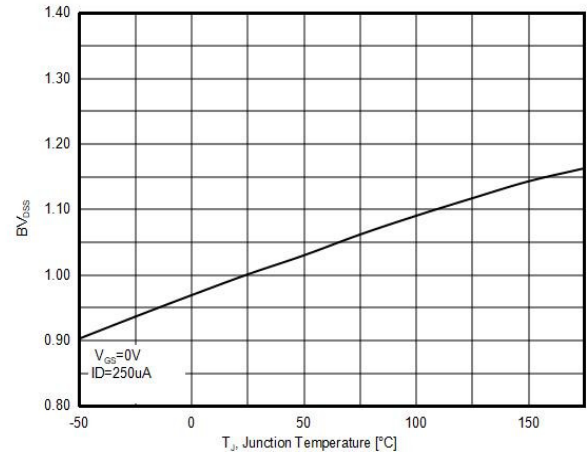
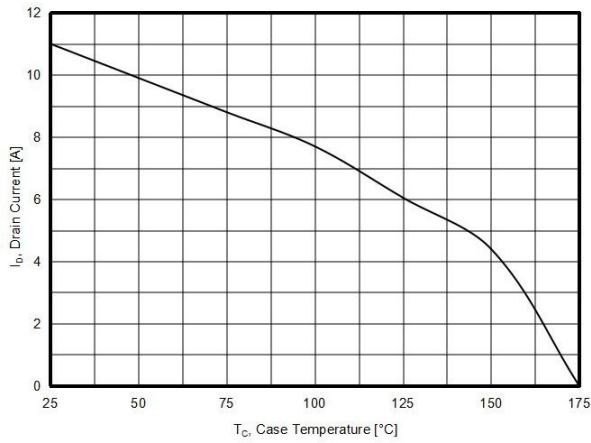
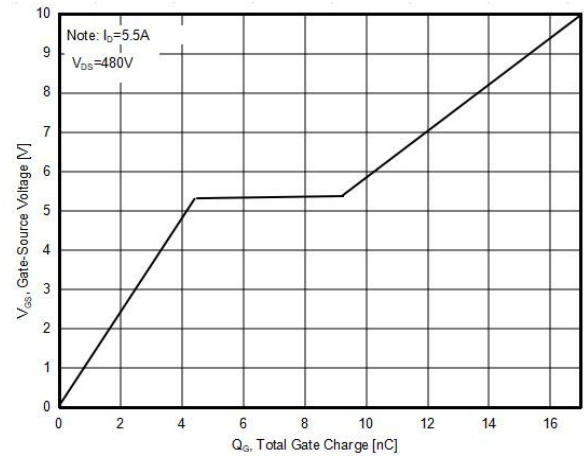
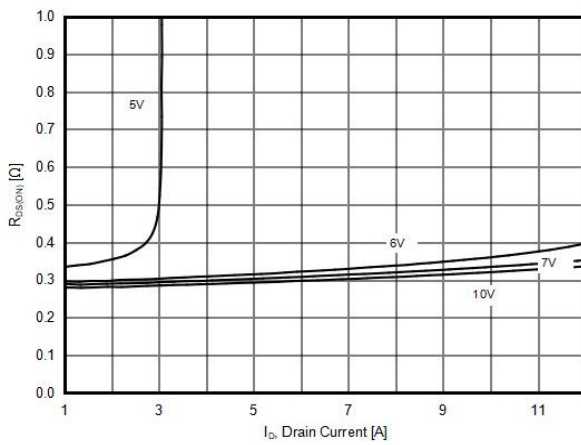
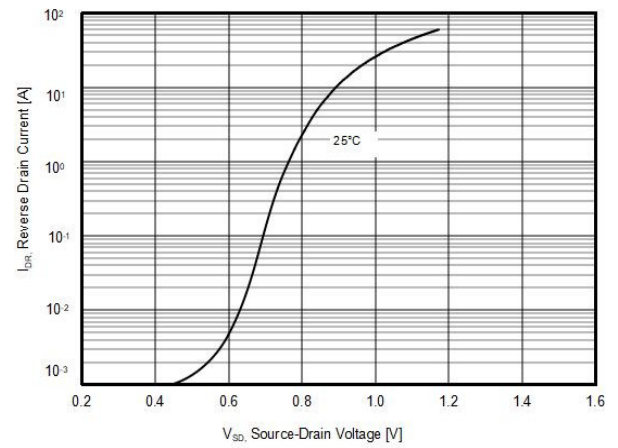
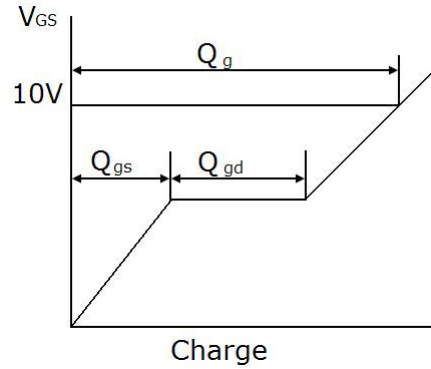
Figure1. Safe operating area

Figure2. Capacitance

Figure3. Transfer characteristics

Figure4. Output characteristics

Figure5. $R_{DS(ON)}$ vs Junction Temperature

Figure6. BV_{DSS} vs Junction Temperature


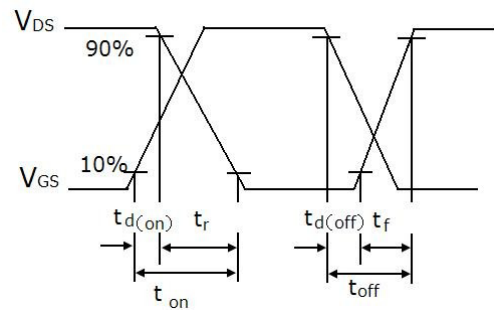
Figure7. Maximum I_D vs Junction Temperature

Figure8. Gate charge waveforms

Figure9. Static drain-source on resistance

Figure10. Source-Drain Diode Forward Voltag


Test circuit

1) Gate charge test circuit & Waveform



2) Switch Time Test Circuit:



3) Unclamped Inductive Switching Test Circuit & Waveforms

