

General Description

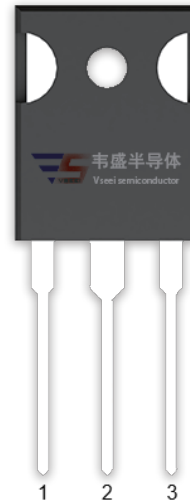
The series of devices use advanced trench gate super junction technology and design to provide ultra-low $R_{DS(ON)}$ and low gate charge and With a rapid recovery body diode. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, industrial power applications, Fast charger, new energy vehicle charging pile, on-board OBC etc.

Features

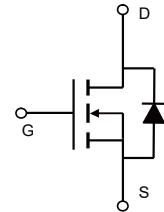
- New technology for high voltage device
- Ultra low on-resistance and ultra low conduction losses
- Ultra Low Gate Charge cause lower driving requirements
- Diode reverse recovery speed is super fast
- 100% Avalanche Tested and 100% T_{rr} Tested
- High reliability
- RoHS & Halogen Free

Application

- Power factor correction (PFC)
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply (UPS)
- On-board charger(OBC)



TO-247



Schematic

Parameter	Symbol	Value	Unit
Drain-Source Voltage ($V_{GS}=0V$)	V_{DS}	600	V
Gate-Source Voltage ($V_{DS}=0V$), AC ($f>1$ Hz)	V_{GS}	± 30	V
Gate-Source Voltage ($V_{DS}=0V$), DC	V_{GS}	± 20	V
Continuous Drain Current at $T_c=25^\circ C$	$I_{D(DC)}$	81	A
Continuous Drain Current at $T_c=100^\circ C$	$I_{D(DC)}$	56.7	A
Pulsed drain current (Note 1)	$I_{DM(pluse)}$	243	A
Maximum Power Dissipation ($T_c=25^\circ C$)	P_D	490	W
Derate above $25^\circ C$		3.2	W/ $^\circ C$
Single pulse avalanche energy (Note 2)	EAS	1024	mJ
Single pulse avalanche current (Note 2)	I_{AS}	16	A
Repetitive Avalanche energy, t_{AR} limited by T_{Jmax} (Note 1)	E_{AR}	0.9	mJ

Reverse diode dv/dt, $V_{DS} \leq 480V, I_{SD} < I_D$	dv/dt	50	V/ns
Drain Source voltage slope, $V_{DS} \leq 480V$	dv/dt	50	V/ns
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55...+175	°C

Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Maximum)	R_{thJC}	0.31	°C /W
Thermal Resistance, Junction-to-Ambient (Maximum)	R_{thJA}	62	°C /W

Table 3. Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
On/off states						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =1mA	600			V
Zero Gate Voltage Drain Current(Tc=25℃)	I _{DSS}	V _{DS} =600V, V _{GS} =0V			10	μA
Zero Gate Voltage Drain Current(Tc=125℃)	I _{DSS}	V _{DS} =600V, V _{GS} =0V			600	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V			±200	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =3mA	3.5	4.2	5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =40A		23	31	mΩ
Dynamic Characteristics						
Gate Resistance	R _g	F=1MHZ, D-S short		3.5		Ω
Input Capacitance	C _{iss}	V _{DS} =50V, V _{GS} =0V, F=1MHz		8000		pF
Output Capacitance	C _{oss}			249		pF
Reverse Transfer Capacitance	C _{rss}			15		pF
Total Gate Charge	Q _g	V _{DS} =400V, I _D =40A, V _{GS} =10V		135	145	nC
Gate-Source Charge	Q _{gs}			54		nC
Gate-Drain Charge	Q _{gd}			46		nC
Gate plateau voltage	V _{gp}			7		V
Switching times						
Turn-on Delay Time	t _{d(on)}	V _{DD} =380V, I _D =40A, R _G =4Ω, V _{GS} =10V		55		nS
Turn-on Rise Time	t _r			16		nS
Turn-Off Delay Time	t _{d(off)}			125		nS
Turn-Off Fall Time	t _f			11		nS
Source- Drain Diode Characteristics						
Source-drain current(Body Diode)	I _{SD}	T _C =25℃			81	A
Pulsed-Source-drain current(Body Diode)	I _{SDM}				243	A
Forward on voltage	V _{SD}	T _j =25℃, I _{SD} =81A, V _{GS} =0V		1.0	1.2	V
Reverse Recovery Time	t _{rr}	T _j =25℃, I _f =40A, di/dt=100A/μs		170		nS
Reverse Recovery Charge	Q _{rr}			1.3		uC
Peak reverse recovery current	I _{rm}			15.5		A

Notes: 1. Repetitive Rating: Pulse width limited by maximum junction temperature

2. $T_J=25^\circ C, V_{DD}=50V, V_G=10V, R_G=25\Omega$

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)

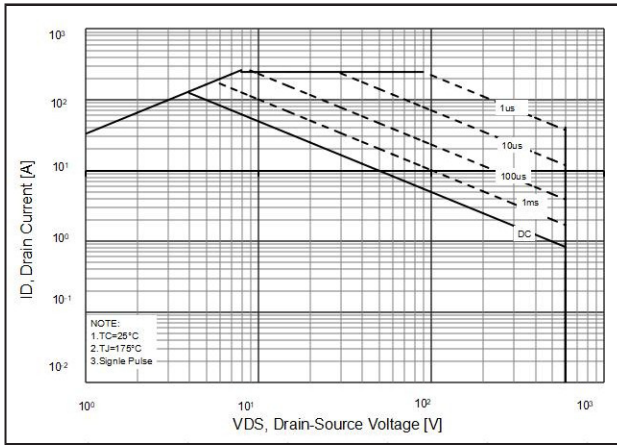
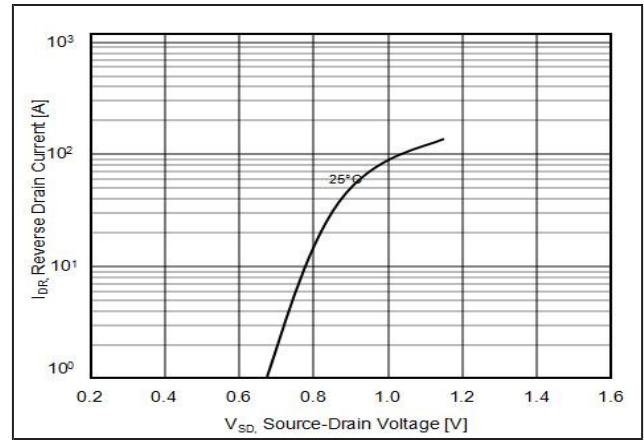
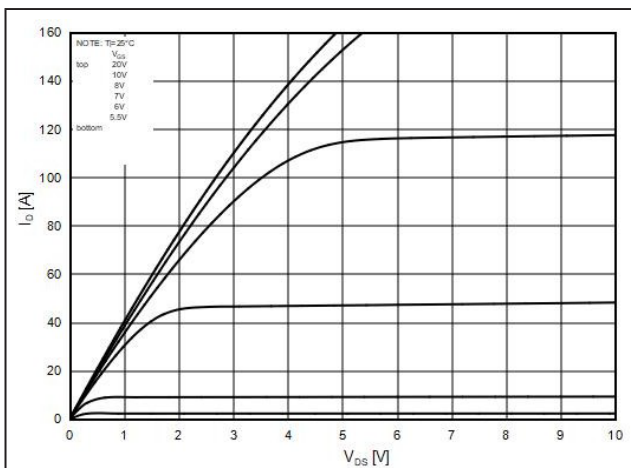
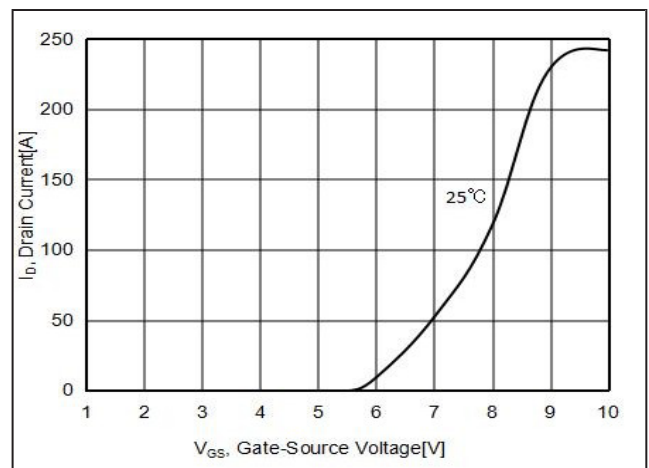
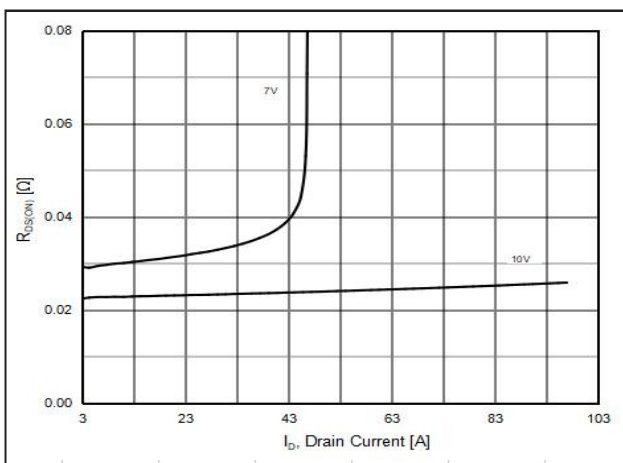
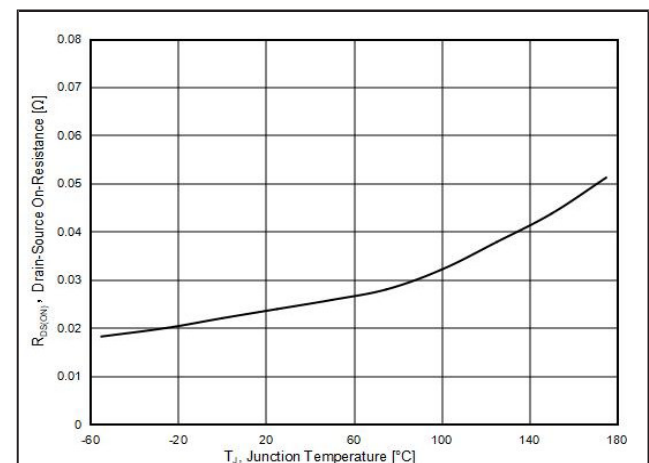
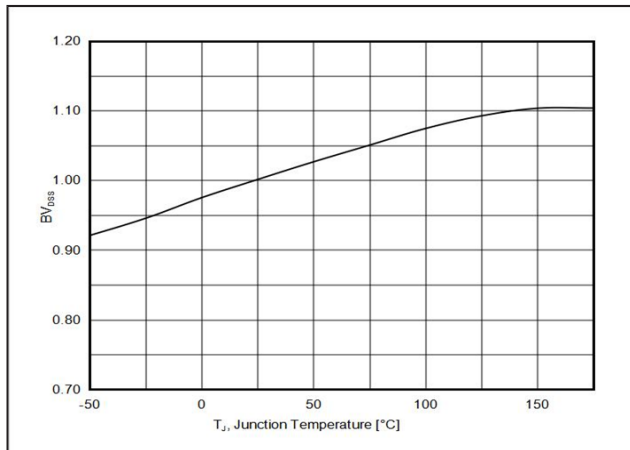
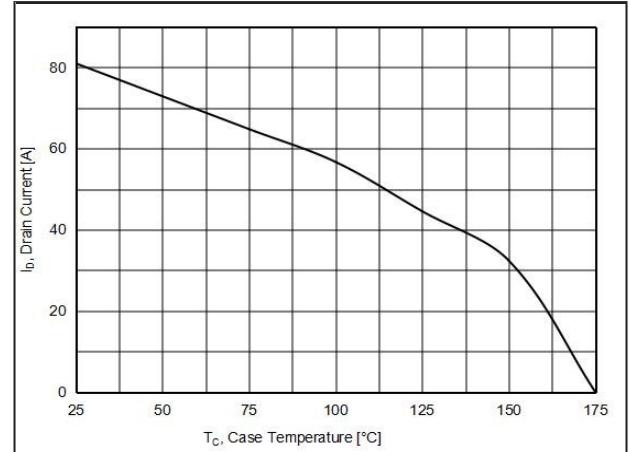
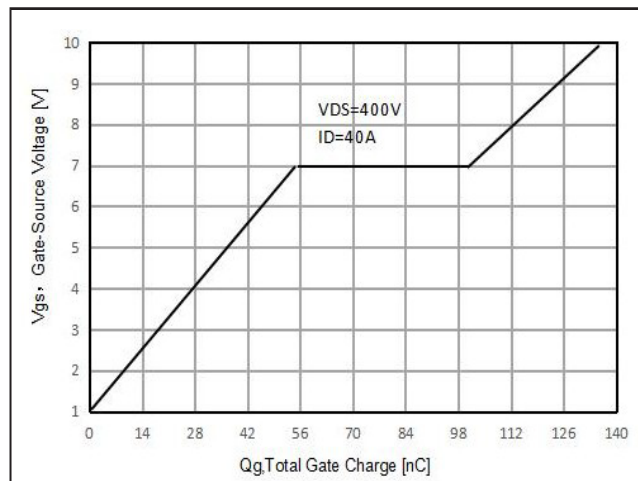
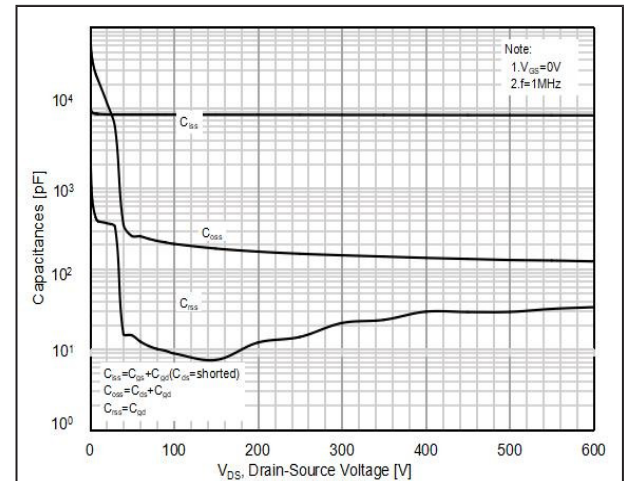
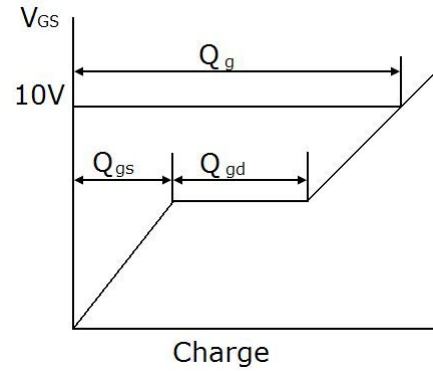
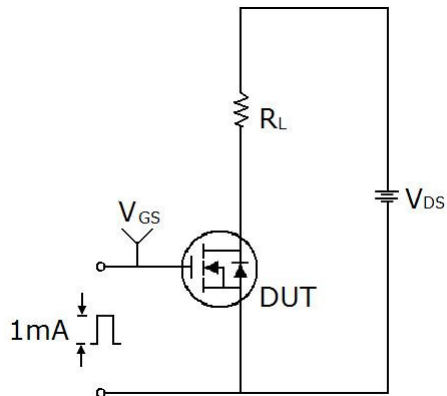
Figure1. Safe operating area

Figure2. Source-Drain Diode Forward Voltage

Figure3. Output characteristics

Figure4. Transfer characteristics

Figure5. Static drain-source on resistance

Figure6. RDS(ON) vs Junction Temperature


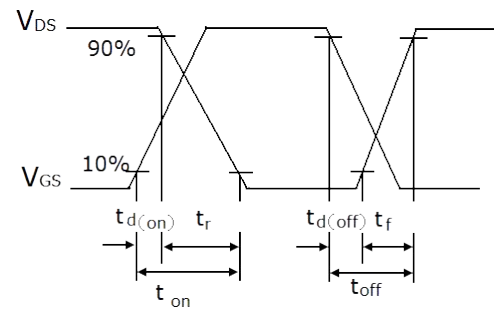
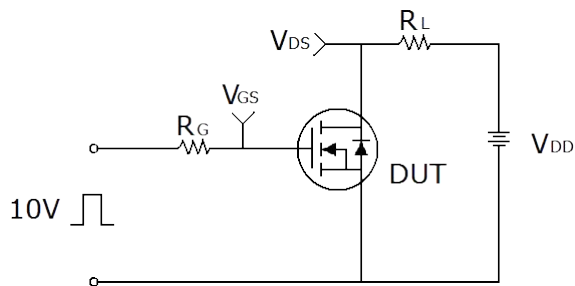
Figure7. BV_{DSS} vs Junction Temperature

Figure8. Maximum I_D vs Junction Temperature

Figure9. Gate charge waveforms

Figure10. Capacitance


Test circuit

1) Gate charge test circuit & Waveform



2) Switch Time Test Circuit:



3) Unclamped Inductive Switching Test Circuit & Waveforms

