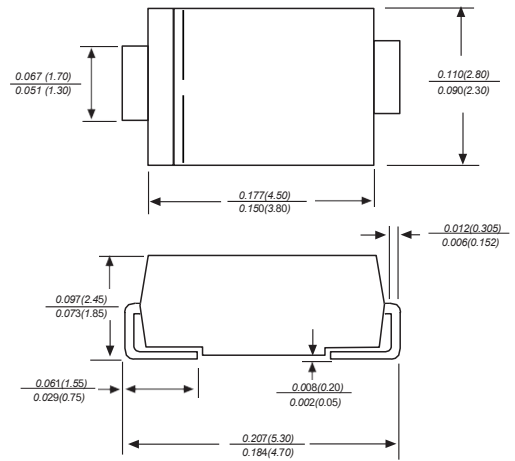


SURFACE MOUNT SCHOTTKY BARRIER DIODES

Features

- ◆ The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- ◆ For surface mounted applications
- ◆ Metal silicon junction, majority carrier conduction
- ◆ Low power loss, high efficiency
- ◆ Built-in strain relief, ideal for automated placement
- ◆ High forward surge current capability
- ◆ High temperature soldering guaranteed:
260°C/10 seconds at terminals

DO-214AC/SMA



Dimensions in inches and (millimeters)

Maximum Ratings And Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.

Parameter	SYMBOLS	SL32	SL33	SL34	SL35	SL36	SL38	SL310	UNITS
Marking Code									
Maximum repetitive peak reverse voltage	V _{RRM}	20	30	40	50	60	80	100	V
Maximum RMS voltage	V _{RMS}	14	21	28	35	42	56	70	V
Maximum DC blocking voltage	V _{DC}	20	30	40	50	60	80	100	V
Maximum average forward rectified current at TL(see fig.1)	I _(AV)	3.0							A
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	70							A
Maximum instantaneous forward voltage at 3.0A	V _F	0.45			0.50		0.70		V
Maximum DC reverse current T _A =25℃ at rated DC blocking voltage T _A =125℃	I _R	0.5 20.0					0.2 10.0		mA
Typical junction capacitance (NOTE 1)	C _J	500			300				pF
Typical thermal resistance (NOTE 2)	R _{θJA}	75.0							℃/W
Operating junction temperature range	T _J	-55 to+125					-55 to +150		℃
Operating storage temperature range	T _{STG}	-55 to +150					℃		

Note: 1. Measured at 1MHz and applied reverse voltage of 4.0V D.C.
2. P.C.B. mounted with 2.0x2.0" (5.0x5.0cm) copper pad areas.

Ratings And Characteristic Curves

FIG. 1- FORWARD CURRENT DERATING CURVE

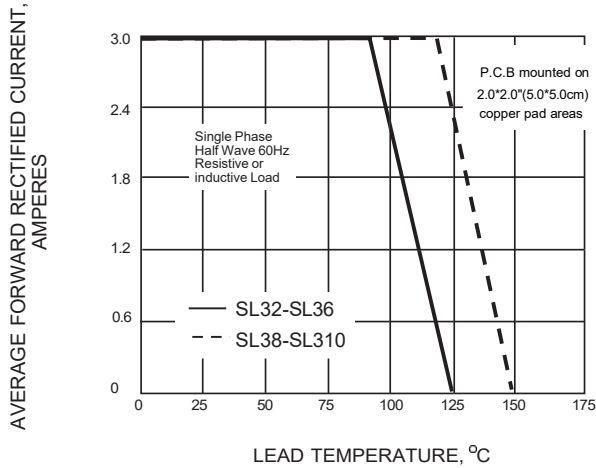


FIG. 2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

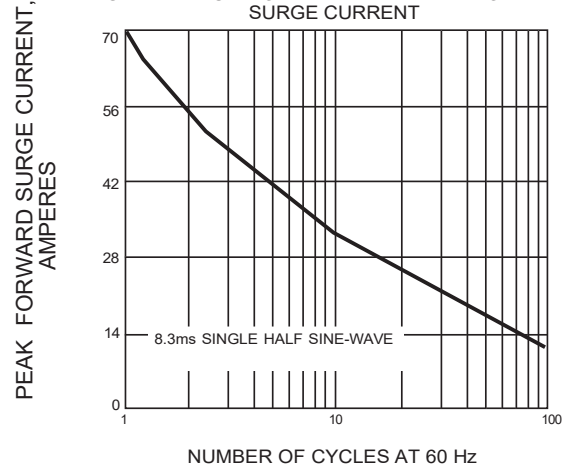


FIG. 3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

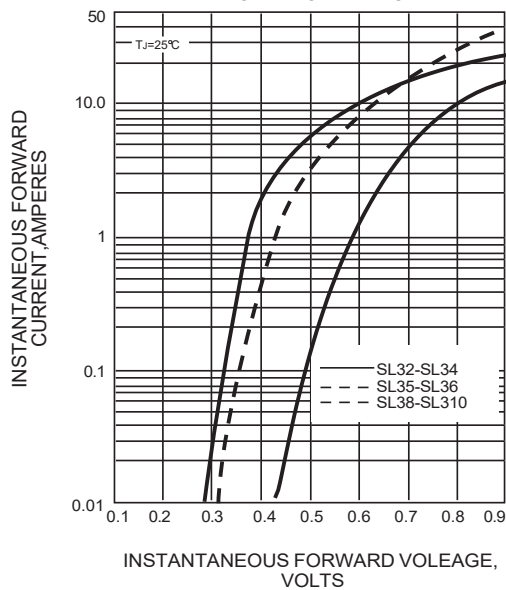


FIG. 4-TYPICAL REVERSE CHARACTERISTICS

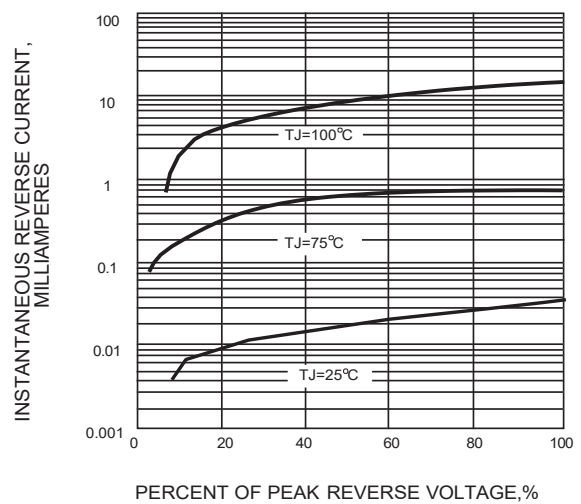


FIG. 5-TYPICAL JUNCTION CAPACITANCE

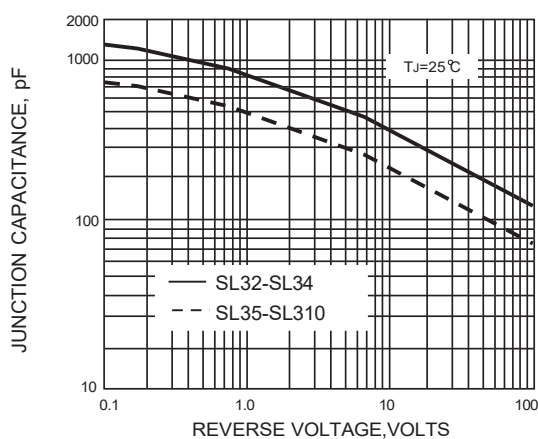
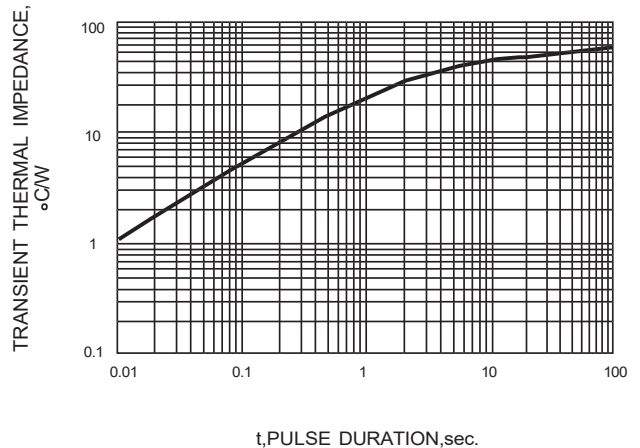


FIG. 6-TYPICAL TRANSIENT THERMAL IMPEDANCE



The curve above is for reference only.