

DESCRIPTION:

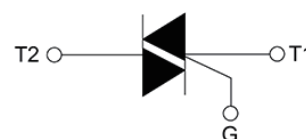
The chip triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers.



TO-252

MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	4	A
V_{DRM}/V_{RRM}	600	V
$I_{GT\ I/II/III/IV}$	5/5/5/10	mA



ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value	Unit
Storage junction temperature range		T _{stg}	-40-150	℃
Operating junction temperature range		T _j	-40-125	℃
Repetitive peak off-state voltage (T _j =25℃)		V _{DRM}	600	V
Repetitive peak reverse voltage (T _j =25℃)		V _{RRM}	600	V
RMS on-state current (T _C ≤83℃)		I _{T(RMS)}	4	A
Non repetitive surge peak on-state current (full cycle , t _p =20ms , T _j =25℃)		I _{TSM}	35	A
Non repetitive surge peak on-state current (full cycle , t _p =16.6ms , T _j =25℃)			38.5	
I ² t value for fusing (t _p =10ms , T _j =25℃)		I ² t	6.1	A ² s
Critical rate of rise of on-state current (I _G =2×I _{GT} , f=100Hz , T _j =125℃)	I - II -III	di/dt	50	A/μs
	IV		40	
Peak gate current (t _p =20μs , T _j =125℃)		I _{GM}	2	A
Average gate power dissipation (T _j =125℃)		P _{G(AV)}	0.5	W
Peak gate power		P _{GM}	5	W
Peak pulse voltage (T _j =25℃; non-repetitive,off-state;FIG)		V _{pp}	3.5	kV

ELECTRICAL CHARACTERISTICS ($T_j=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12\text{V}$ $R_L=33\Omega$	I - II -III	MAX.	5	mA
		IV		10	
V_{GT}		ALL	MAX.	1	V
V_{GD}	$V_D=V_{DRM}$ $T_j=125^{\circ}\text{C}$ $R_L=3.3\text{k}\Omega$	ALL	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	I -III	MAX.	15	mA
		II -IV		25	
I_H	$I_T=100\text{mA}$		MAX.	15	mA
dV/dt	$V_D=400\text{V}$ Gate Open $T_j=110^{\circ}\text{C}$		MIN.	120	$\text{V}/\mu\text{s}$
$(dV/dt)_c$	$(dI/dt)_c=1.8\text{A/ms}$, $T_j=110^{\circ}\text{C}$		MIN.	2.5	$\text{V}/\mu\text{s}$
t_{on}	$I_G=20\text{mA}$ $I_A=200\text{mA}$ $I_R=20\text{mA}$		TYP.	1	μs
t_{off}	$T_j=25^{\circ}\text{C}$			12	

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_{TM}=5\text{A}$ $t_p=380\mu\text{s}$	$T_j=25^{\circ}\text{C}$	1.7	V
V_{TO}	Threshold voltage	$T_j=125^{\circ}\text{C}$	0.94	V
R_D	Dynamic resistance	$T_j=125^{\circ}\text{C}$	124	$\text{m}\Omega$
I_{DRM}	$V_D=V_{DRM}$ $V_R=V_{RRM}$	$T_j=25^{\circ}\text{C}$	5	μA
I_{RRM}		$T_j=125^{\circ}\text{C}$	0.3	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	6.5	$^{\circ}\text{C}/\text{W}$
$R_{th(j-a)}$	junction to ambient (AC)	150	$^{\circ}\text{C}/\text{W}$

FIG : Maximum power dissipation versus RMS on-state current

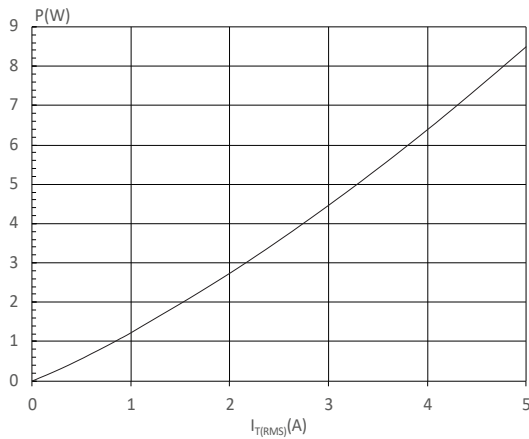


FIG : RMS on-state current versus ambient temperature (printed circuit board FR4, copper thickness: 35 μ m) (full cycle)

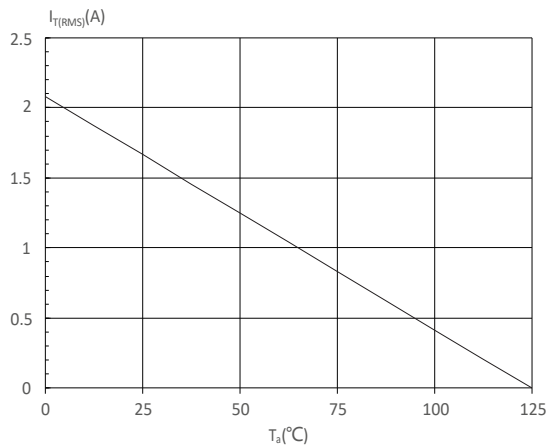


FIG : On-state characteristics

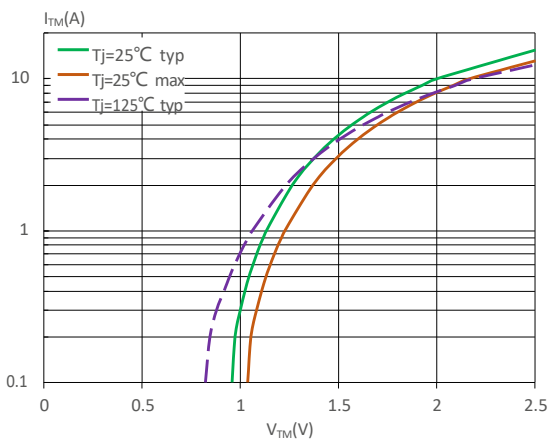


FIG : RMS on-state current versus case temperature

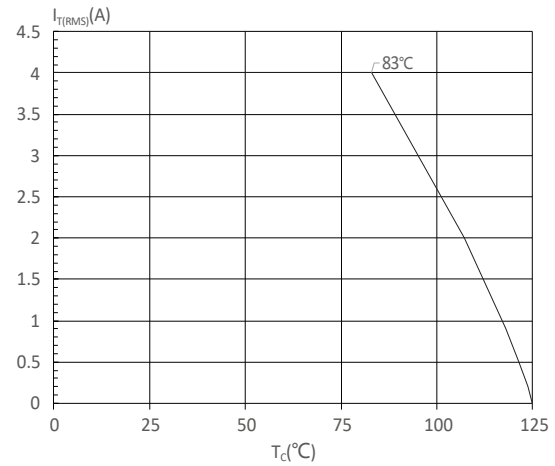


FIG : Surge peak on-state current versus number of cycles

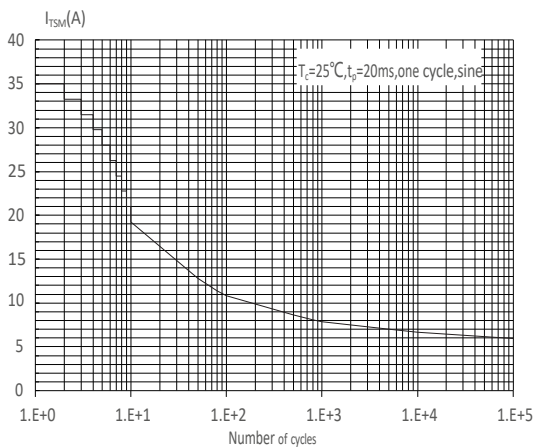


FIG : Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20ms$, and corresponding value of I^2t (I - II - III: $di/dt < 50A/\mu s$; IV: $di/dt < 40A/\mu s$)

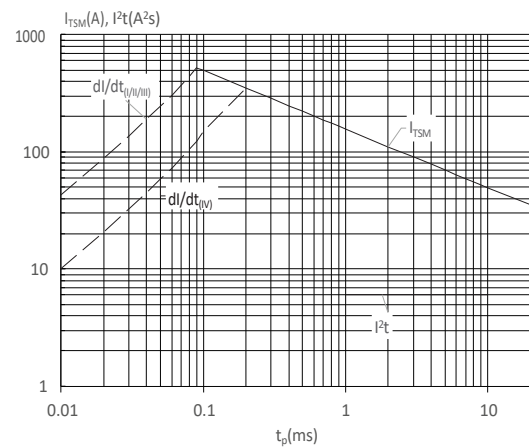


FIG : Relative variations of gate trigger current, holding current and latching current versus junction temperature

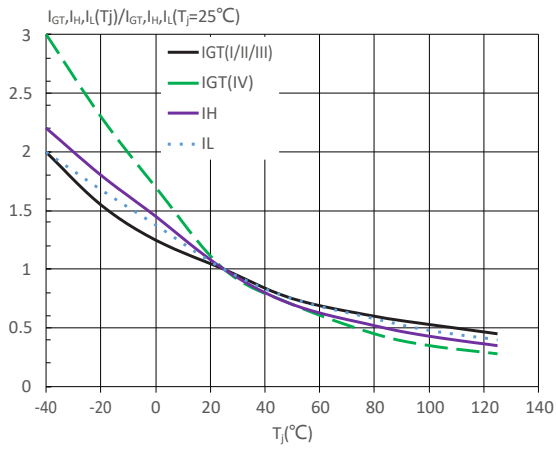
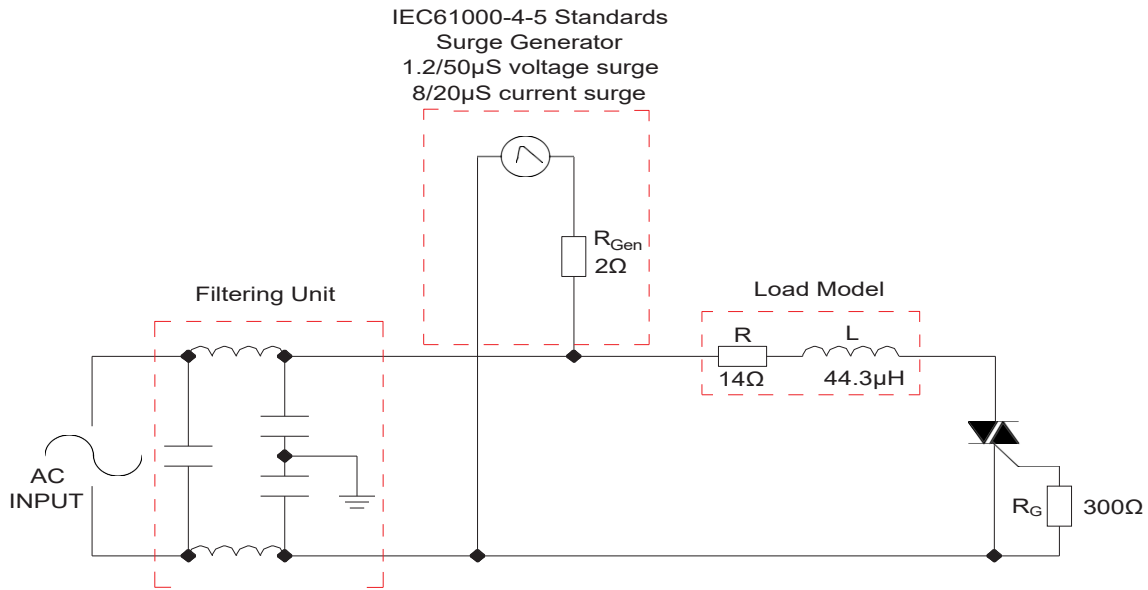


FIG : Test circuit for inductive and resistive loads to IEC-61000-4-5 standards


SOLDERING PARAMETERS

Reflow Condition		Pb-Free assembly (see figure at right)
Pre Heat	-Temperature Min (T _{s(min)})	+150°C
	-Temperature Max(T _{s(max)})	+200°C
	-Time (Min to Max) (t _s)	60-180 secs.
Average ramp up rate (Liquidus Temp (T _L) to peak)		3°C/sec. Max
T _{s(max)} to T _L - Ramp-up Rate		3°C/sec. Max
Reflow	-Temperature(T _L) (Liquidus)	+217°C
	-Temperature(t _L)	60-150 secs.
Peak Temp (T _p)		+260(+0/-5)°C
Time within 5°C of actual Peak Temp (t _p)		20-40secs.
Ramp-down Rate		6°C/sec. Max
Time 25°C to Peak Temp (T _p)		8 min. Max
Do not exceed		+260°C

