

Features

- Extremely Low Gate Charge
- Excellent Output Capacitance (C_{oss}) Profile
- Fast Switching Capability
- 100% UIS Tested, 100% Rg Tested
- Pb-free Lead Plating
- Halogen-free and RoHS-compliant

Applications

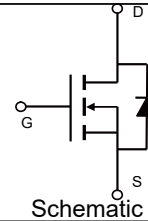
- Telecom / Server Power Supplies
- Industrial Power Supplies
- UPS / Solar
- Lighting / Charger / Adapter

Product Summary

Parameter	Value	Unit
V_{DS}	700	V
$V_{GS(th)}$ _Typ	3.5	V
I_D (@ $V_{GS} = 10V$) ⁽¹⁾	12	A
$R_{DS(ON)}$ _Typ (@ $V_{GS} = 10V$)	352	m Ω
$E_{oss@400V}$	3.6	μJ



TO-252



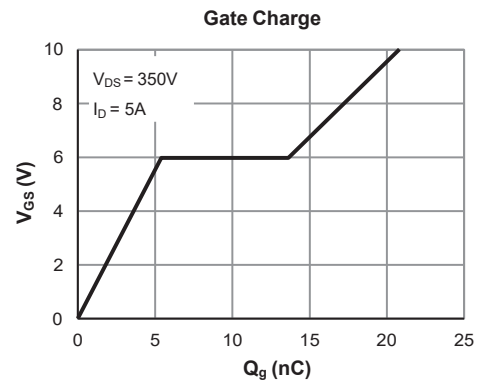
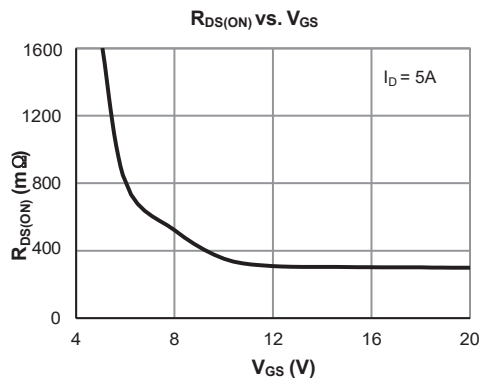
Schematic

Ordering Information

Device	Package	# of Pins	Marking	MSL	T_J (°C)	Media	Quantity (pcs)
VSM12N70-T2	TO-252	3	VSM12N70	1	-55 to 150	13-inch Reel	2500

Absolute Maximum Ratings (@ $T_A = 25^\circ C$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DS}	700	V
Gate-to-Source Voltage	V_{GS}	± 30	V
Continuous Drain Current ⁽¹⁾	I_D	$T_C = 25^\circ C$	A
		$T_C = 100^\circ C$	
Pulsed Drain Current ⁽²⁾	I_{DM}	38	A
Avalanche Energy ⁽³⁾	E_{AS}	180	mJ
Power Dissipation ⁽⁴⁾	P_D	$T_C = 25^\circ C$	W
		$T_C = 100^\circ C$	
Junction & Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ C$



Electrical Characteristics (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
STATIC PARAMETERS						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = 250\ \mu\text{A}$, $V_{GS} = 0\text{V}$	700			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 560\text{V}$, $V_{GS} = 0\text{V}$			1.0	μA
Gate-Body Leakage Current	I_{GSS}	$V_{DS} = 0\text{V}$, $V_{GS} = \pm 30\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250\ \mu\text{A}$	2.5	3.5	4.5	V
Static Drain-Source ON-Resistance	$R_{DS(on)}$	$V_{GS} = 10\text{V}$, $I_D = 5.5\text{A}$		352	422.0	m Ω
Diode Forward Voltage	V_{SD}	$I_S = 1\text{A}$, $V_{GS} = 0\text{V}$		0.70	1.0	V
Diode Continuous Current	I_S	$T_C = 25^\circ\text{C}$			12	A

DYNAMIC PARAMETERS ⁽⁵⁾

Input Capacitance	C_{iss}	$V_{GS} = 0\text{V}$, $V_{DS} = 350\text{V}$, $f = 1\text{MHz}$		810		pF
Output Capacitance	C_{oss}			27		pF
Reverse Transfer Capacitance	C_{rss}			5.1		pF
Effective output capacitance, energy related	$C_{o(er)}$	$V_{GS} = 0\text{V}$, $V_{DS} = 0$ to 400V		45		pF
Effective output capacitance, time related	$C_{o(tr)}$			190		pF
Gate Resistance	R_g	$V_{GS} = 0\text{V}$, $V_{DS} = 0\text{V}$, $f = 1\text{MHz}$		8.8		Ω

SWITCHING PARAMETERS ⁽⁵⁾

Total Gate Charge (@ $V_{GS} = 10\text{V}$)	Q_g	$V_{GS} = 0$ to 10V $V_{DS} = 700\text{V}$, $I_D = 5\text{A}$		21		nC
Total Gate Charge (@ $V_{GS} = 6.0\text{V}$)	Q_g			13.8		nC
Gate Source Charge	Q_{gs}			5.4		nC
Gate Drain Charge	Q_{gd}			8.2		nC
Turn-On DelayTime	$t_{D(on)}$	$V_{GS} = 10\text{V}$, $V_{DS} = 350\text{V}$ $R_L = 75\ \Omega$, $R_{GEN} = 6.0\ \Omega$		13.4		ns
Turn-On Rise Time	t_r			19.4		ns
Turn-Off DelayTime	$t_{D(off)}$			46		ns
Turn-Off Fall Time	t_f			18.6		ns
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 9\text{A}$, $dI_F/dt = 100\text{A}/\mu\text{s}$		322		ns
Body Diode Reverse Recovery Charge	Q_{rr}	$I_F = 9\text{A}$, $dI_F/dt = 100\text{A}/\mu\text{s}$		4560		nC
Peak Diode Recovery Voltage Slope	dv/dt	$I_F \leq 10\text{A}$, $dI/dt = 200\text{A}/\mu\text{s}$, $V_{DS} = 400\text{V}$		15		V/ns
MOSFET dv/dt Ruggedness	dv/dt	$V_{DS} = 0 \dots 400\text{V}$		50		V/ns

Thermal Performance

Parameter	Symbol	Typ.	Max.	Unit
Thermal Resistance, Junction-to-Ambient	R_{JA}	28	32	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Case	R_{JC}	0.69	0.79	$^\circ\text{C}/\text{W}$

Notes:

1. Computed continuous current assumes the condition of T_{J_Max} while the actual continuous current depends on the thermal & electro-mechanical application board design.
2. This single-pulse measurement was taken under $T_{J_Max} = 150^\circ\text{C}$.
3. E_{AS} of 180 mJ is based on starting $T_J = 25^\circ\text{C}$, $L = 10\text{mH}$, $I_{AS} = 6.3\text{A}$, $V_{GS} = 10\text{V}$, $V_{DD} = 350\text{V}$; 100% test at $L = 10\text{mH}$, $I_{AS} = 6.3\text{A}$.
 $T_{J_Max} = 150^\circ\text{C}$.
4. The power dissipation P_D is based on $T_{J_Max} = 150^\circ\text{C}$.
5. This value is guaranteed by design hence it is not included in the production test.

Typical Electrical & Thermal Characteristics

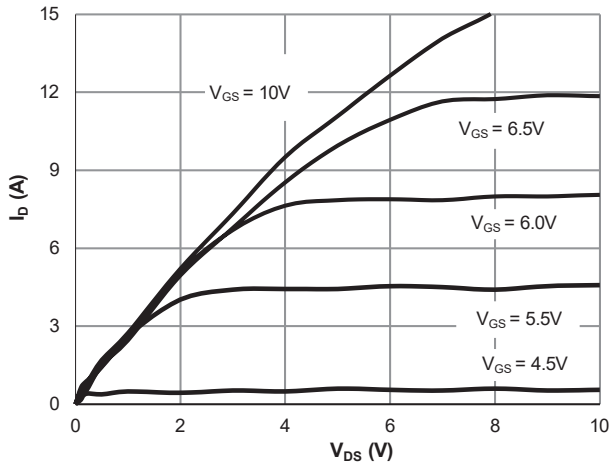


Figure 1: Saturation Characteristics

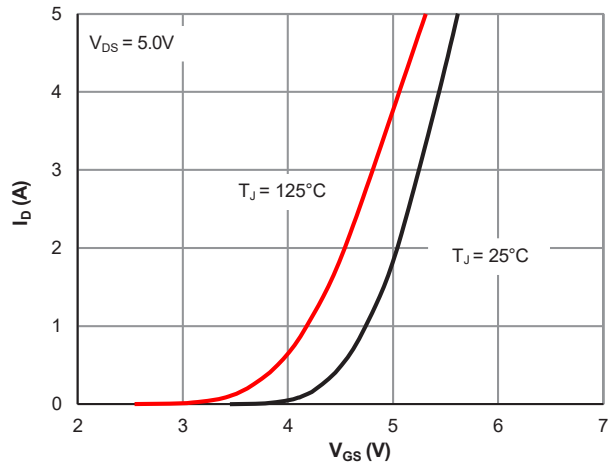


Figure 2: Transfer Characteristics

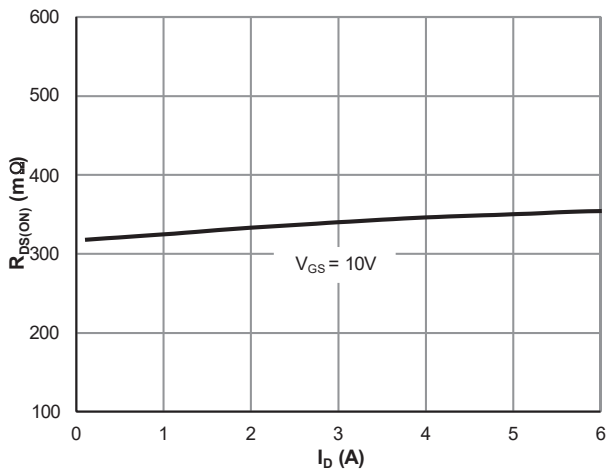


Figure 3: $R_{DS(on)}$ vs. Drain Current

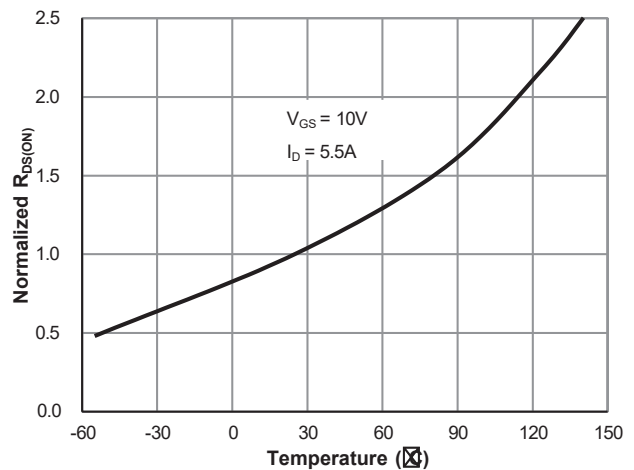


Figure 4: $R_{DS(on)}$ vs. Junction Temperature

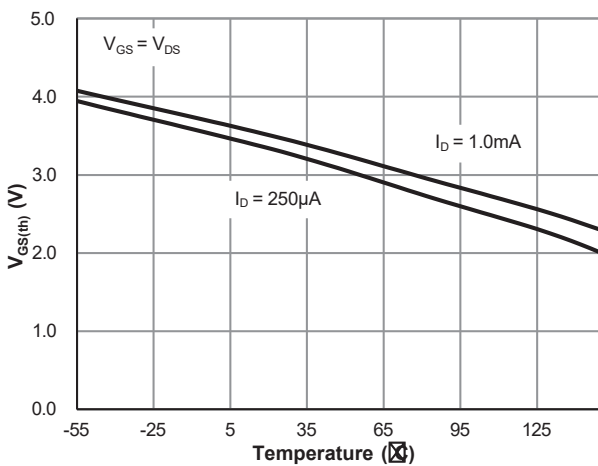


Figure 5: $V_{GS(th)}$ vs. Junction Temperature

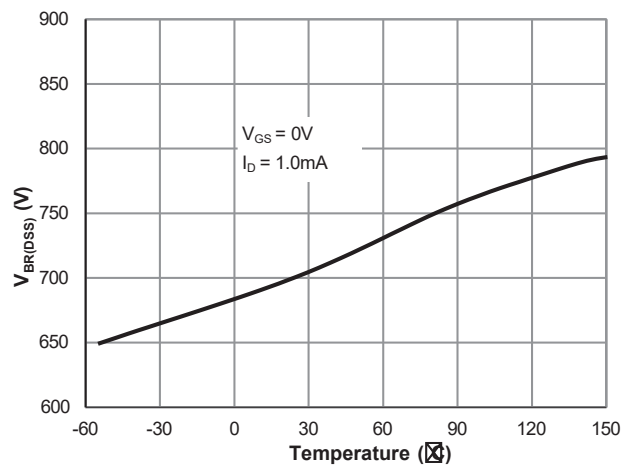


Figure 6: $V_{BR(dss)}$ vs. Junction Temperature

Typical Electrical & Thermal Characteristics

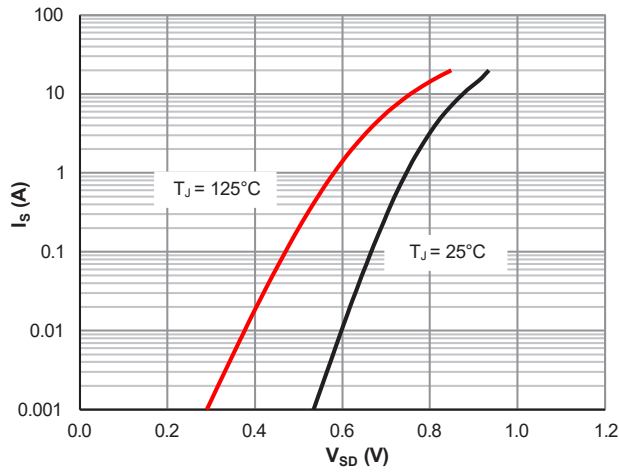


Figure 7: Body-Diode Characteristics

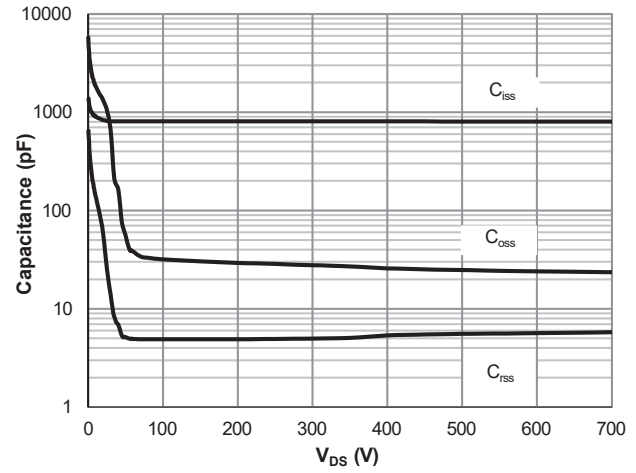


Figure 8: Capacitance Characteristics

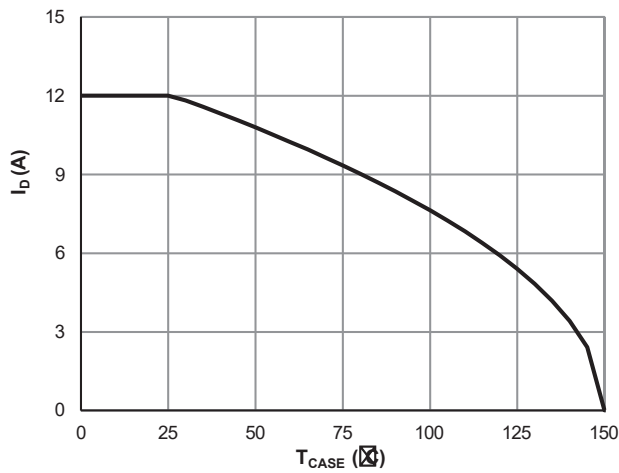


Figure 9: Current De-rating

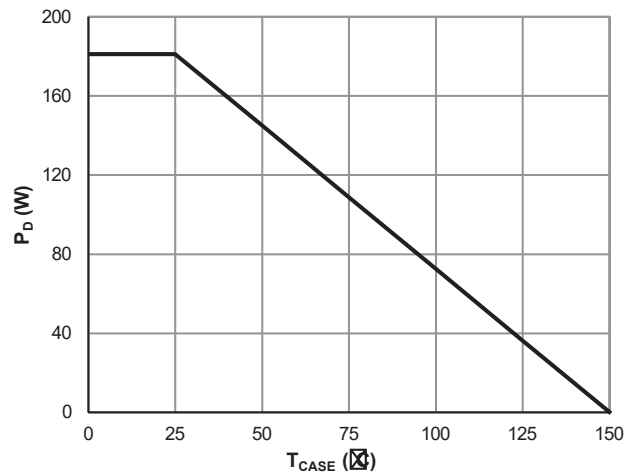


Figure 10: Power De-rating

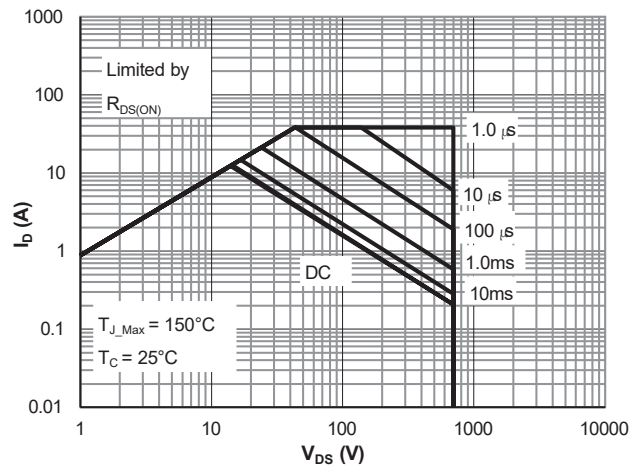


Figure 11: Maximum Safe Operating Area

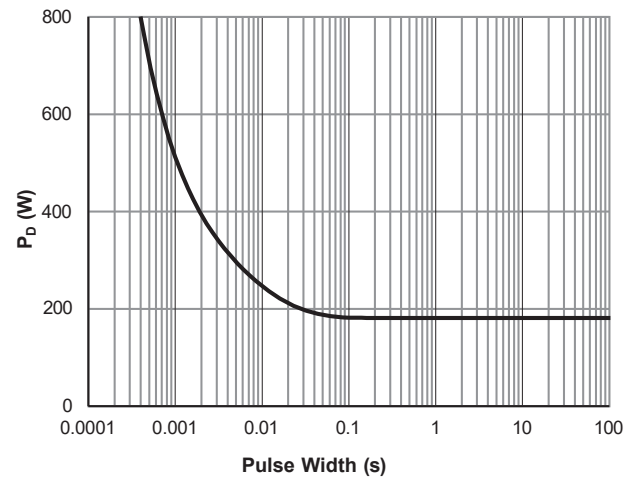


Figure 12: Single Pulse Power Rating, Junction-to-Case

Typical Electrical & Thermal Characteristics

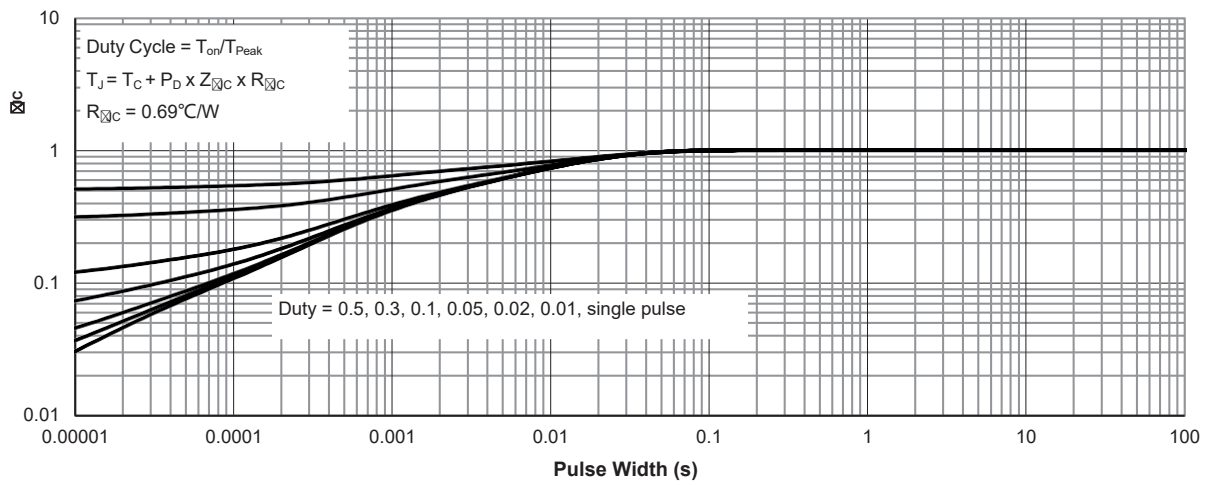


Figure 13: Normalized Maximum Transient Thermal Impedance