

## Features

- Ultra-low ON-resistance,  $R_{DS(ON)}$
- Low Gate Charge,  $Q_g$
- 100% UIS and  $R_g$  Tested
- Pb-free Lead Plating
- Halogen-free and RoHS-compliant

## Applications

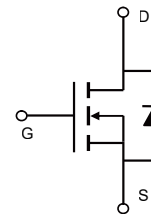
- Power Management in Computing, CE, IE 4.0, Communications
- Current Switching in DC/DC & AC/DC (SR) Sub-systems
- Load Switching, e-Fuse Switching, Motor Driving

## Product Summary

| Parameter                                | Value | Unit       |
|------------------------------------------|-------|------------|
| $V_{DS}$                                 | 60    | V          |
| $V_{GS(th)}_{Typ}$                       | 1.6   | V          |
| $I_D$ (@ $V_{GS} = 10V$ ) <sup>(1)</sup> | 142   | A          |
| $R_{DS(ON)}_{Typ}$ (@ $V_{GS} = 10V$ )   | 2.4   | m $\Omega$ |
| $R_{DS(ON)}_{Typ}$ (@ $V_{GS} = 4.5V$ )  | 3.4   | m $\Omega$ |



DFN5x6



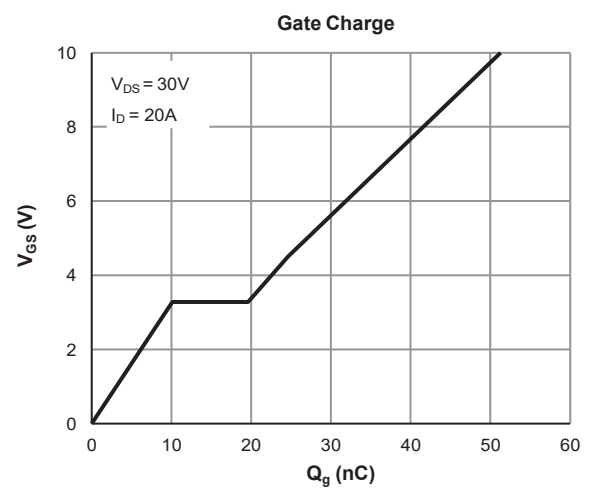
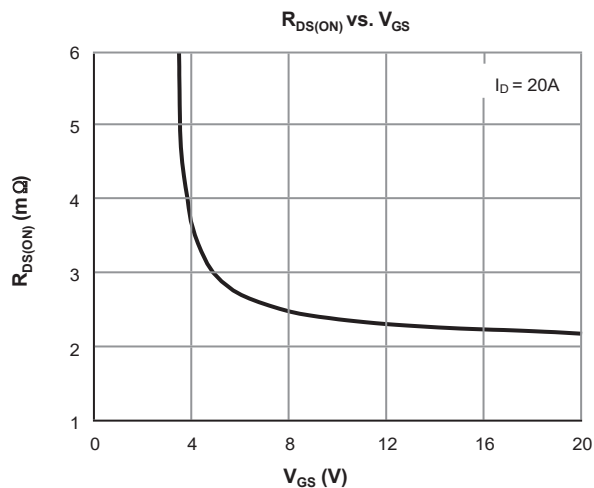
Schematic

## Ordering Information

| Device        | Package | # of Pins | Marking   | MSL | $T_J$ (°C) | Media        | Quantity (pcs) |
|---------------|---------|-----------|-----------|-----|------------|--------------|----------------|
| VSM142N06-D56 | DFN5x6  | 8         | VSM142N06 | 1   | -55 to 150 | 13-inch Reel | 5000           |

## Absolute Maximum Ratings (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

| Parameter                               | Symbol         | Value      | Unit |
|-----------------------------------------|----------------|------------|------|
| Drain-to-Source Voltage                 | $V_{DS}$       | 60         | V    |
| Gate-to-Source Voltage                  | $V_{GS}$       | $\pm 20$   | V    |
| Continuous Drain Current <sup>(1)</sup> | $I_D$          | 142        | A    |
|                                         |                | 90         | A    |
| Pulsed Drain Current <sup>(2)</sup>     | $I_{DM}$       | 354        | A    |
| Avalanche Energy <sup>(3)</sup>         | $E_{AS}$       | 338        | mJ   |
| Power Dissipation <sup>(4)</sup>        | $P_D$          | 114        | W    |
|                                         |                | 45         | W    |
| Junction & Storage Temperature Range    | $T_J, T_{STG}$ | -55 to 150 | °C   |



**Electrical Characteristics** (@  $T_J = 25^\circ\text{C}$  unless otherwise specified)

| Parameter                         | Symbol        | Conditions                                                               | Min. | Typ. | Max.       | Unit             |
|-----------------------------------|---------------|--------------------------------------------------------------------------|------|------|------------|------------------|
| <b>STATIC PARAMETERS</b>          |               |                                                                          |      |      |            |                  |
| Drain-Source Breakdown Voltage    | $V_{(BR)DSS}$ | $I_D = 250\ \mu\text{A}$ , $V_{GS} = 0\text{V}$                          | 60   |      |            | V                |
| Zero Gate Voltage Drain Current   | $I_{DSS}$     | $V_{DS} = 48\text{V}$ , $V_{GS} = 0\text{V}$<br>$T_J = 55^\circ\text{C}$ |      |      | 1.0<br>5.0 | $\mu\text{A}$    |
| Gate-Body Leakage Current         | $I_{GSS}$     | $V_{DS} = 0\text{V}$ , $V_{GS} = \pm 20\text{V}$                         |      |      | $\pm 100$  | nA               |
| Gate Threshold Voltage            | $V_{GS(th)}$  | $V_{DS} = V_{GS}$ , $I_D = 250\ \mu\text{A}$                             | 1.2  | 1.6  | 2.5        | V                |
| Static Drain-Source ON-Resistance | $R_{DS(ON)}$  | $V_{GS} = 10\text{V}$ , $I_D = 20\text{A}$                               |      | 2.4  | 3.0        | $\text{m}\Omega$ |
|                                   |               | $V_{GS} = 4.5\text{V}$ , $I_D = 15\text{A}$                              |      | 3.4  | 4.4        | $\text{m}\Omega$ |
| Forward Transconductance          | $g_{FS}$      | $V_{DS} = 5\text{V}$ , $I_D = 20\text{A}$                                |      | 77   |            | S                |
| Diode Forward Voltage             | $V_{SD}$      | $I_S = 1\text{A}$ , $V_{GS} = 0\text{V}$                                 |      | 0.65 | 1.0        | V                |
| Diode Continuous Current          | $I_S$         | $T_C = 25^\circ\text{C}$                                                 |      |      | 114        | A                |

**DYNAMIC PARAMETERS** <sup>(5)</sup>

|                              |           |                                                                  |  |      |  |          |
|------------------------------|-----------|------------------------------------------------------------------|--|------|--|----------|
| Input Capacitance            | $C_{iss}$ | $V_{GS} = 0\text{V}$ , $V_{DS} = 30\text{V}$ , $f = 1\text{MHz}$ |  | 3174 |  | pF       |
| Output Capacitance           | $C_{oss}$ |                                                                  |  | 872  |  | pF       |
| Reverse Transfer Capacitance | $C_{rss}$ |                                                                  |  | 39   |  | pF       |
| Gate Resistance              | $R_g$     | $V_{GS} = 0\text{V}$ , $V_{DS} = 0\text{V}$ , $f = 1\text{MHz}$  |  | 1.5  |  | $\Omega$ |

**SWITCHING PARAMETERS** <sup>(5)</sup>

|                                               |              |                                                                                              |  |      |  |    |
|-----------------------------------------------|--------------|----------------------------------------------------------------------------------------------|--|------|--|----|
| Total Gate Charge (@ $V_{GS} = 10\text{V}$ )  | $Q_g$        | $V_{GS} = 0$ to $10\text{V}$<br>$V_{DS} = 30\text{V}$ , $I_D = 20\text{A}$                   |  | 51   |  | nC |
| Total Gate Charge (@ $V_{GS} = 4.5\text{V}$ ) | $Q_g$        |                                                                                              |  | 25   |  | nC |
| Gate Source Charge                            | $Q_{gs}$     |                                                                                              |  | 10.1 |  | nC |
| Gate Drain Charge                             | $Q_{gd}$     |                                                                                              |  | 9.5  |  | nC |
| Turn-On DelayTime                             | $t_{D(on)}$  | $V_{GS} = 10\text{V}$ , $V_{DS} = 30\text{V}$<br>$R_L = 1.5\ \Omega$ , $R_{GEN} = 6\ \Omega$ |  | 15.9 |  | ns |
| Turn-On Rise Time                             | $t_r$        |                                                                                              |  | 67   |  | ns |
| Turn-Off DelayTime                            | $t_{D(off)}$ |                                                                                              |  | 64   |  | ns |
| Turn-Off Fall Time                            | $t_f$        |                                                                                              |  | 108  |  | ns |
| Body Diode Reverse Recovery Time              | $t_{rr}$     | $I_F = 20\text{A}$ , $dI_F/dt = 100\text{A}/\mu\text{s}$                                     |  | 49   |  | ns |
| Body Diode Reverse Recovery Charge            | $Q_{rr}$     | $I_F = 20\text{A}$ , $dI_F/dt = 100\text{A}/\mu\text{s}$                                     |  | 52   |  | nC |

**Thermal Performance**

| Parameter                               | Symbol          | Typ. | Max. | Unit                      |
|-----------------------------------------|-----------------|------|------|---------------------------|
| Thermal Resistance, Junction-to-Ambient | $R_{\theta JA}$ | 45   | 55   | $^\circ\text{C}/\text{W}$ |
| Thermal Resistance, Junction-to-Case    | $R_{\theta JC}$ | 1.1  | 1.3  | $^\circ\text{C}/\text{W}$ |

**Notes:**

1. Computed continuous current assumes the condition of  $T_{J\_Max}$  while the actual continuous current depends on the thermal & electro-mechanical application board design.
2. This single-pulse measurement was taken under  $T_{J\_Max} = 150^\circ\text{C}$ .
3.  $E_{AS}$  of 338 mJ is based on starting  $T_J = 25^\circ\text{C}$ ,  $L = 3.0\text{mH}$ ,  $I_{AS} = 15\text{A}$ ,  $V_{GS} = 10\text{V}$ ,  $V_{DD} = 30\text{V}$ ; 100% test at  $L = 0.3\text{mH}$ ,  $I_{AS} = 40\text{A}$ .
4. The power dissipation  $P_D$  is based on  $T_{J\_Max} = 150^\circ\text{C}$ .
5. This value is guaranteed by design hence it is not included in the production test.

### Typical Electrical & Thermal Characteristics

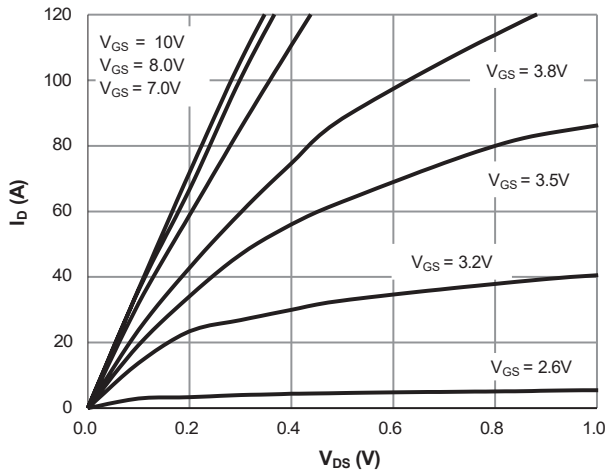


Figure 1: Saturation Characteristics

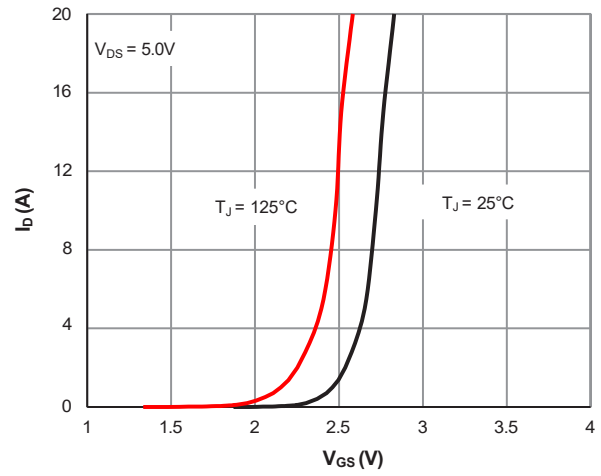


Figure 2: Transfer Characteristics

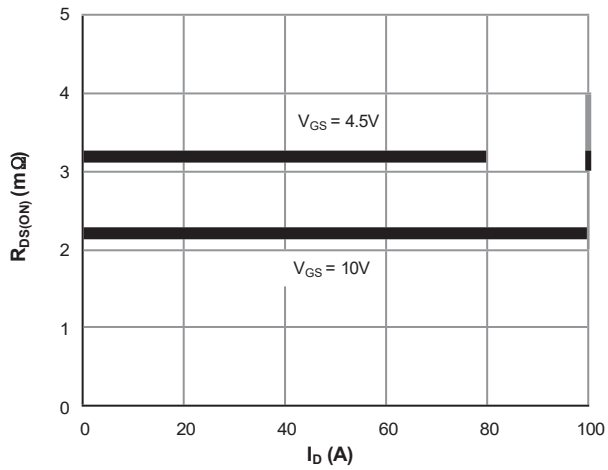


Figure 3:  $R_{DS(ON)}$  vs. Drain Current

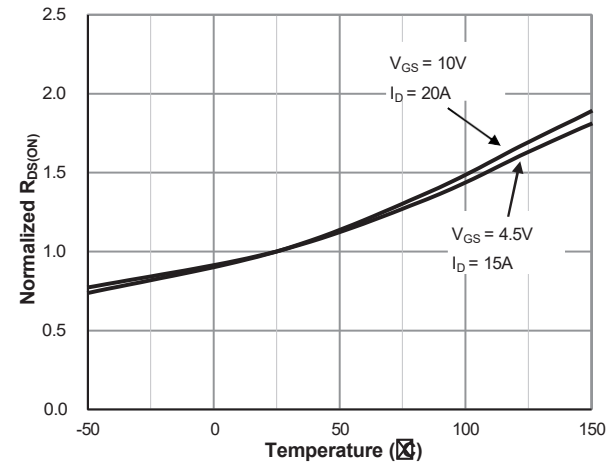


Figure 4:  $R_{DS(ON)}$  vs. Junction Temperature

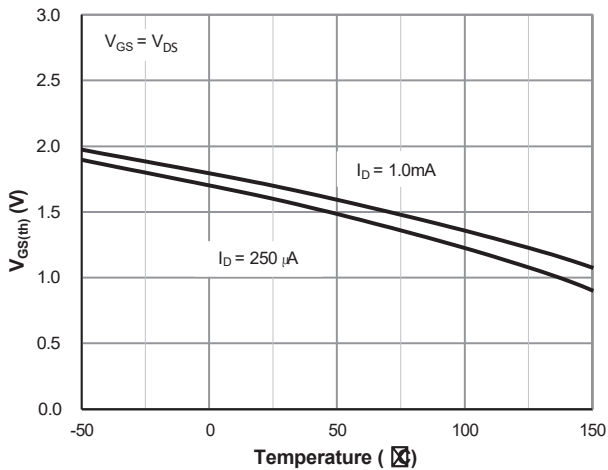


Figure 5:  $V_{GS(th)}$  vs. Junction Temperature

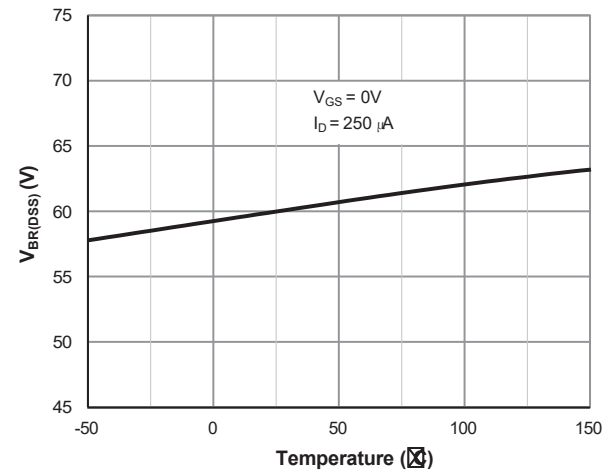


Figure 6:  $V_{BR(DSS)}$  vs. Junction Temperature

## Typical Electrical & Thermal Characteristics

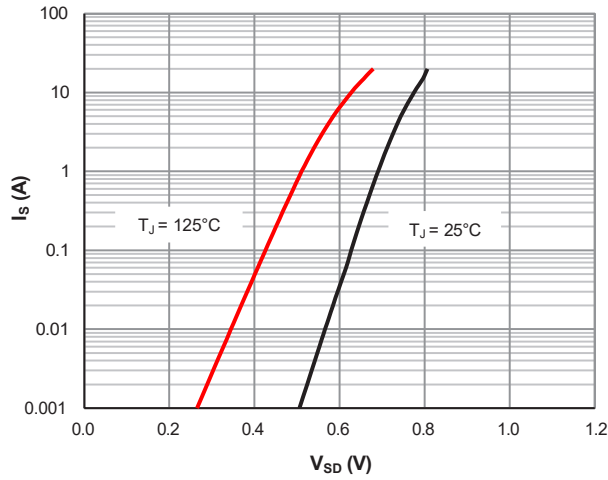


Figure 7: Body-Diode Characteristics

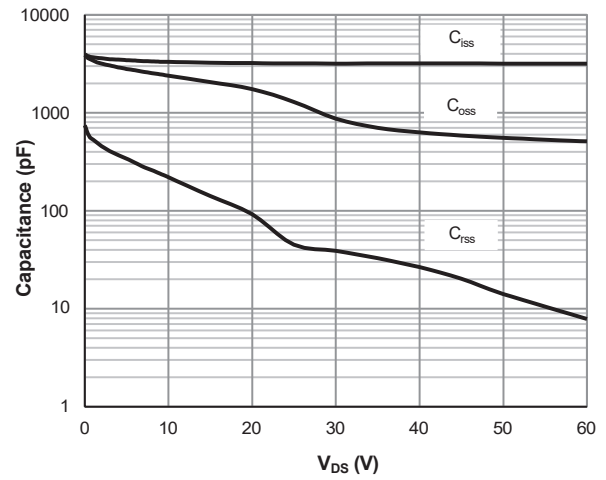


Figure 8: Capacitance Characteristics

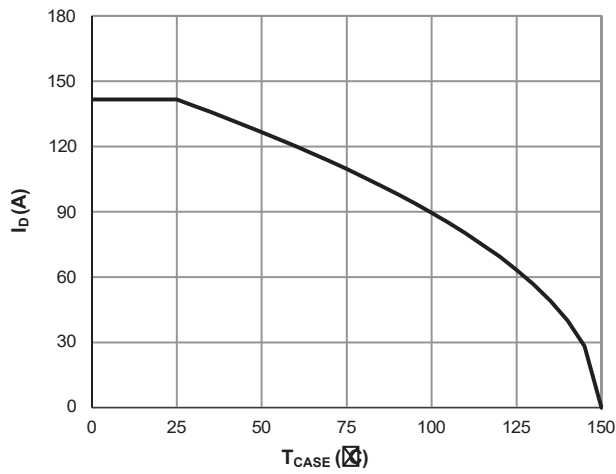


Figure 9: Current De-rating

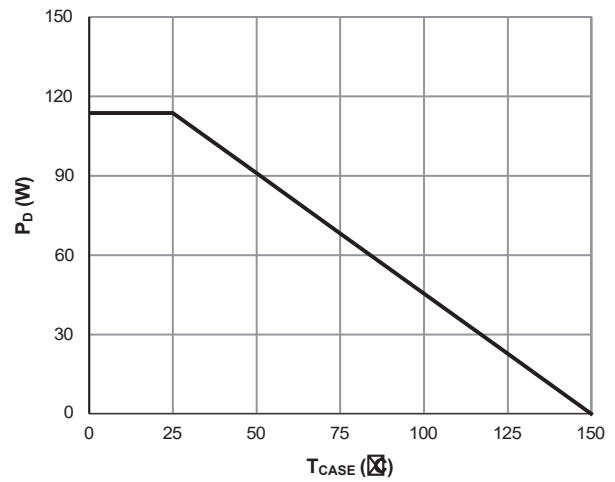


Figure 10: Power De-rating

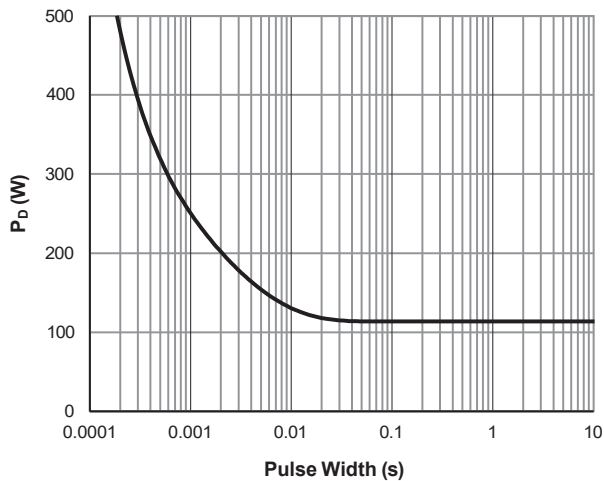


Figure 11: Single Pulse Power Rating, Junction-to-Case

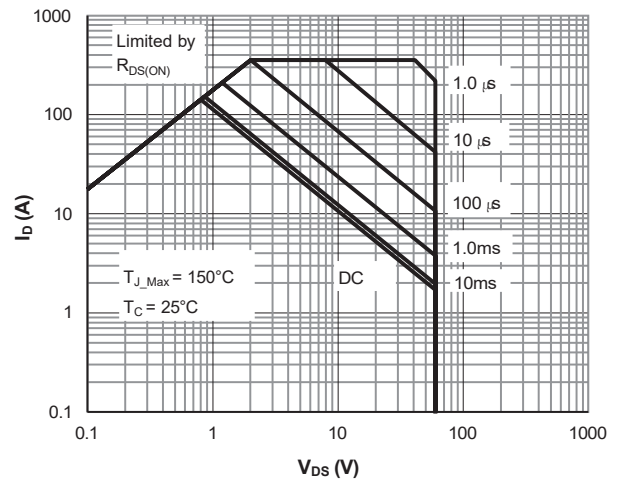


Figure 12: Maximum Safe Operating Area

### Typical Electrical & Thermal Characteristics

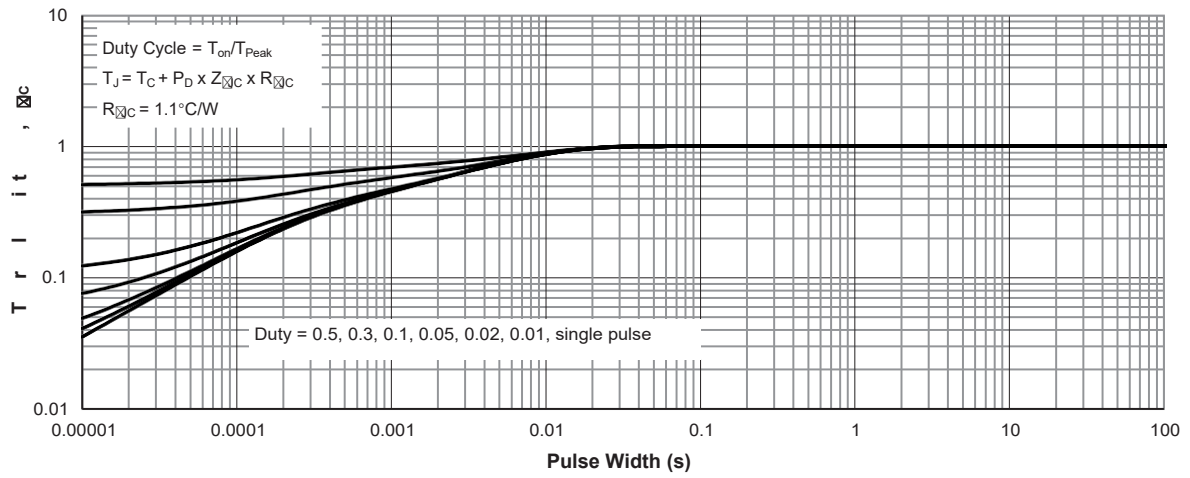


Figure 13: Normalized Maximum Transient Thermal Impedance