

Description

Features

- 100V, 3A
 $R_{DS(ON)} < 115m\Omega @ V_{GS} = 10V$
 $R_{DS(ON)} < 140m\Omega @ V_{GS} = 4.5V$
- Advanced Trench Technology
- Provide Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead free product is acquired

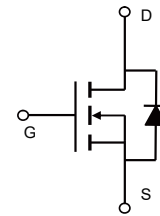
Application

- Load Switch
- PWM Application
- Power management

100% UIS TESTED!
100% ΔV_{ds} TESTED!



SOP-8



Schematic

Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
VSM3N10	VSM3N10-S8	TAPING	SOP-8	13inch	4000	48000

Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

Symbol	Parameter		Max.	Units
V _{DSS}	Drain-Source Voltage		100	V
V _{GSS}	Gate-Source Voltage		±20	V
I _D	Continuous Drain Current	T _A = 25°C	3	A
		T _A = 100°C	2	A
I _{DM}	Pulsed Drain Current ^{note1}		12	A
EAS	Single Pulsed Avalanche Energy ^{note2}		4	mJ
P _D	Power Dissipation	T _A = 25°C	1.9	W
R _{θJA}	Thermal Resistance, Junction to Ambient		65.8	°C/W
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C

Electrical Characteristics (T_J=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	100	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =100V, V _{GS} =0V,	-	-	1.0	μA
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} =±20V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.0	1.5	2.5	V
R _{DS(on)}	Static Drain-Source on-Resistance note3	V _{GS} =10V, I _D =3A	-	88	115	mΩ
		V _{GS} =4.5V, I _D =2A	-	100	140	mΩ
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, f=1.0MHz	-	610	-	pF
C _{oss}	Output Capacitance		-	40	-	pF
C _{rss}	Reverse Transfer Capacitance		-	25	-	pF
Q _g	Total Gate Charge	V _{DS} =50V, I _D =2A, V _{GS} =10V	-	12	-	nC
Q _{gs}	Gate-Source Charge		-	2.2	-	nC
Q _{gd}	Gate-Drain(“Miller”) Charge		-	2.5	-	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DS} =50V, I _D =3A, R _G =1.8Ω, V _{GS} =10V	-	7	-	ns
t _r	Turn-on Rise Time		-	5	-	ns
t _{d(off)}	Turn-off Delay Time		-	16	-	ns
t _f	Turn-off Fall Time		-	6	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _s	Maximum Continuous Drain to Source Diode Forward Current		-	-	3	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	12	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _s =3A	-	-	1.2	V
t _{rr}	Body Diode Reverse Recovery Time	I _F =3A, dI/dt=100A/μs	-	21	-	ns
Q _{rr}	Body Diode Reverse Recovery Charge		-	21	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : T_J=25°C, V_{DD}=50V, V_G=10V, L=0.5mH, R_G=25Ω, I_{AS}=4A

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

Typical Performance Characteristics

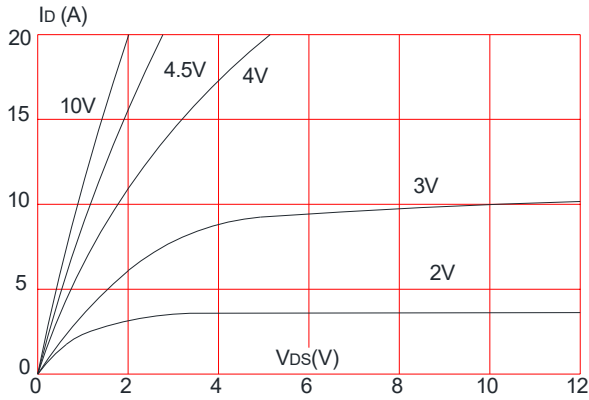
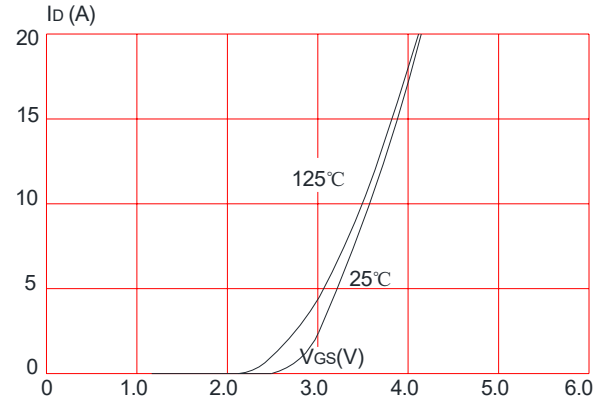
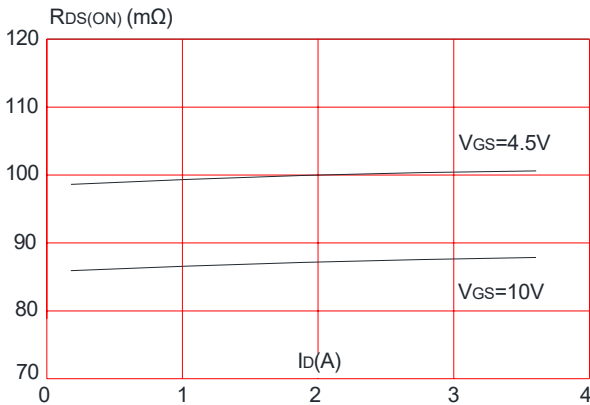
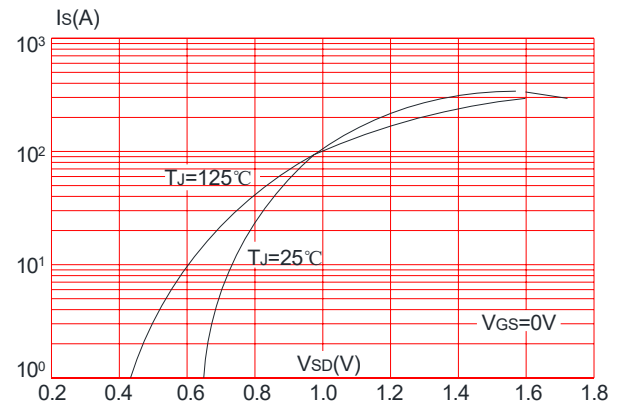
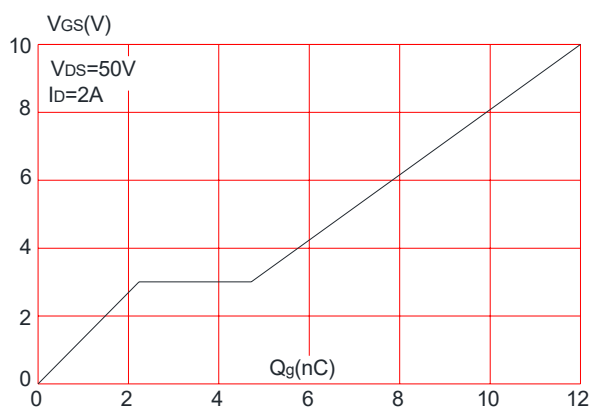
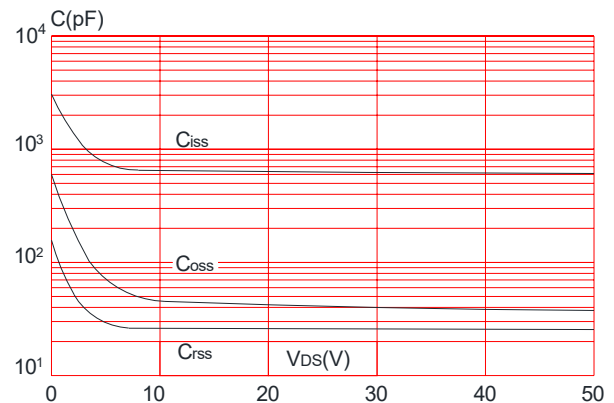
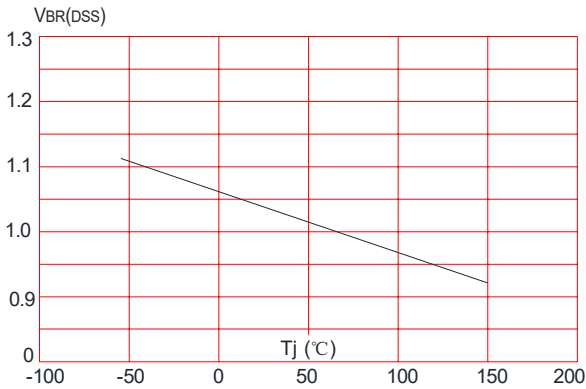
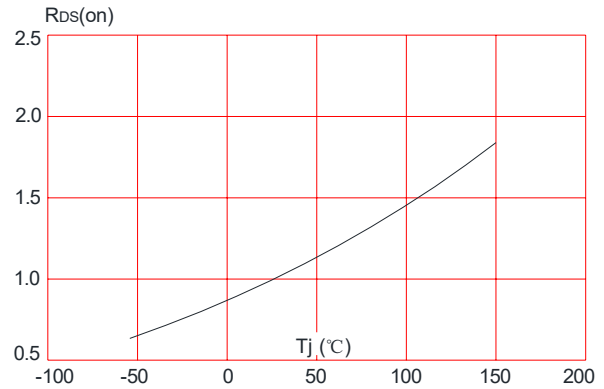
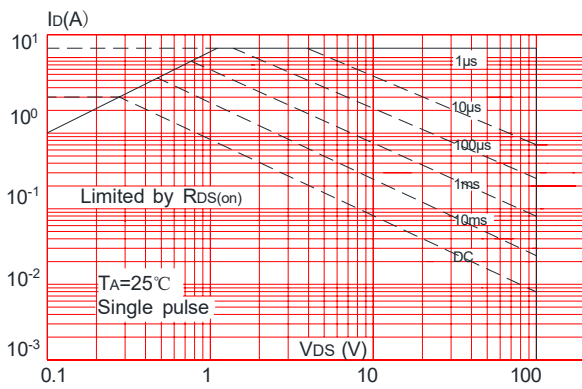
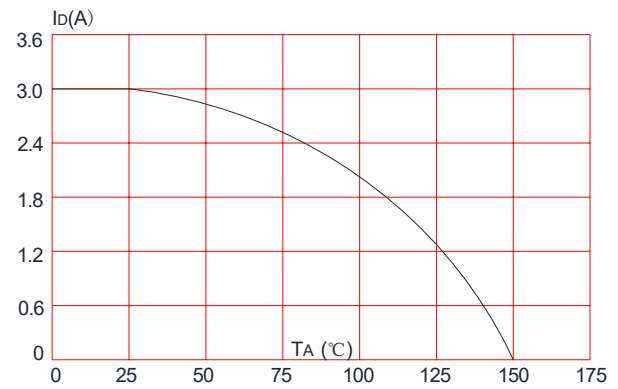
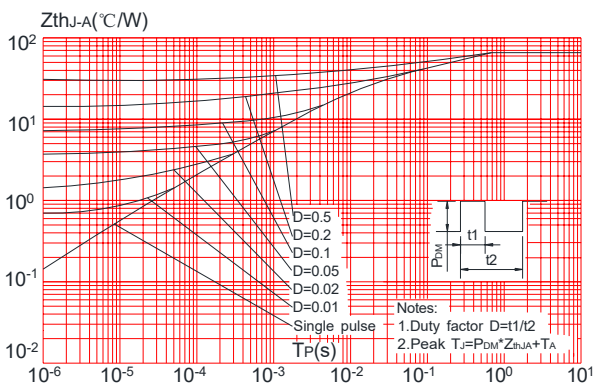
Figure1: Output Characteristics

Figure 2: Typical Transfer Characteristics

Figure 3: On-resistance vs. Drain Current

Figure 4: Body Diode Characteristics

Figure 5: Gate Charge Characteristics

Figure 6: Capacitance Characteristics


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

Figure 8: Normalized on Resistance vs. Junction Temperature

Figure 9: Maximum Safe Operating Area

Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient


Test Circuit

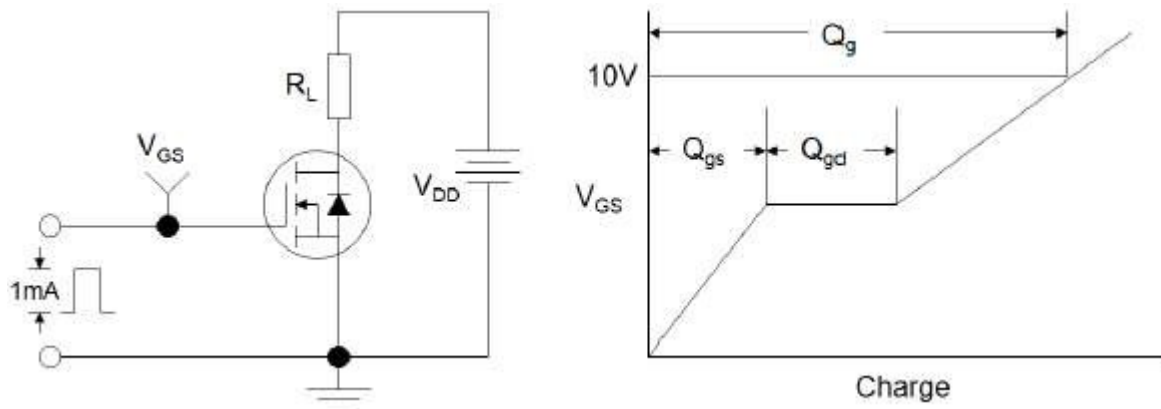


Figure1:Gate Charge Test Circuit & Waveform

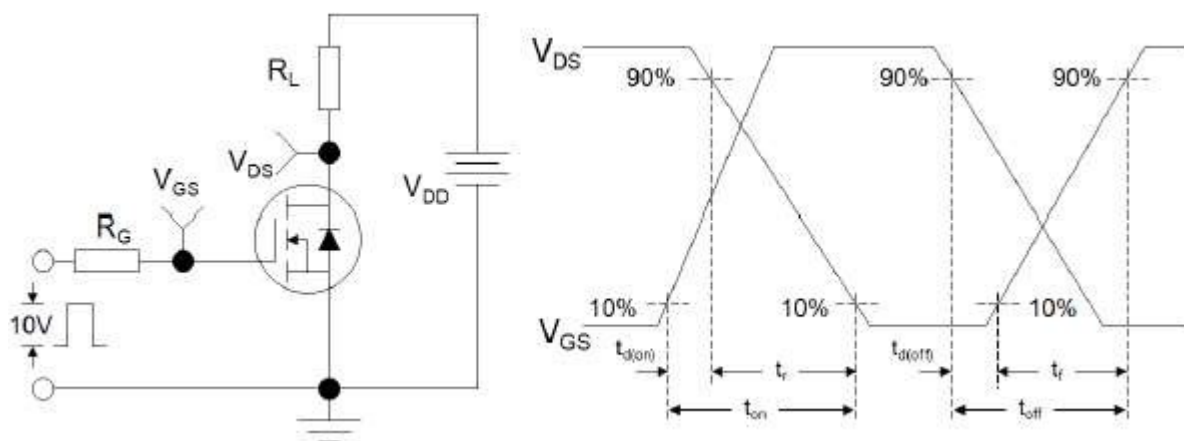


Figure 2: Resistive Switching Test Circuit & Waveforms

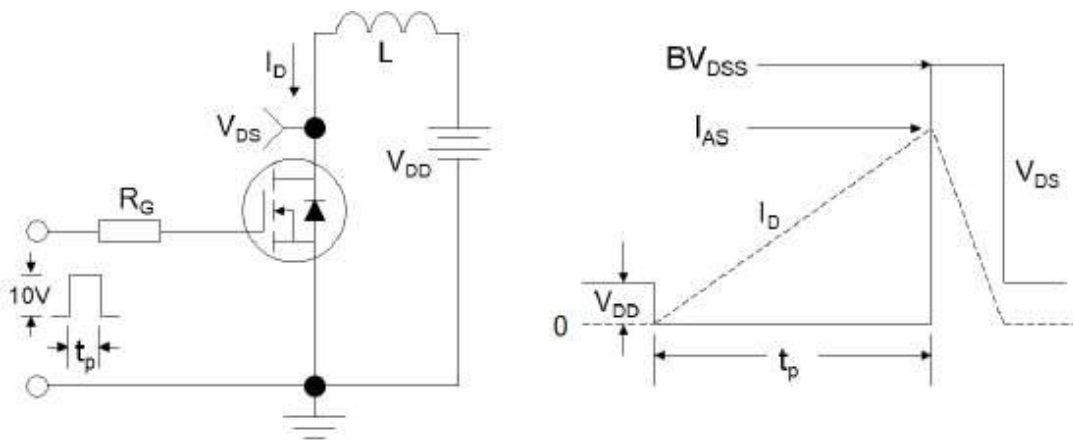


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms