

Description

Features

- 100V, 10A
 $R_{DS(ON)} < 108m\Omega @ V_{GS} = 10V$
 $R_{DS(ON)} < 125m\Omega @ V_{GS} = 4.5V$
- Advanced Trench Technology
- Provide Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead free product is acquired

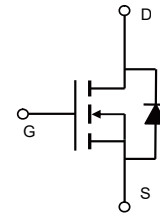
Application

- Load Switch
- PWM Application
- Power management

100% UIS TESTED!
100% ΔV_{ds} TESTED!



TO-252



Schematic

Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
VSM10N10	VSM10N10-T2	TAPING	TO-252	13inch	2500	25000

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter		Max.	Units
V_{DSS}	Drain-Source Voltage		100	V
V_{GSS}	Gate-Source Voltage		± 20	V
I_D	Continuous Drain Current	$T_C = 25^\circ\text{C}$	10	A
		$T_C = 100^\circ\text{C}$	6.5	A
I_{DM}	Pulsed Drain Current ^{note1}		40	A
EAS	Single Pulsed Avalanche Energy ^{note2}		6	mJ
P_D	Power Dissipation	$T_C = 25^\circ\text{C}$	44	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		3.4	$^\circ\text{C/W}$
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +175	$^\circ\text{C}$

Electrical Characteristics (T_J=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	100	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =100V, V _{GS} =0V,	-	-	1.0	μA
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} =±20V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.0	1.5	2.5	V
R _{DS(on)}	Static Drain-Source on-Resistance note3	V _{GS} =10V, I _D =5A	-	86	108	mΩ
		V _{GS} =4.5V, I _D =3A	-	96	125	mΩ
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, f=1.0MHz	-	847	-	pF
C _{oss}	Output Capacitance		-	40	-	pF
C _{rss}	Reverse Transfer Capacitance		-	12	-	pF
Q _g	Total Gate Charge	V _{DS} =50V, I _D =2A, V _{GS} =10V	-	20	-	nC
Q _{gs}	Gate-Source Charge		-	2.8	-	nC
Q _{gd}	Gate-Drain(“Miller”) Charge		-	4	-	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DS} =50V, I _D =3A, R _G =1.8Ω, V _{GS} =10V	-	6	-	ns
t _r	Turn-on Rise Time		-	7	-	ns
t _{d(off)}	Turn-off Delay Time		-	21	-	ns
t _f	Turn-off Fall Time		-	3	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	10	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	40	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _S =10A	-	-	1.2	V
t _{rr}	Body Diode Reverse Recovery Time	I _F =10A, dI/dt=100A/μs	-	22	-	ns
Q _{rr}	Body Diode Reverse Recovery Charge		-	29	-	nC

Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : T_J=25°C, V_{DD}=50V, V_G=10V, L=0.5mH, R_G=25Ω, I_{AS}=5A

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

Typical Performance Characteristics

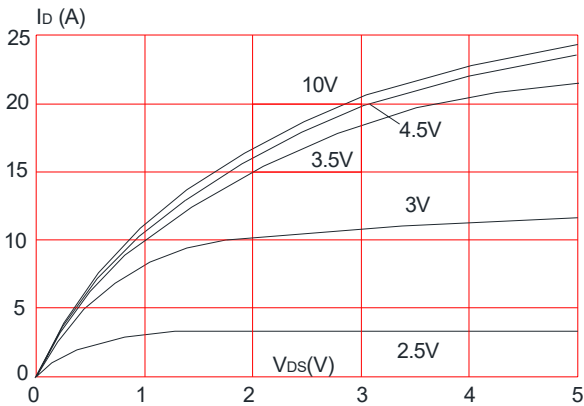
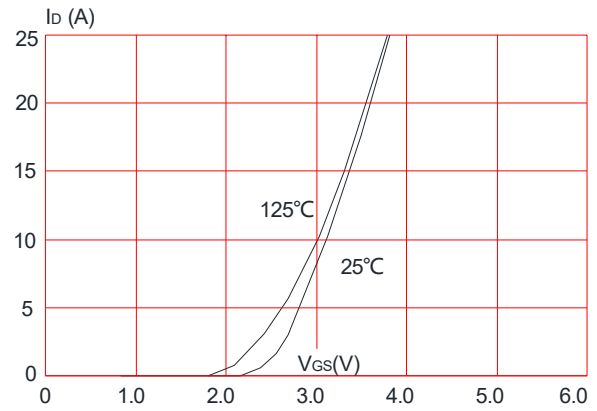
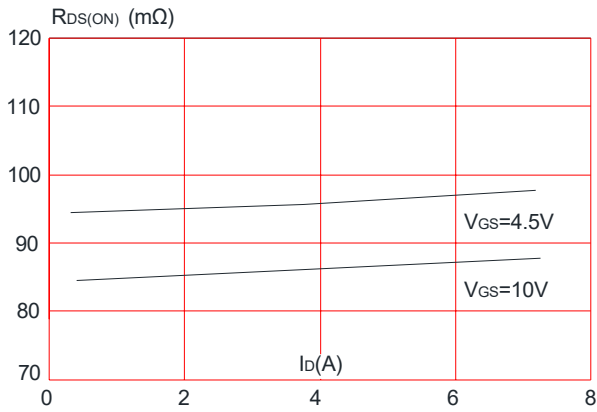
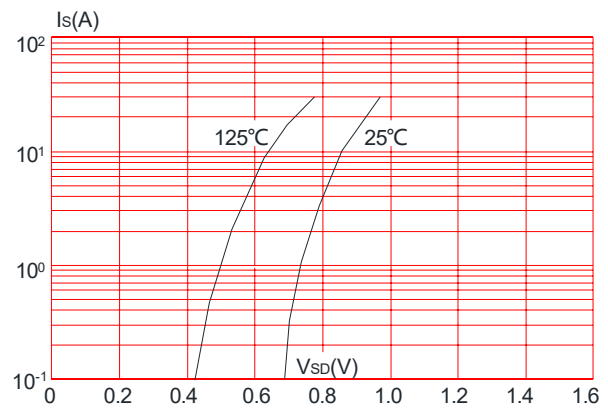
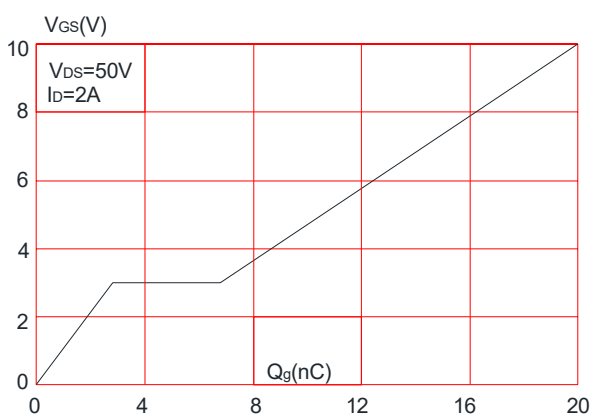
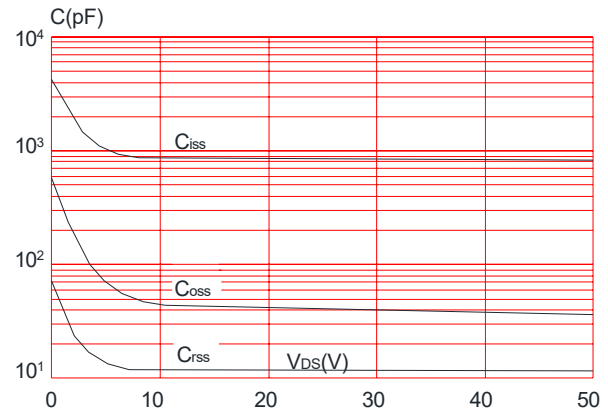
Figure1: Output Characteristics

Figure 2: Typical Transfer Characteristics

Figure 3: On-resistance vs. Drain Current

Figure 4: Body Diode Characteristics

Figure 5: Gate Charge Characteristics

Figure 6: Capacitance Characteristics


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

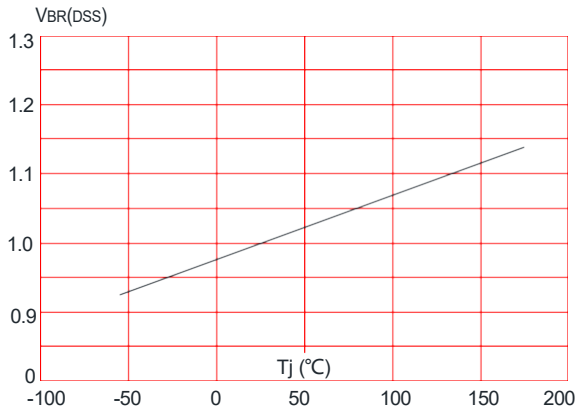


Figure 8: Normalized on Resistance vs. Junction Temperature

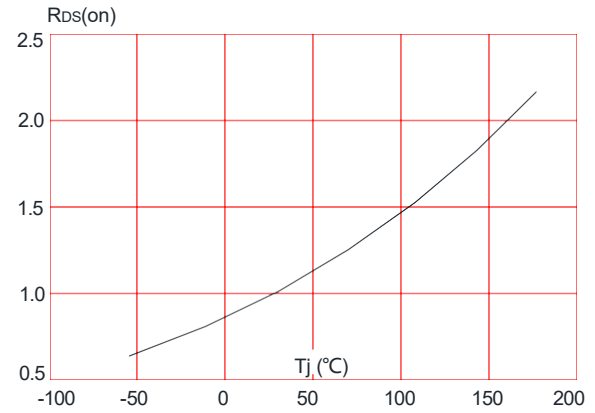


Figure 9: Maximum Safe Operating Area

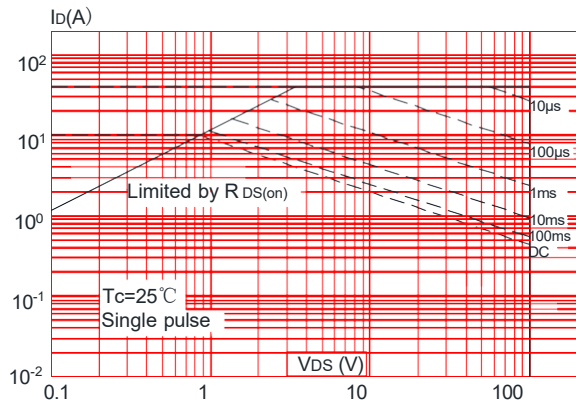


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

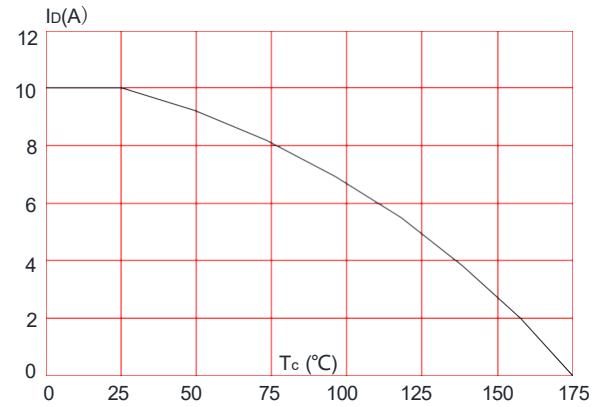
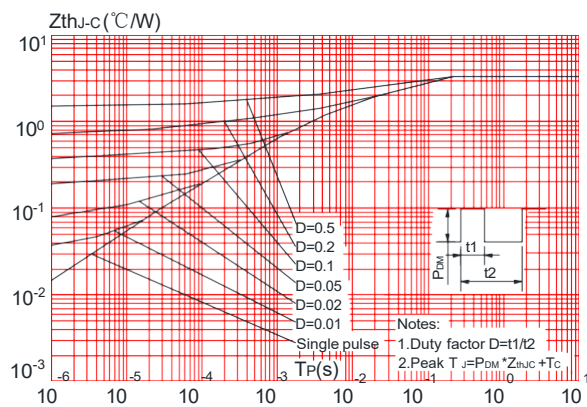


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case



Test Circuit

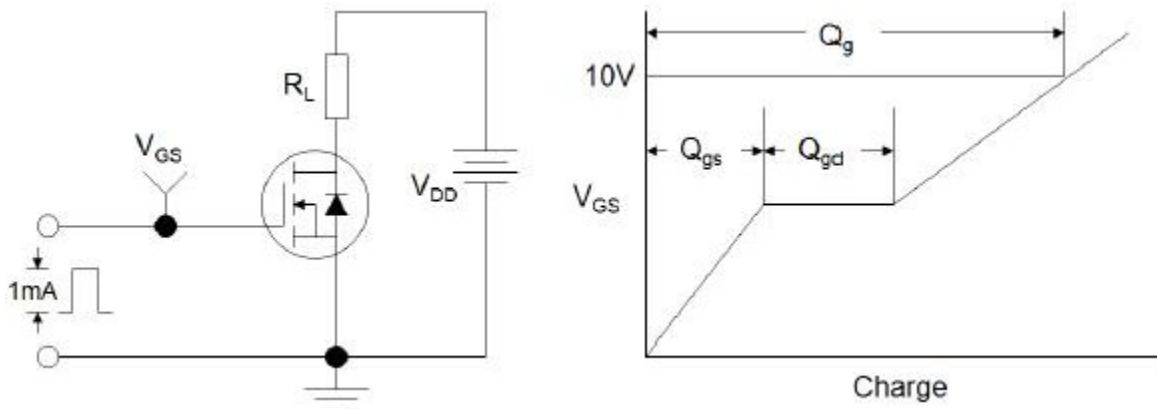


Figure1:Gate Charge Test Circuit & Waveform

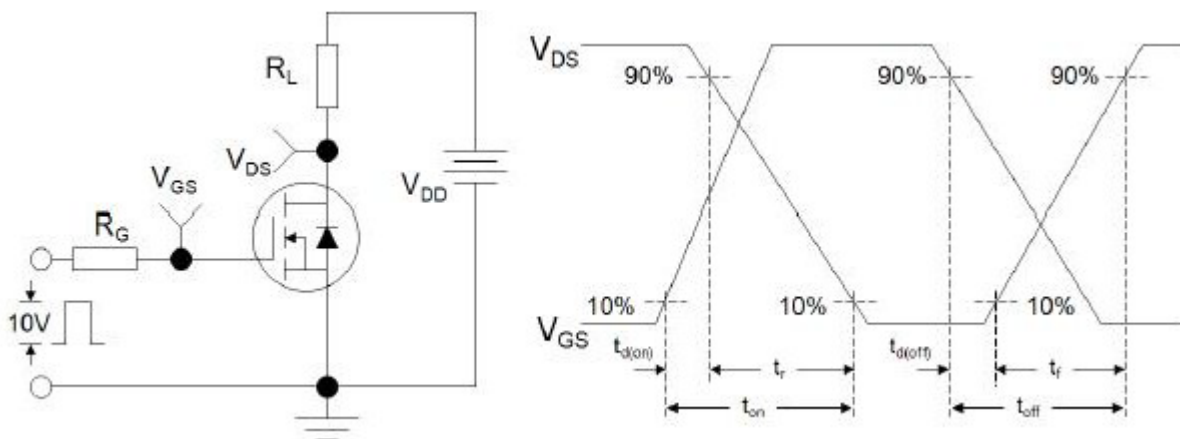


Figure 2: Resistive Switching Test Circuit & Waveforms

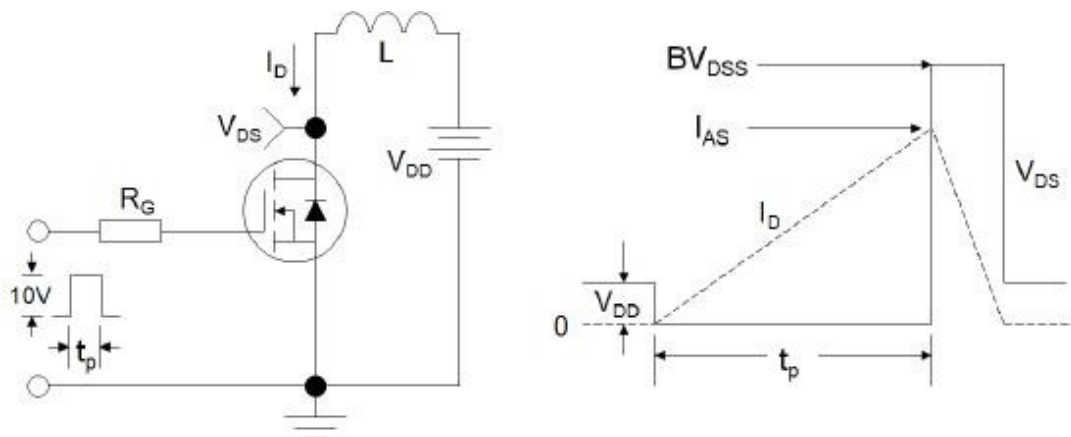


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms