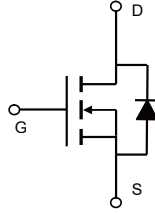


Description

Features 40V, 70A $R_{DS(ON)} < 5.9m\Omega @ V_{GS} = 10V$ $R_{DS(ON)} < 8.6m\Omega @ V_{GS} = 4.5V$ - Advanced Trench Technology - Excellent $R_{DS(ON)}$ and Low Gate Charge - Lead Free	Applications - Load Switch - PWM Application - Power Management <i>100% UIS TESTED!</i> <i>100% ΔV_{ds} TESTED!</i>
 TO-252	 Schematic

Package Marking and Ordering Information

Device Marking	Device	Outline	Package	Reel Size	Reel(pcs)	Per Carton (pcs)
VSM70N04	VSM70N04-T2	TAPING	TO-252	13"	2500	25000

Absolute Maximum Ratings (@ $T_C = 25^\circ C$ unless otherwise specified)

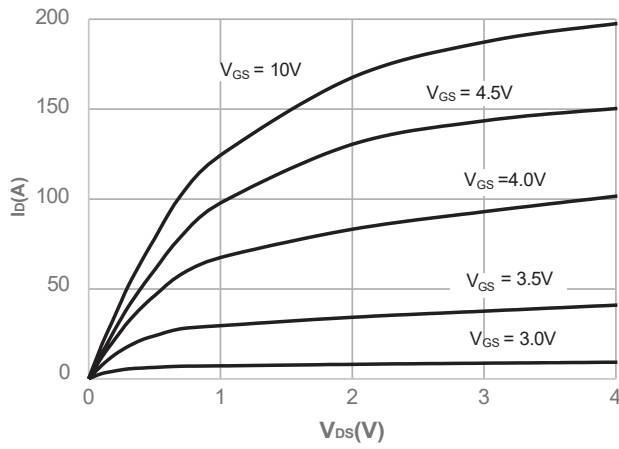
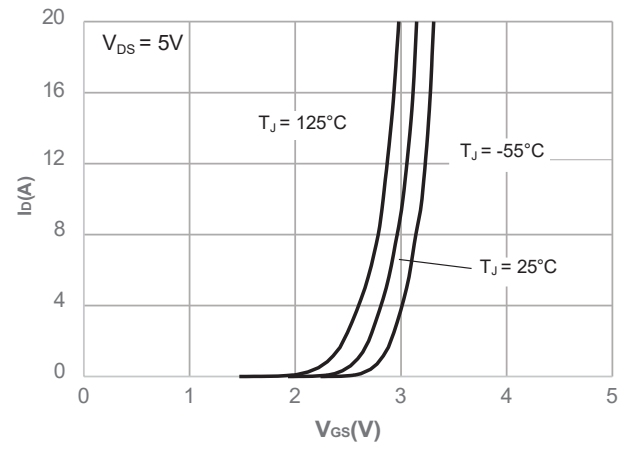
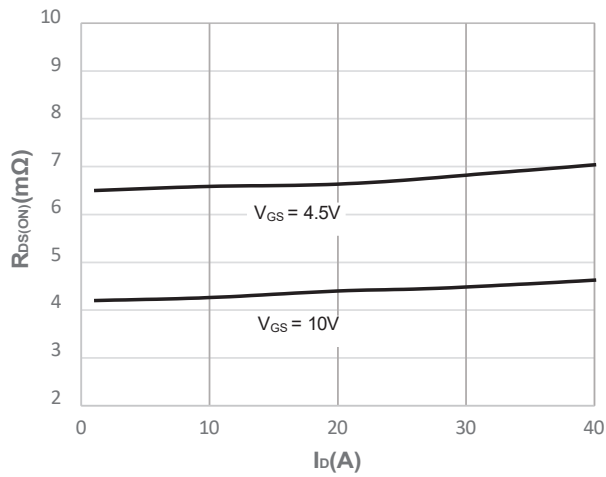
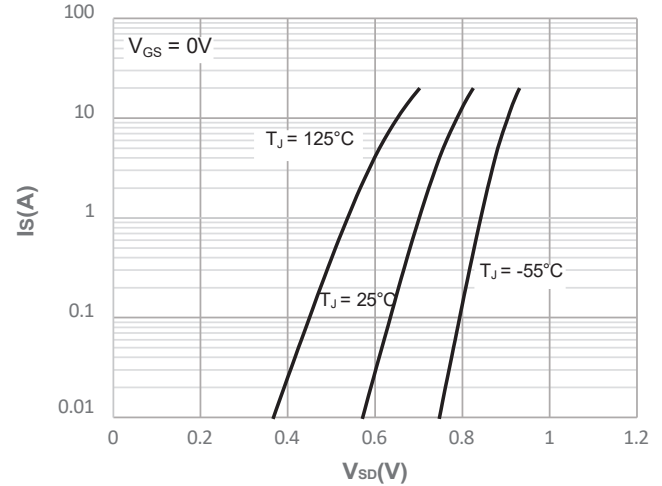
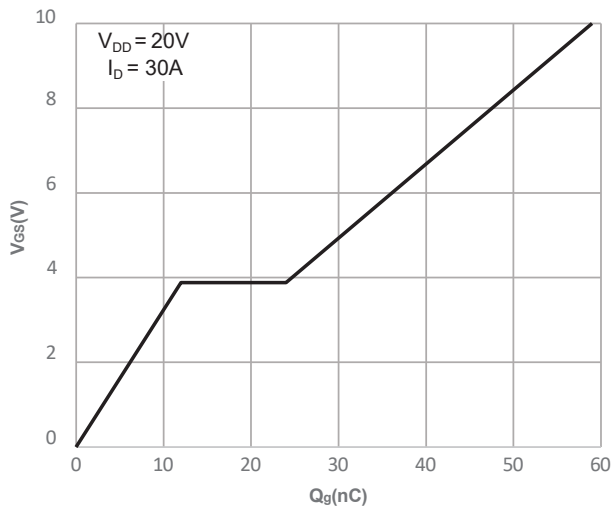
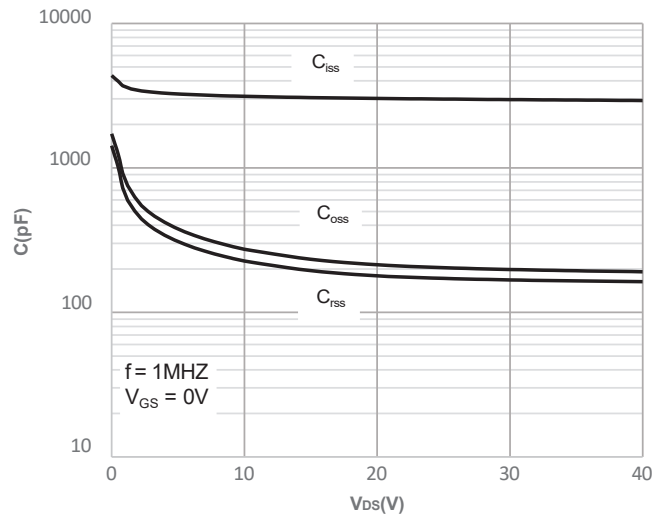
Symbol	Parameter	Value	Units
V_{DS}	Drain-to-Source Voltage	40	V
V_{GS}	Gate-to-Source Voltage	± 20	V
I_D	Continuous Drain Current	$T_C = 25^\circ C$	A
		$T_C = 100^\circ C$	
I_{DM}	Pulsed Drain Current ⁽¹⁾	280	A
E_{AS}	Single Pulsed Avalanche Energy ⁽²⁾	121	mJ
P_D	Power Dissipation	$T_C = 25^\circ C$	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient ⁽³⁾	34	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance, Junction to Case	2.1	
T_J, T_{STG}	Junction & Storage Temperature Range	-55 to 150	$^\circ C$

Electrical Characteristics ($T_J = 25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	I _D = 250 μA, V _{GS} = 0V	40	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 40V, V _{GS} = 0V	-	-	1.0	μA
I _{GSS}	Gate-Body Leakage Current	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	1.3	1.8	2.3	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽⁴⁾	V _{GS} = 10V, I _D = 30A	-	4.5	5.9	mΩ
		V _{GS} = 4.5V, I _D = 20A	-	6.6	8.6	mΩ
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{GS} = 0V, V _{DS} = 20V, f = 1MHz	-	3031	-	pF
C _{oss}	Output Capacitance		-	213	-	pF
C _{rss}	Reverse Transfer Capacitance		-	179	-	pF
Q _g	Total Gate Charge	V _{GS} = 0 to 10V V _{DS} = 20V, I _D = 30A	-	59	-	nC
Q _{gs}	Gate Source Charge		-	12	-	nC
Q _{gd}	Gate Drain("Miller") Charge		-	12	-	nC
Switching Characteristics						
t _{d(on)}	Turn-On DelayTime	V _{GS} = 10V, V _{DD} = 20V I _D = 30A, R _{GEN} = 3 Ω	-	11	-	ns
t _r	Turn-On Rise Time		-	32	-	ns
t _{d(off)}	Turn-Off DelayTime		-	52	-	ns
t _f	Turn-Off Fall Time		-	13	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	70	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	280	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S = 30A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	I _F = 20A, di/dt = 100A/us	-	13	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	7	-	nC

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
 2. E_{AS} condition: Starting $T_J=25^{\circ}\text{C}$, $V_{DD}=20\text{V}$, $V_G=10\text{V}$, $R_G=25\text{ohm}$, $L=0.5\text{mH}$, $I_{AS}=22\text{A}$
 3. $R_{\theta JA}$ is measured with the device mounted on a 1inch² pad of 2oz copper FR4 PCB
 4. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.

Typical Performance Characteristics

Figure 1: Output Characteristics

Figure 2: Typical Transfer Characteristics

Figure 3: On-resistance vs. Drain Current

Figure 4: Body Diode Characteristics

Figure 5: Gate Charge Characteristics

Figure 6: Capacitance Characteristics


Typical Performance Characteristics

Figure 7: Normalized Breakdown voltage vs. Junction Temperature

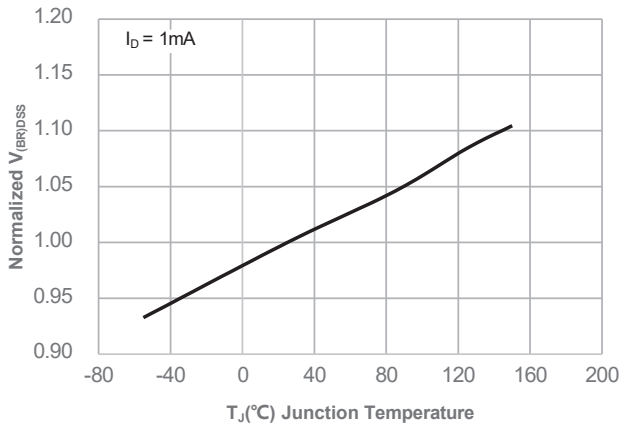


Figure 8: Normalized on Resistance vs. Junction Temperature

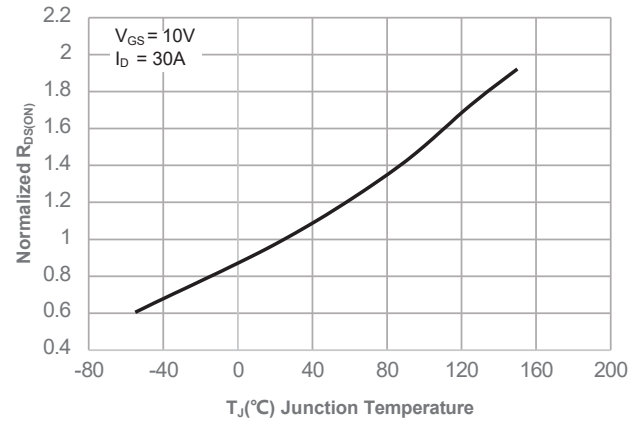


Figure 9: Maximum Safe Operating Area

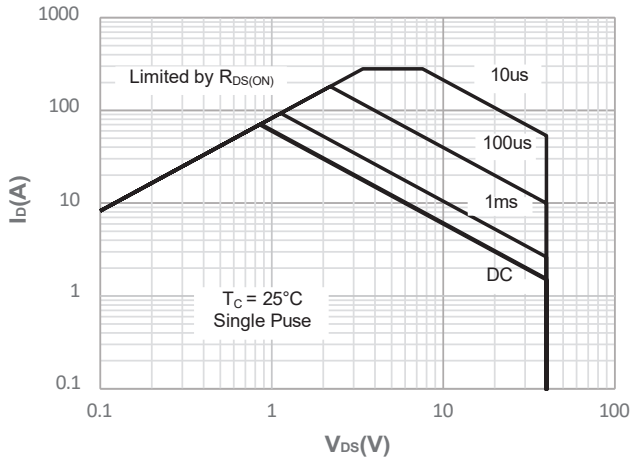


Figure 10: Maximum Continuous Driant Current vs. Case Temperature

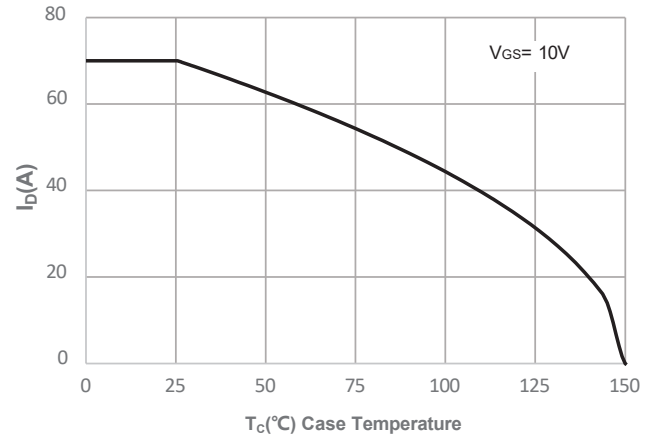


Figure 11: Normalized Maximum Transient Thermal Impedance

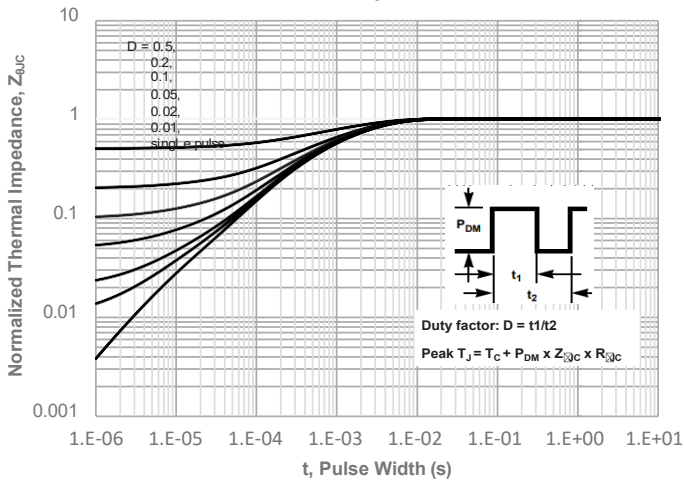
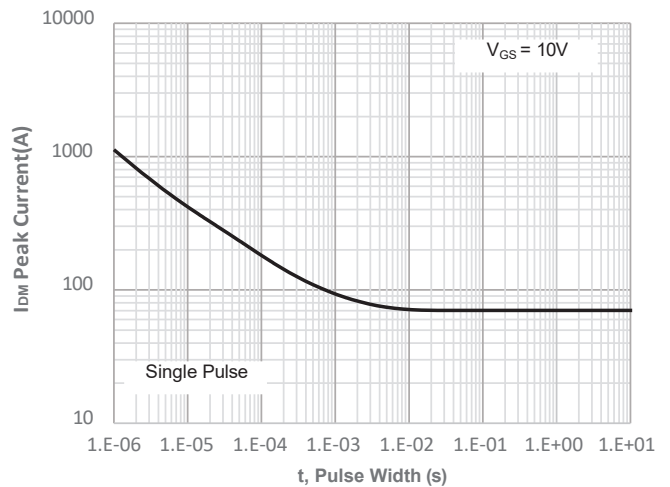


Figure 12: Peak Current Capacity



Test Circuit

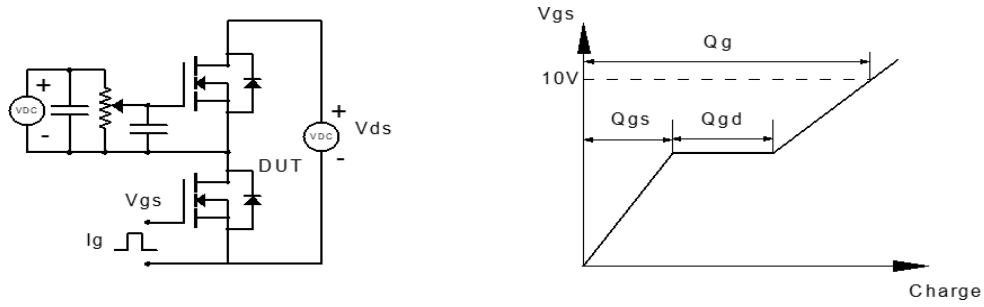


Figure 1: Gate Charge Test Circuit & Waveform

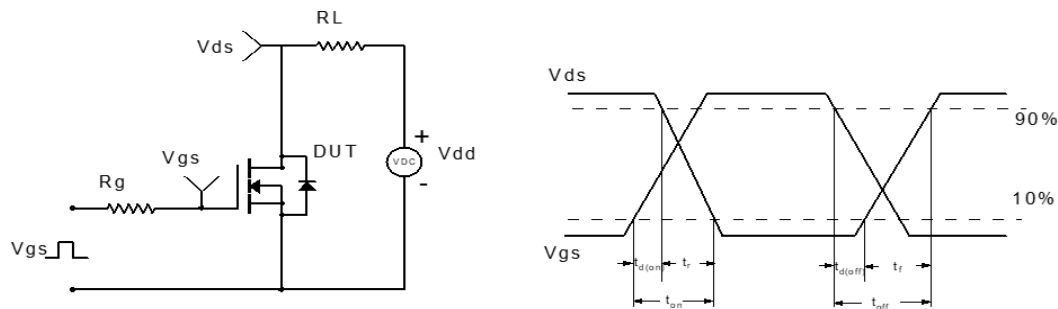


Figure 2: Resistive Switching Test Circuit & Waveform

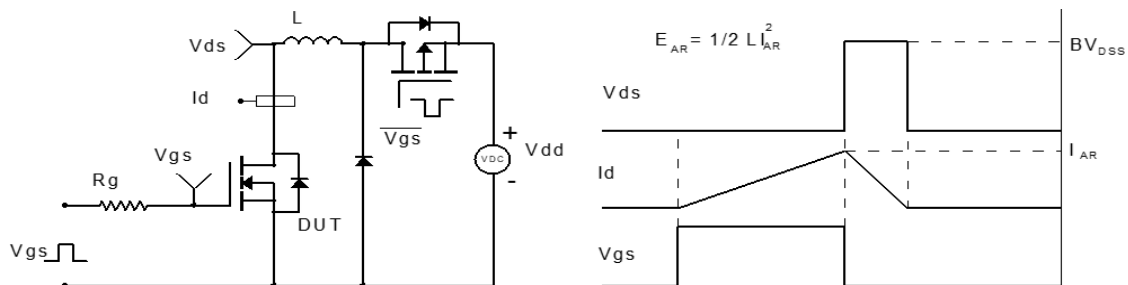


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

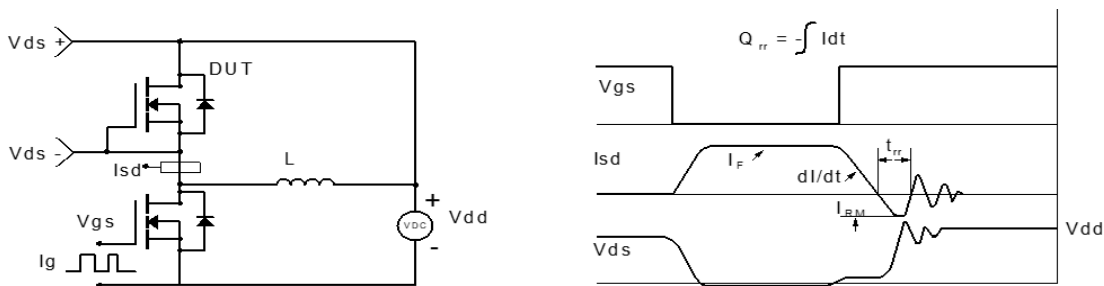


Figure 4: Diode Recovery Test Circuit & Waveform