

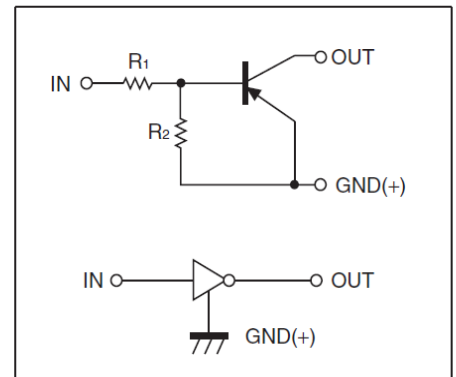
DTA115EM/DTA115EE/DTA115EUA DTA115EKA/DTA115ECA/DTA115ESA

DIGITAL TRANSISTOR (PNP)

FEATURES

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors(see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input.They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy

• Equivalent Circuit



Part Number	MARKING ⁽¹⁾	Package	Packing Method	Pack Quantity
DTA115EM	19	SOT-723	Reel	8000pcs/Reel
DTA115EE	19	SOT-523	Reel	3000pcs/Reel
DTA115EUA	19	SOT-323	Reel	3000pcs/Reel
DTA115EKA	19	SOT-23-3L	Reel	3000pcs/Reel
DTA115ECA	19	SOT-23	Reel	3000pcs/Reel
DTA115ESA	A115 ⁽²⁾ E·XXX	TO-92S	Bulk	1000pcs/Bag
DTA115ESA-TA	A115 ⁽²⁾ E·XXX	TO-92S	Tape	3000pcs/Box

Notes: (1). Solid dot = Green molding compound device, if none, the normal device.

(2). XXX=Code

MAXIMUM RATINGS(Ta=25°C unless otherwise noted)

Symbol	Parameter	Limits(DTA115E□)						Unit
		M	E	UA	KA	CA	SA	
V_{CC}	Supply Voltage	-50						V
V_{IN}	Input Voltage	-40~+10						V
I_O	Output Current	-100						mA
P_D	Power Dissipation	100	150	200	200	200	300	mW
T_J, T_{stg}	Operation Junction and Storage Temperature Range	-55~+150						°C

ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Input voltage	V _{I(off)}	V _{CC} =-5V, I _O =-100μA	-0.5			V
	V _{I(on)}	V _O =-0.3V, I _O =-1 mA			-3	V
Output voltage	V _{O(on)}	I _O /I _I =-5mA/-0.25mA			-0.3	V
Input current	I _I	V _I =-5V			-0.15	mA
Output current	I _{O(off)}	V _{CC} =-50V, V _I =0			-0.5	μA
DC current gain	G _I	V _O =-5V, I _O =-5mA	82			
Input resistance	R ₁		70	100	130	kΩ
Resistance ratio	R ₂ /R ₁		0.8	1	1.2	
Transition frequency	f _T	V _O =-10V, I _O =-5mA, f=100MHz		250		MHz