

VS78L00C Series Three Terminal Positive Regulators

1 Introduction

The VS78L00C series is a group of three terminal positive voltage linear regulators with fixed voltage output. Its input voltage can reach up to 30V (or 35V) and can deliver output current up to 100mA with good power dissipation. The VS78L00C series adopts fixed voltage output and no peripheral resistance, improving the space utilization efficiency of printed circuit boards (PCBs). In addition, the VS78L00C series also integrates internal current limiting, thermal shutdown, and safe-area compensation, which helps protect the device from overload. Therefore, the VS78L00C series is widely used in various scenarios, such as on card adjustment, to eliminate noise and distribution issues related to single point supervision. When used as a substitute for Zener diode resistor combinations, it can effectively increase output impedance and reduce bias current.

2 Available Packages

PART NUMBER	PACKAGE
VS78L00C Series	SOT-23-3L
	SOT-89-3L
	TO-92

Note: For all available packages, please refer to the part *Orderable Information*.

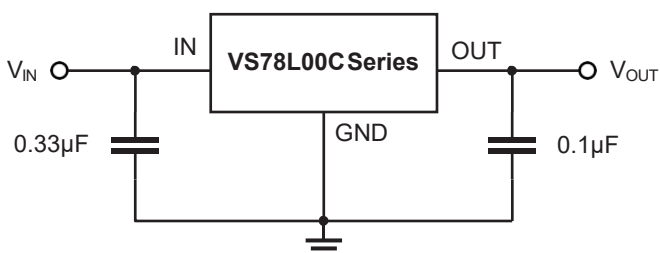


Figure 2-1. Typical Application Circuit

3 Features

- Available Output Voltage:
 - 5.0V: VS78L05C
 - 6.0V: VS78L06C
 - 8.0V: VS78L08C
 - 9.0V: VS78L09C
 - 12V: VS78L12C
 - 15V: VS78L15C
 - 18V: VS78L18C
- Maximum Input Voltage:
 - 30V for $V_{OUT} < 10V$
 - 35V for $V_{OUT} > 10V$
- Maximum Output Current:
 - up to 100mA
- Operating Junction Temperature:
 - 40 ~ 125°C
- Output Voltage Tolerances at $T_J = 25^{\circ}C$:
 - ±3% for Conventional Device
 - ±2% can be Customized
- Output Voltage Tolerances of ±5% over the Temperature Range
- No External Components
- Build-in Current Limit
- Thermal Shutdown Protection
- Output Transistor Safe-Area Protection

4 Applications

- Chemical or Gas Sensors
- Computing and Servers
- Field Transmitter: Temperature Sensors
- Flow Meters
- On-Card Regulation
- Telecommunications
- White Goods

5 Orderable Information

MODEL	DEVICE	PACKAGE	OP T _J	ECO PLAN	MSL	PACKING OPTION	SORT
±3% Grade Products (continued)							
VS78L00C-5.0	VS78L05C-TAB-AAG	TO-92	-40 ~ 125°C	RoHS & Green	N / A for Pkg Type	Ammo 2000 Units / Box	Active
VS78L00C-6.0	VS78L06C-TAB-AAG	TO-92	-40 ~ 125°C	RoHS & Green	N / A for Pkg Type	Ammo 2000 Units / Box	Active
VS78L00C-8.0	VS78L08C-TAB-AAG	TO-92	-40 ~ 125°C	RoHS & Green	N / A for Pkg Type	Ammo 2000 Units / Box	Active
VS78L00C-9.0	VS78L09C-TAB-AAG	TO-92	-40 ~ 125°C	RoHS & Green	N / A for Pkg Type	Ammo 2000 Units / Box	Active
VS78L00C-12	VS78L12C-TAB-AAG	TO-92	-40 ~ 125°C	RoHS & Green	N / A for Pkg Type	Ammo 2000 Units / Box	Active
VS78L00C-15	VS78L15C-TAB-AAG	TO-92	-40 ~ 125°C	RoHS & Green	N / A for Pkg Type	Ammo 2000 Units / Box	Active
VS78L00C-18	VS78L18C-TAB-AAG	TO-92	-40 ~ 125°C	RoHS & Green	N / A for Pkg Type	Ammo 2000 Units / Box	Active
VS78L00C-5.0	78L05C-TAB-ABG	TO-92	-40 ~ 125°C	RoHS & Green	N / A for Pkg Type	Bulk 1000 Units / Bag	Active
VS78L00C-6.0	VS78L06C-TAB-ABG	TO-92	-40 ~ 125°C	RoHS & Green	N / A for Pkg Type	Bulk 1000 Units / Bag	Active
VS78L00C-8.0	VS78L08C-TAB-ABG	TO-92	-40 ~ 125°C	RoHS & Green	N / A for Pkg Type	Bulk 1000 Units / Bag	Active
VS78L00C-9.0	VS78L09C-TAB-ABG	TO-92	-40 ~ 125°C	RoHS & Green	N / A for Pkg Type	Bulk 1000 Units / Bag	Active
VS78L00C-12	VS78L12C-TAB-ABG	TO-92	-40 ~ 125°C	RoHS & Green	N / A for Pkg Type	Bulk 1000 Units / Bag	Active
VS78L00C-15	VS78L15C-TAB-ABG	TO-92	-40 ~ 125°C	RoHS & Green	N / A for Pkg Type	Bulk 1000 Units / Bag	Active
VS78L00C-18	VS78L18C-TAB-ABG	TO-92	-40 ~ 125°C	RoHS & Green	N / A for Pkg Type	Bulk 1000 Units / Bag	Active

7 Specifications

7.1 Absolute Maximum Ratings

(over operating free-air temperature range, unless otherwise specified)⁽¹⁾

CHARACTERISTIC			SYMBOL	VALUE	UNIT
Maximum input voltage ⁽²⁾			V _{IN} MAX	36	V
Maximum power dissipation	VS78L00C Series	SOT-23-3L	P _D Max	Internally Limited ⁽³⁾	W
		SOT-89-3L			
		TO-92			
Maximum junction temperature			T _J Max	150	°C
Storage temperature			T _{stg}	-65 ~ 150	°C
Soldering temperature & time			T _{solder}	260°C, 10s	-

(1) Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions*. Exposure to absolute-maximum rated conditions for extended periods may affect device reliability.

(2) All voltages are with respect to network ground terminal.

(3) Refer to *Thermal Information* for details.

7.2 Recommended Operating Conditions

PARAMETER ⁽⁴⁾		SYMBOL	MIN.	NOM.	MAX.	UNIT
Input voltage range	VS78L05C	V _{IN}	-	-	30	V
	VS78L06C		-	-		
	VS78L08C		-	-		
	VS78L09C		-	-	35	
	VS78L12C		-	-		
	VS78L15C		-	-		
	VS78L18C		-	-		
Operating junction temperature		T _J	-40	-	125	°C
Operating ambient temperature		T _A	-	-(5)	-	°C

(4) It is necessary to ensure that the operating junction temperature of the device does not exceed the rated value of the recommended operating conditions when using the device for design.

7 Specifications

7.3 ESD Ratings

ESD RATINGS		SYMBOL	VALUE	UNIT
Electrostatic discharge ⁽⁶⁾	Human body model	$V_{ESD-HBM}$	2000	V
	Machine model	V_{ESD-MM}	200	

(5) ESD testing is conducted in accordance with the relevant specifications formulated by the Joint Electronic Equipment Engineering Commission (JEDEC). The human body mode (HBM) electrostatic discharge test is based on the JESD22-A114D test standard, using a 100pF capacitor and discharging to each pin of the device through a resistance of 1.5kΩ. The electrostatic discharge test in mechanical mode (MM) is based on the JESD22-A115-A test standard and uses a 200pF capacitor to discharge directly to each pin of the device.

7.4 Thermal Information

THERMAL METRIC ⁽⁷⁾	SYMBOL	VS78L00C Series			UNIT
		SOT-23-3L	SOT-89-3L	TO-92	
Junction-to-ambient thermal resistance	$R_{\theta JA}$	260.7	121.6	167.2	°C/W
Junction-to-case thermal resistance	$R_{\theta JC}$	64.9	29.4	40.3	°C/W
Reference maximum power dissipation for continuous operation	$P_{D Ref}$	0.38	0.82	0.60	W

(6) Thermal metric is measured in still air with $T_A = 25^{\circ}\text{C}$ and installed on a 1 in² FR-4 board covered with 2 ounces of copper.

7 Specifications

7.5 Electrical Characteristics

VS78L05C ($V_{IN} = 10V$, $I_{OUT} = 40mA$, $C_{IN} = 0.33\mu F$, $C_{OUT} = 0.1\mu F$, $T_J = 25^\circ C$, unless otherwise specified)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS ⁽⁷⁾		MIN.	TYP.	MAX.	UNIT
Input voltage	V_{IN}	-		-	-	30	V
Output voltage	V_{OUT}	$T_J = 25^\circ C$	$\pm 3\%$ grade ⁽⁸⁾	4.85	5.00	5.15	V
			$\pm 2\%$ grade ⁽⁸⁾	4.90		5.10	
		$V_{IN} = 7$ to $20V$, $I_{OUT} = 1$ to $100mA$, $T_J = -40$ to $125^\circ C$, $P_D \leq 0.75W$		4.75	5.00	5.25	
Output current	I_{OUT}	$T_J = 25^\circ C$		100	-	-	mA
Quiescent current	I_Q	$I_{OUT} = 0mA$		-	3.0	5.5	mA
Quiescent current change	ΔI_Q	$V_{IN} = 8$ to $20V$, $T_J = -40$ to $125^\circ C$		-	-	1.5	mA
		$I_{OUT} = 1$ to $40mA$, $T_J = -40$ to $125^\circ C$		-	-	0.1	
Dropout voltage	$V_{DO}^{(9)}$	$I_{OUT} = 40mA$		-	1.7	2.0	V
		$I_{OUT} = 100mA$		-	1.8	2.3	
Line regulation	ΔV_{LINE}	$V_{IN} = 7$ to $20V$		-	8	150	mV
Load regulation	ΔV_{LOAD}	$I_{OUT} = 1$ to $100mA$		-	10	60	mV
Output voltage temperature coefficient	$\Delta V_{OUT} / \Delta T$	$I_{OUT} = 5mA$		-	0.42	-	mV/ $^\circ C$
	$\Delta V_{OUT} / (V_{OUT} \times \Delta T)$			-	84	-	ppm/ $^\circ C$
Output noise voltage	V_N	$f = 10$ to $100k$ Hz ⁽¹⁰⁾		-	40	-	μV
Ripple rejection	RR	$V_{IN} = 8$ to $18V$, $f = 120Hz$		47	62	-	dB

Note:

(7) Pulse test technology is used to make T_J as close to T_A as possible. Thermal effects must be considered separately.

(8) Output voltage tolerances of $\pm 3\%$ for conventional device, $\pm 2\%$ can be customized.

(9) The difference of output voltage and input voltage when input voltage is decreased gradually till output voltage equals to 95% of V_{OUT} .

(10) 0.01 μF minimum load capacitance is recommended to limit high frequency noise.

7 Specifications

7.5 Electrical Characteristics (continued)

VS78L06C ($V_{IN} = 11V$, $I_{OUT} = 40mA$, $C_{IN} = 0.33\mu F$, $C_{OUT} = 0.1\mu F$, $T_J = 25^\circ C$, unless otherwise specified)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS ⁽⁷⁾		MIN.	TYP.	MAX.	UNIT
Input voltage	V_{IN}	-		-	-	30	V
Output voltage	V_{OUT}	$T_J = 25^\circ C$	$\pm 3\%$ grade ⁽⁸⁾	5.82	6.00	6.18	V
			$\pm 2\%$ grade ⁽⁸⁾	5.88		6.12	
		$V_{IN} = 8$ to $20V$, $I_{OUT} = 1$ to $100mA$, $T_J = -40$ to $125^\circ C$, $P_D \leq 0.75W$		5.70	6.00	6.30	
Output current	I_{OUT}	$T_J = 25^\circ C$		100	-	-	mA
Quiescent current	I_Q	$I_{OUT} = 0mA$		-	3.0	5.5	mA
Quiescent current change	ΔI_Q	$V_{IN} = 9$ to $20V$, $T_J = -40$ to $125^\circ C$		-	-	1.5	mA
		$I_{OUT} = 1$ to $40mA$, $T_J = -40$ to $125^\circ C$		-	-	0.1	
Dropout voltage	$V_{DO}^{(9)}$	$I_{OUT} = 40mA$		-	1.7	2.0	V
		$I_{OUT} = 100mA$		-	1.8	2.3	
Line regulation	ΔV_{LINE}	$V_{IN} = 8$ to $20V$		-	8	150	mV
Load regulation	ΔV_{LOAD}	$I_{OUT} = 1$ to $100mA$		-	10	60	mV
Output voltage temperature coefficient	$\Delta V_{OUT} / \Delta T$	$I_{OUT} = 5mA$		-	0.50	-	mV/ $^\circ C$
	$\Delta V_{OUT} / (V_{OUT} \times \Delta T)$			-	84	-	ppm/ $^\circ C$
Output noise voltage	V_N	$f = 10$ to $100k$ Hz ⁽¹⁰⁾		-	50	-	μV
Ripple rejection	RR	$V_{IN} = 9$ to $19V$, $f = 120Hz$		47	59	-	dB

Note:

(7) Pulse test technology is used to make T_J as close to T_A as possible. Thermal effects must be considered separately.

(8) Output voltage tolerances of $\pm 3\%$ for conventional device, $\pm 2\%$ can be customized.

(9) The difference of output voltage and input voltage when input voltage is decreased gradually till output voltage equals to 95% of V_{OUT} .

(10) $0.01\mu F$ minimum load capacitance is recommended to limit high frequency noise.

7 Specifications

7.5 Electrical Characteristics (continued)

VS78L08C ($V_{IN} = 14V$, $I_{OUT} = 40mA$, $C_{IN} = 0.33\mu F$, $C_{OUT} = 0.1\mu F$, $T_J = 25^\circ C$, unless otherwise specified)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS ⁽⁷⁾		MIN.	TYP.	MAX.	UNIT
Input voltage	V_{IN}	-		-	-	30	V
Output voltage	V_{OUT}	$T_J = 25^\circ C$	$\pm 3\%$ grade ⁽⁸⁾	7.76	8.00	8.24	V
			$\pm 2\%$ grade ⁽⁸⁾	7.84		8.16	
		$V_{IN} = 10.5$ to $23V$, $T_J = -40$ to $125^\circ C$, $I_{OUT} = 1$ to $100mA$, $P_D \leq 0.75W$		7.60	8.00	8.40	
Output current	I_{OUT}	$T_J = 25^\circ C$		100	-	-	mA
Quiescent current	I_Q	$I_{OUT} = 0mA$		-	3.0	5.5	mA
Quiescent current change	ΔI_Q	$V_{IN} = 11$ to $23V$, $T_J = -40$ to $125^\circ C$		-	-	1.5	mA
		$I_{OUT} = 1$ to $40mA$, $T_J = -40$ to $125^\circ C$		-	-	0.1	
Dropout voltage	$V_{DO}^{(9)}$	$I_{OUT} = 40mA$		-	1.7	2.0	V
		$I_{OUT} = 100mA$		-	1.8	2.3	
Line regulation	ΔV_{LINE}	$V_{IN} = 10.5$ to $23V$		-	8	150	mV
Load regulation	ΔV_{LOAD}	$I_{OUT} = 1$ to $100mA$		-	10	70	mV
Output voltage temperature coefficient	$\Delta V_{OUT} / \Delta T$	$I_{OUT} = 5mA$		-	0.62	-	mV/ $^\circ C$
	$\Delta V_{OUT} / (V_{OUT} \times \Delta T)$			-	84	-	ppm/ $^\circ C$
Output noise voltage	V_N	$f = 10$ to $100k$ Hz ⁽¹⁰⁾		-	60	-	μV
Ripple rejection	RR	$V_{IN} = 13$ to $23V$, $f = 120Hz$		44	53	-	dB

Note:

(7) Pulse test technology is used to make T_J as close to T_A as possible. Thermal effects must be considered separately.

(8) Output voltage tolerances of $\pm 3\%$ for conventional device, $\pm 2\%$ can be customized.

(9) The difference of output voltage and input voltage when input voltage is decreased gradually till output voltage equals to 95% of V_{OUT} .

(10) $0.01\mu F$ minimum load capacitance is recommended to limit high frequency noise.

7 Specifications

7.5 Electrical Characteristics (continued)

VS78L09C ($V_{IN} = 16V$, $I_{OUT} = 40mA$, $C_{IN} = 0.33\mu F$, $C_{OUT} = 0.1\mu F$, $T_J = 25^\circ C$, unless otherwise specified)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS ⁽⁷⁾		MIN.	TYP.	MAX.	UNIT
Input voltage	V_{IN}	-		-	-	30	V
Output voltage	V_{OUT}	$T_J = 25^\circ C$	$\pm 3\%$ grade ⁽⁸⁾	8.73	9.00	9.27	V
			$\pm 2\%$ grade ⁽⁸⁾	8.82		9.18	
		$V_{IN} = 12$ to $24V$, $I_{OUT} = 1$ to $100mA$, $T_J = -40$ to $125^\circ C$, $P_D \leq 0.75W$		8.55	9.00	9.45	
Output current	I_{OUT}	$T_J = 25^\circ C$		100	-	-	mA
Quiescent current	I_Q	$I_{OUT} = 0mA$		-	3.0	5.5	mA
Quiescent current change	ΔI_Q	$V_{IN} = 13$ to $24V$, $T_J = -40$ to $125^\circ C$		-	-	1.5	mA
		$I_{OUT} = 1$ to $40mA$, $T_J = -40$ to $125^\circ C$		-	-	0.1	
Dropout voltage	$V_{DO}^{(9)}$	$I_{OUT} = 40mA$		-	1.7	2.0	V
		$I_{OUT} = 100mA$		-	1.8	2.3	
Line regulation	ΔV_{LINE}	$V_{IN} = 12$ to $24V$		-	8	150	mV
Load regulation	ΔV_{LOAD}	$I_{OUT} = 1$ to $100mA$		-	10	70	mV
Output voltage temperature coefficient	$\Delta V_{OUT} / \Delta T$	$I_{OUT} = 5mA$		-	0.70	-	mV/ $^\circ C$
	$\Delta V_{OUT} / (V_{OUT} \times \Delta T)$			-	84	-	ppm/ $^\circ C$
Output noise voltage	V_N	$f = 10$ to $100k$ Hz ⁽¹⁰⁾		-	65	-	μV
Ripple rejection	RR	$V_{IN} = 15$ to $25V$, $f = 120Hz$		42	51	-	dB

Note:

(7) Pulse test technology is used to make T_J as close to T_A as possible. Thermal effects must be considered separately.

(8) Output voltage tolerances of $\pm 3\%$ for conventional device, $\pm 2\%$ can be customized.

(9) The difference of output voltage and input voltage when input voltage is decreased gradually till output voltage equals to 95% of V_{OUT} .

(10) 0.01 μF minimum load capacitance is recommended to limit high frequency noise.

7 Specifications

7.5 Electrical Characteristics (continued)

VS78L12C ($V_{IN} = 19V$, $I_{OUT} = 40mA$, $C_{IN} = 0.33\mu F$, $C_{OUT} = 0.1\mu F$, $T_J = 25^\circ C$, unless otherwise specified)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS ⁽⁷⁾		MIN.	TYP.	MAX.	UNIT
Input voltage	V_{IN}	-		-	-	35	V
Output voltage	V_{OUT}	$T_J = 25^\circ C$	$\pm 3\%$ grade ⁽⁸⁾	11.64	12.00	12.36	V
			$\pm 2\%$ grade ⁽⁸⁾	11.76		12.24	
		$V_{IN} = 14.5$ to $27V$, $T_J = -40$ to $125^\circ C$, $I_{OUT} = 1$ to $100mA$, $P_D \leq 0.75W$		11.40	12.00	12.60	
Output current	I_{OUT}	$T_J = 25^\circ C$		100	-	-	mA
Quiescent current	I_Q	$I_{OUT} = 0mA$		-	3.0	6.0	mA
Quiescent current change	ΔI_Q	$V_{IN} = 16$ to $27V$, $T_J = -40$ to $125^\circ C$		-	-	1.5	mA
		$I_{OUT} = 1$ to $40mA$, $T_J = -40$ to $125^\circ C$		-	-	0.1	
Dropout voltage	$V_{DO}^{(9)}$	$I_{OUT} = 40mA$		-	1.7	2.0	V
		$I_{OUT} = 100mA$		-	1.8	2.3	
Line regulation	ΔV_{LINE}	$V_{IN} = 14.5$ to $27V$		-	20	250	mV
Load regulation	ΔV_{LOAD}	$I_{OUT} = 1$ to $100mA$		-	20	100	mV
Output voltage temperature coefficient	$\Delta V_{OUT} / \Delta T$	$I_{OUT} = 5mA$		-	1.00	-	mV/ $^\circ C$
	$\Delta V_{OUT} / (V_{OUT} \times \Delta T)$			-	84	-	ppm/ $^\circ C$
Output noise voltage	V_N	$f = 10$ to $100k$ Hz ⁽¹⁰⁾		-	80	-	μV
Ripple rejection	RR	$V_{IN} = 15$ to $25V$, $f = 120Hz$		37	42	-	dB

Note:

(7) Pulse test technology is used to make T_J as close to T_A as possible. Thermal effects must be considered separately.

(8) Output voltage tolerances of $\pm 3\%$ for conventional device, $\pm 2\%$ can be customized.

(9) The difference of output voltage and input voltage when input voltage is decreased gradually till output voltage equals to 95% of V_{OUT} .

(10) 0.01 μF minimum load capacitance is recommended to limit high frequency noise.

7 Specifications

7.5 Electrical Characteristics (continued)

VS78L15C ($V_{IN} = 23V$, $I_{OUT} = 40mA$, $C_{IN} = 0.33\mu F$, $C_{OUT} = 0.1\mu F$, $T_J = 25^\circ C$, unless otherwise specified)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS ⁽⁸⁾		MIN.	TYP.	MAX.	UNIT
Input voltage	V_{IN}	-		-	-	35	V
Output voltage	V_{OUT}	$T_J = 25^\circ C$	$\pm 3\%$ grade ⁽⁹⁾	14.55	15.00	15.45	V
			$\pm 2\%$ grade ⁽⁹⁾	14.70		15.30	
		$V_{IN} = 17.5$ to $30V$, $T_J = -40$ to $125^\circ C$, $I_{OUT} = 1$ to $100mA$, $P_D \leq 0.75W$		14.25	15.00	15.75	
Output current	I_{OUT}	$T_J = 25^\circ C$		100	-	-	mA
Quiescent current	I_Q	$I_{OUT} = 0mA$		-	3.0	6.0	mA
Quiescent current change	ΔI_Q	$V_{IN} = 20$ to $30V$, $T_J = -40$ to $125^\circ C$		-	-	1.5	mA
		$I_{OUT} = 1$ to $40mA$, $T_J = -40$ to $125^\circ C$		-	-	0.1	
Dropout voltage	$V_{DO}^{(10)}$	$I_{OUT} = 40mA$		-	1.7	2.0	V
		$I_{OUT} = 100mA$		-	1.8	2.3	
Line regulation	ΔV_{LINE}	$V_{IN} = 17.5$ to $30V$		-	25	250	mV
Load regulation	ΔV_{LOAD}	$I_{OUT} = 1$ to $100mA$		-	25	150	mV
Output voltage temperature coefficient	$\Delta V_{OUT} / \Delta T$	$I_{OUT} = 5mA$		-	1.25	-	mV/ $^\circ C$
	$\Delta V_{OUT} / (V_{OUT} \times \Delta T)$			-	84	-	ppm/ $^\circ C$
Output noise voltage	V_N	$f = 10$ to $100k$ Hz ⁽¹⁰⁾		-	90	-	μV
Ripple rejection	RR	$V_{IN} = 18.5$ to $28.5V$, $f = 120Hz$		34	39	-	dB

Note:

(7) Pulse test technology is used to make T_J as close to T_A as possible. Thermal effects must be considered separately.

(8) Output voltage tolerances of $\pm 3\%$ for conventional device, $\pm 2\%$ can be customized.

(9) The difference of output voltage and input voltage when input voltage is decreased gradually till output voltage equals to 95% of V_{OUT} .

(10) 0.01 μF minimum load capacitance is recommended to limit high frequency noise.

7 Specifications

7.5 Electrical Characteristics (continued)

VS78L18C ($V_{IN} = 26V$, $I_{OUT} = 40mA$, $C_{IN} = 0.33\mu F$, $C_{OUT} = 0.1\mu F$, $T_J = 25^\circ C$, unless otherwise specified)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS ⁽⁸⁾		MIN.	TYP.	MAX.	UNIT
Input voltage	V_{IN}	-		-	-	35	V
Output voltage	V_{OUT}	$T_J = 25^\circ C$	$\pm 3\%$ grade ⁽⁹⁾	17.46	18.00	18.54	V
			$\pm 2\%$ grade ⁽⁹⁾	17.64		18.36	
		$V_{IN} = 20.5$ to $33V$, $T_J = -40$ to $125^\circ C$, $I_{OUT} = 1$ to $100mA$, $P_D \leq 0.75W$		17.10	18.00	18.9	
Output current	I_{OUT}	$T_J = 25^\circ C$		100	-	-	mA
Quiescent current	I_Q	$I_{OUT} = 0mA$		-	3.0	6.0	mA
Quiescent current change	ΔI_Q	$V_{IN} = 22$ to $33V$, $T_J = -40$ to $125^\circ C$		-	-	1.5	mA
		$I_{OUT} = 1$ to $40mA$, $T_J = -40$ to $125^\circ C$		-	-	0.1	
Dropout voltage	$V_{DO}^{(10)}$	$I_{OUT} = 40mA$		-	1.7	2.0	V
		$I_{OUT} = 100mA$		-	1.8	2.3	
Line regulation	ΔV_{LINE}	$V_{IN} = 20.5$ to $33V$		-	25	250	mV
Load regulation	ΔV_{LOAD}	$I_{OUT} = 1$ to $100mA$		-	25	150	mV
Output voltage temperature coefficient	$\Delta V_{OUT} / \Delta T$	$I_{OUT} = 5mA$		-	1.50	-	mV/ $^\circ C$
	$\Delta V_{OUT} / (V_{OUT} \times \Delta T)$			-	84	-	ppm/ $^\circ C$
Output noise voltage	V_N	$f = 10$ to $100k$ Hz ⁽¹⁰⁾		-	100	-	μV
Ripple rejection	RR	$V_{IN} = 21.5$ to $31.5V$, $f = 120Hz$		34	39	-	dB

Note:

(7) Pulse test technology is used to make T_J as close to T_A as possible. Thermal effects must be considered separately.

(8) Output voltage tolerances of $\pm 3\%$ for conventional device, $\pm 2\%$ can be customized.

(9) The difference of output voltage and input voltage when input voltage is decreased gradually till output voltage equals to 95% of V_{OUT} .

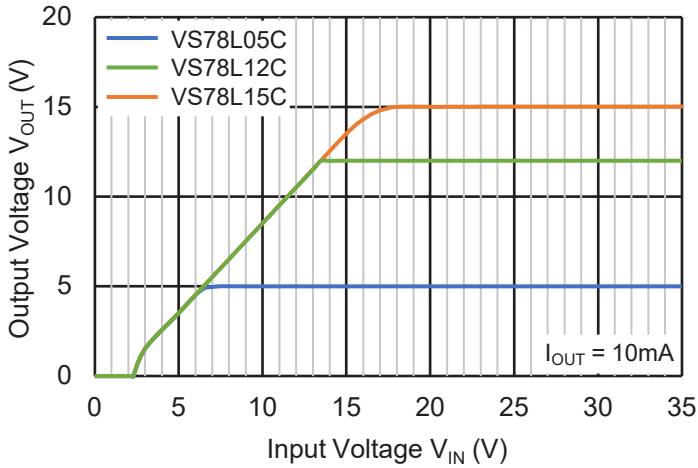
(10) 0.01 μF minimum load capacitance is recommended to limit high frequency noise.

7 Specifications

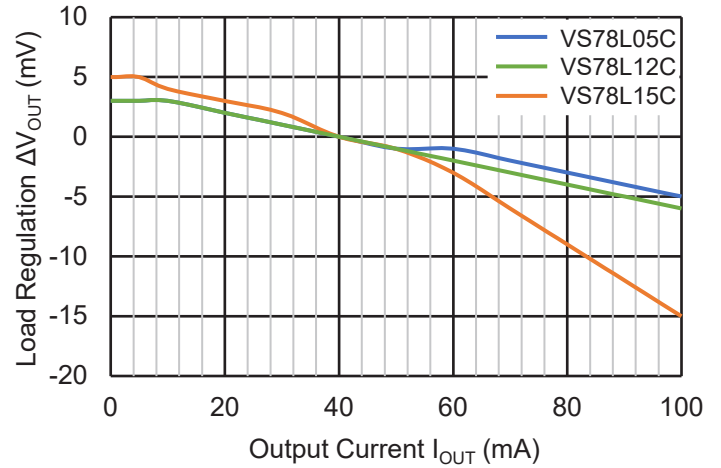
7.6 Typical Characteristics

VS78L00C Series ($V_{IN} = 10V$ for VS78L05C, $V_{IN} = 14.5V$ for VS78L12C, $V_{IN} = 23V$ for VS78L15C, $I_{OUT} = 40mA$, $C_{IN} = 0.33\mu F$, $C_{OUT} = 0.1\mu F$, $T_A = 25^\circ C$, unless otherwise specified)

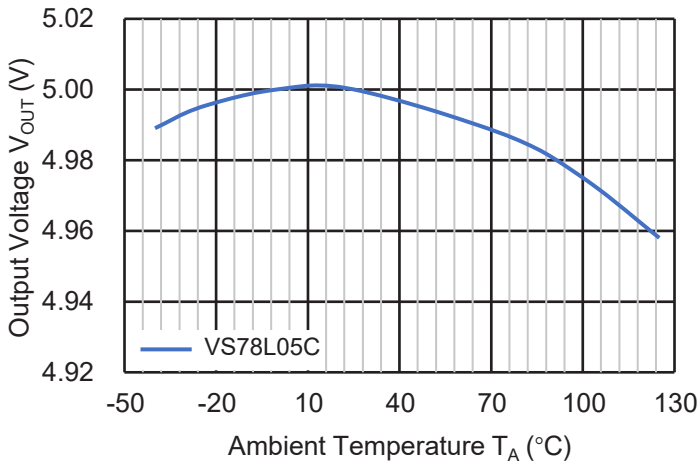
Line Regulation



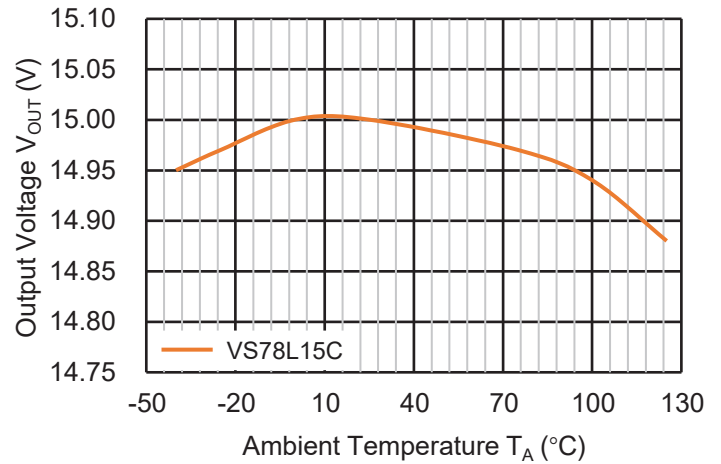
Load Regulation



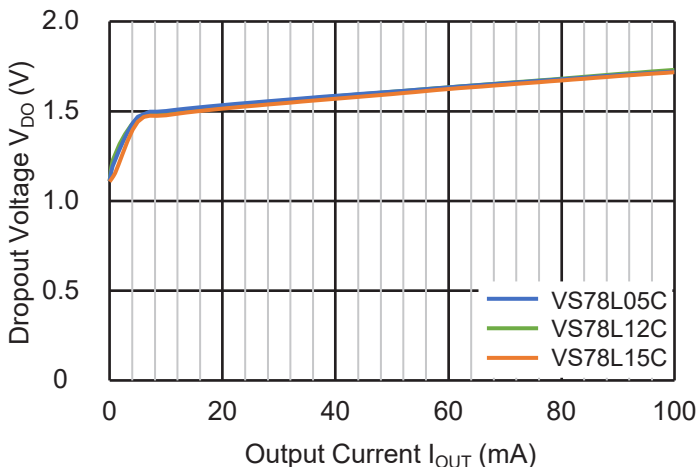
Temperature Characteristics



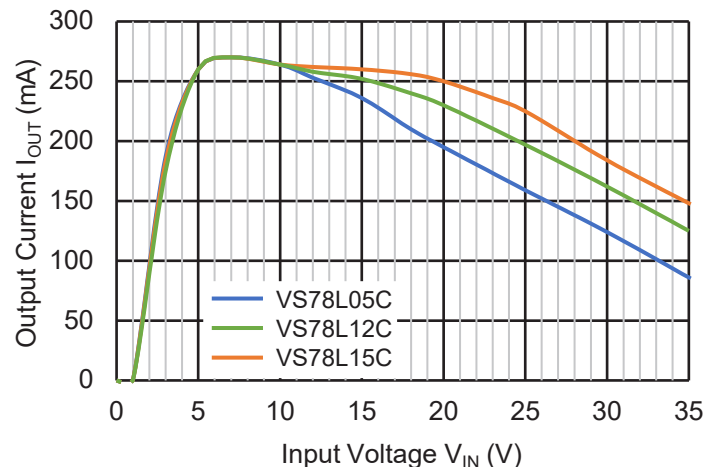
Temperature Characteristics



Dropout Voltage



Output Current Limit

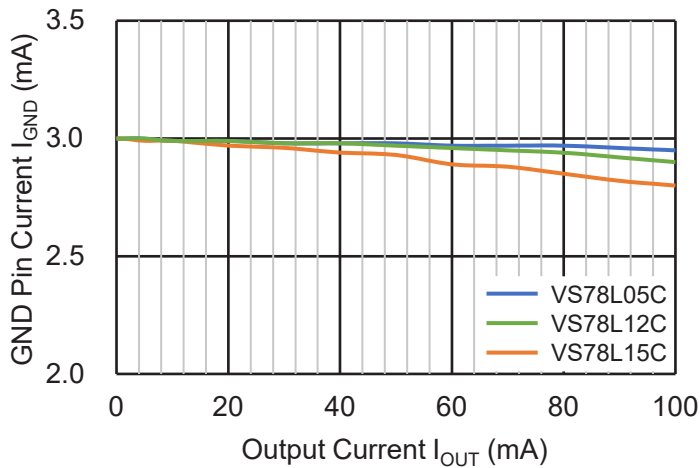


7 Specifications

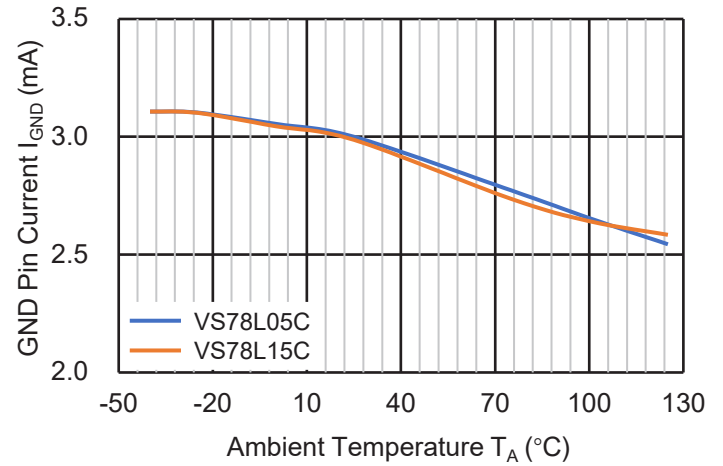
7.6 Typical Characteristics (continued)

VS78L00C Series ($V_{IN} = 10V$ for VS78L05C, $V_{IN} = 14.5V$ for VS78L12C, $V_{IN} = 23V$ for VS78L15C, $I_{OUT} = 40mA$, $C_{IN} = 0.33\mu F$, $C_{OUT} = 0.1\mu F$, $T_A = 25^\circ C$, unless otherwise specified)

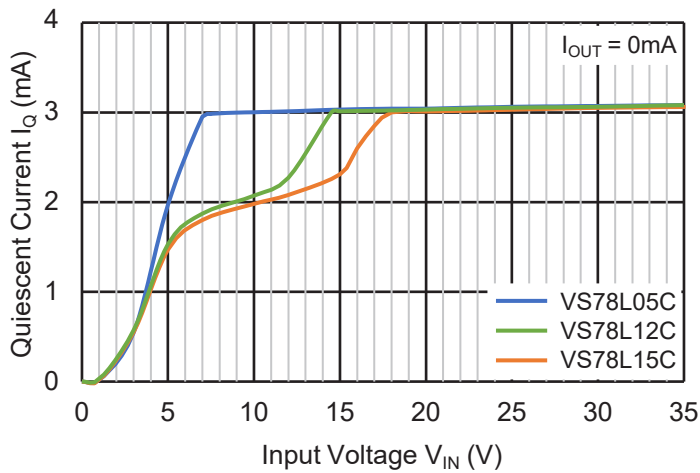
GND Pin Current



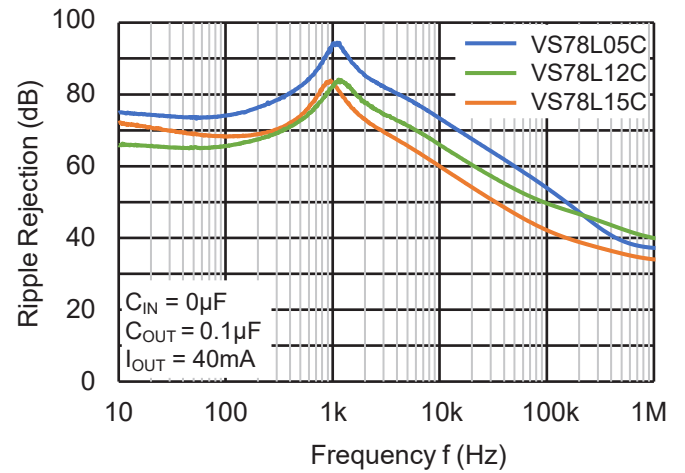
GND Pin Current



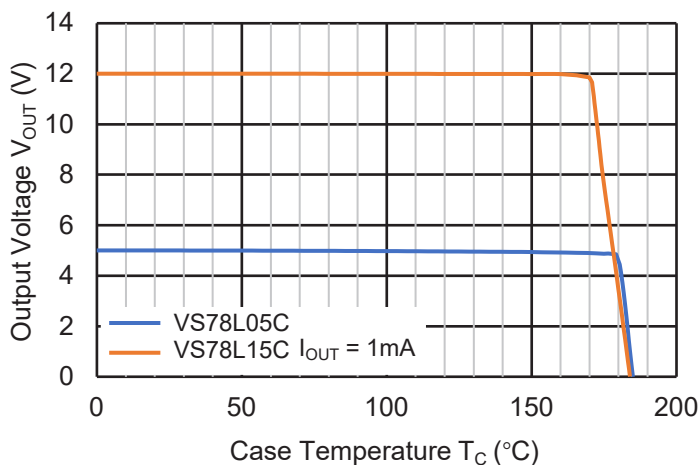
Quiescent Current



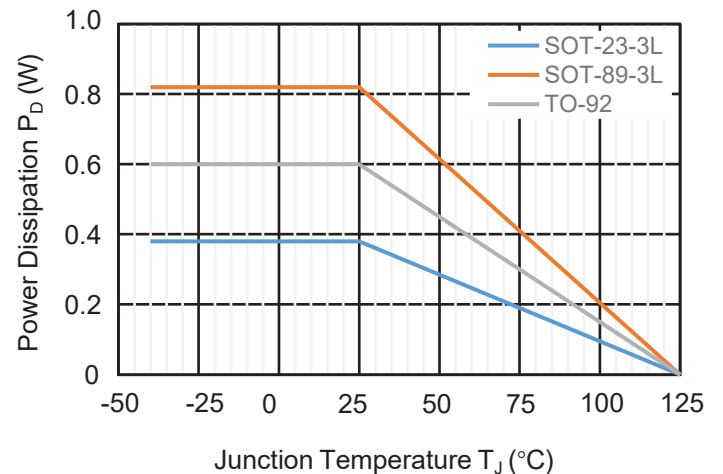
Ripple Rejection



Thermal Shutdown



Power Dissipation



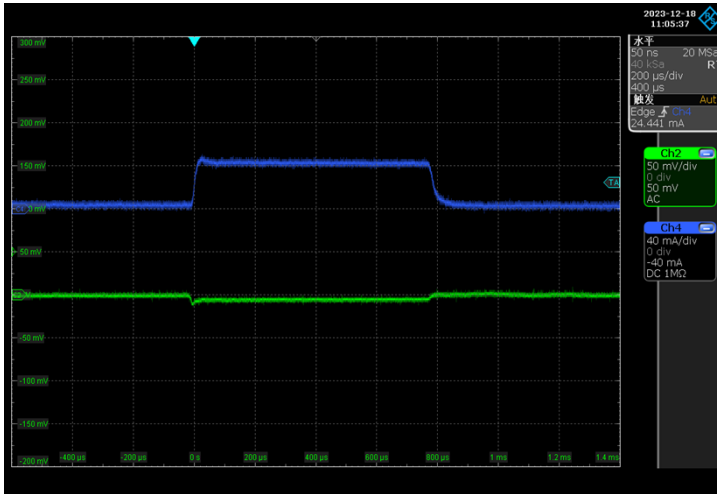
7 Specifications

7.6 Typical Characteristics (continued)

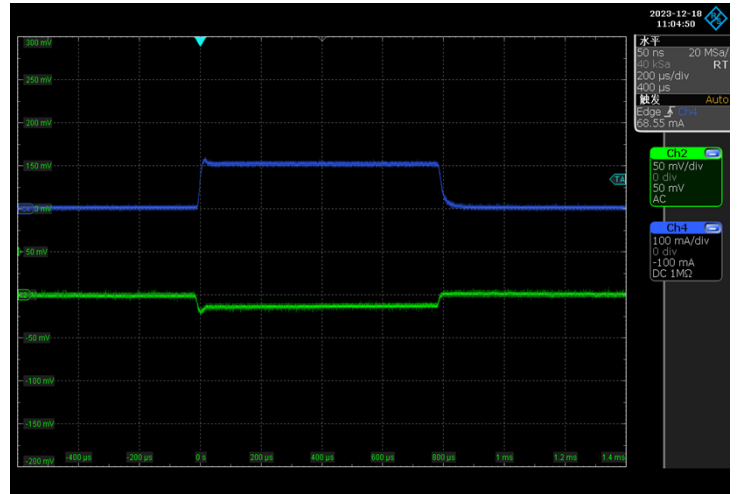
VS78L00C Series ($C_{IN} = 0.33\mu F$, $C_{OUT} = 0.1\mu F$, $T_A = 25^\circ C$, unless otherwise specified)

Load Transient (**CH2: V_{OUT}** , **CH4: I_{OUT}**)

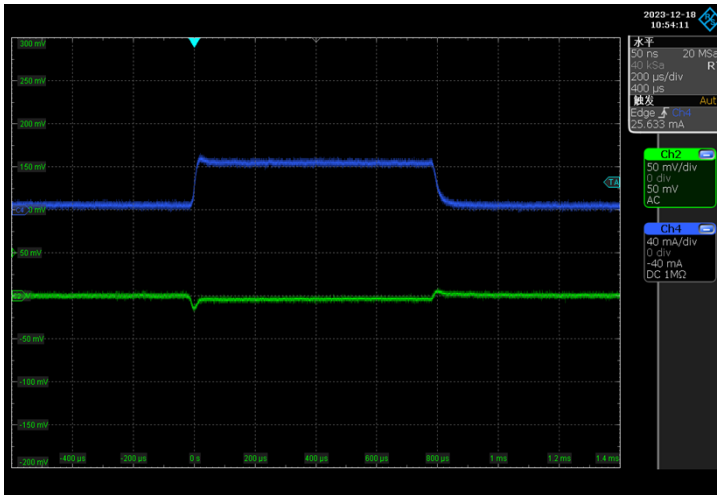
VS78L05C, $V_{IN} = 10V$, $I_{OUT} = 0 \sim 40mA$



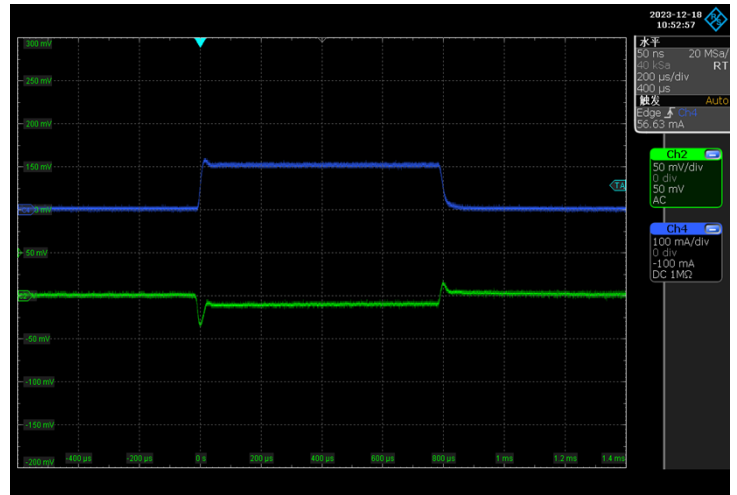
VS78L05C, $V_{IN} = 10V$, $I_{OUT} = 0 \sim 100mA$



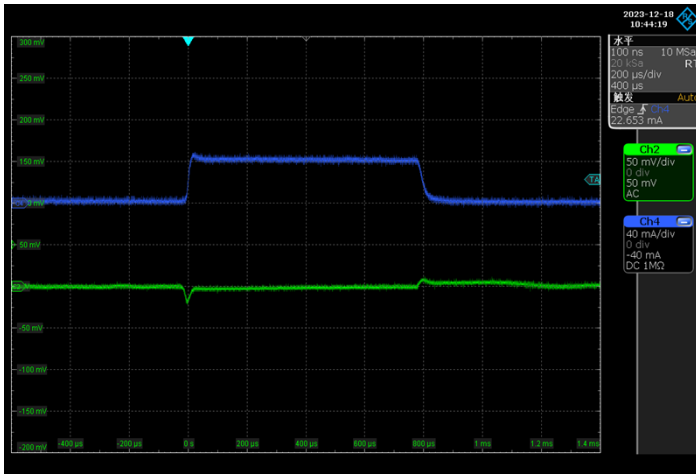
VS78L12C, $V_{IN} = 14.5V$, $I_{OUT} = 0 \sim 40mA$



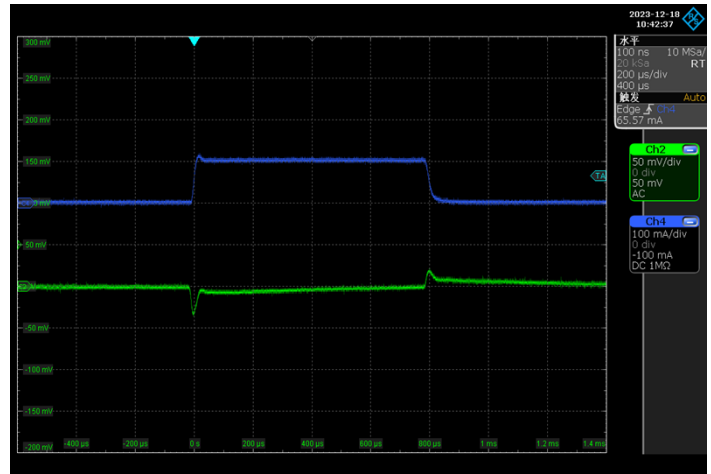
VS78L12C, $V_{IN} = 14.5V$, $I_{OUT} = 0 \sim 100mA$



VS78L15C, $V_{IN} = 23V$, $I_{OUT} = 0 \sim 40mA$



VS78L15C, $V_{IN} = 23V$, $I_{OUT} = 0 \sim 100mA$

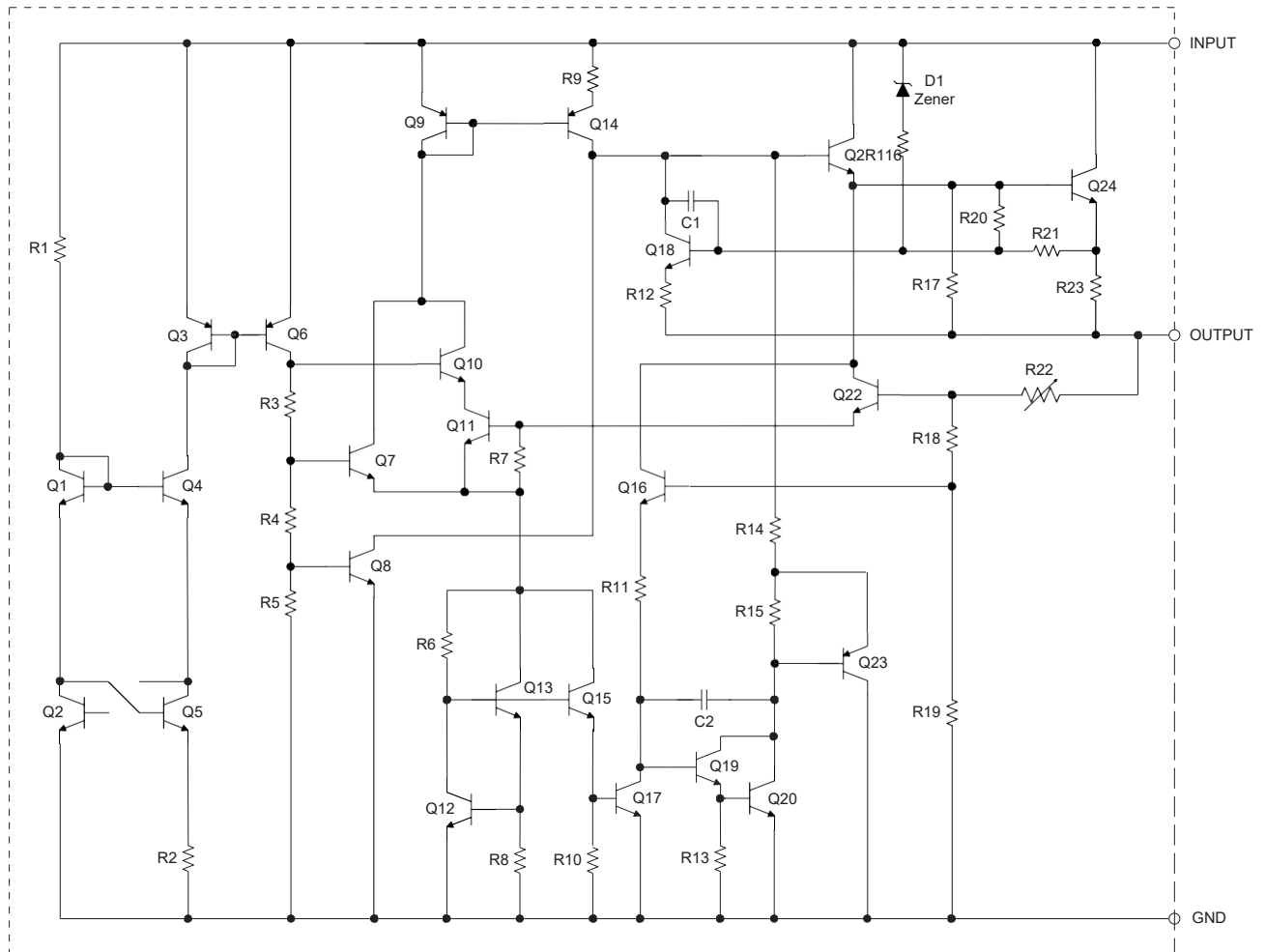


8 Detailed Description

8.1 Description

As a positive voltage linear regulator with a fixed output voltage, the VS78L00C series is designed for a wide range of application conditions. It integrates current limiting, thermal shutdown, and output transistor SOA protection mechanisms to ensure that it is basically unaffected by output overload. By adding and setting two bypass resistors, the fixed output VS78L00C series can be used as a voltage adjustable output circuit.

8.2 Representative Schematic Diagram



8 Detailed Description

8.3 Feature Description

Power Input

When the input voltage is lower than the rated range of the data sheet, the device will lose the regulation function of stabilizing the output voltage, that is, it is unable to maintain the output voltage within the rated range. At this time, compared with normal operation, the quiescent current of the device may exceed the rated range, and the transient response performance of the device may be seriously degraded.

When the input voltage is higher than the rated range of the data sheet, the device may cause irreversible damage or failure due to exceeding the maximum rated range of electrical stress.

Output Current

When the circuit design is appropriate, the VS78L00C series can reach the maximum load capacity of at least 100mA. According to the heat dissipation power consumption of the package and the effective connection thermal resistance with the environment, selecting the appropriate package for the circuit design can make the device emit more heat energy.

Thermal Shutdown

The VS78L00C series has thermal shutdown protection mechanism. When the junction temperature exceeds the rated temperature range for normal operation in the data sheet, the device will enter the thermal shutdown state. At this time, the output voltage of the device will be reduced to prevent catastrophic damage to the chip due to accidental heat. When the junction temperature decreases and no longer remains too high, the device will release the thermal shutdown and output normally.

To ensure reliable operation, please limit the junction temperature to the specified range of recommended operating conditions in the data sheet. Applications that exceed the recommended temperature range may cause the device to exceed its operating specifications. Although the internal protection circuitry of the device is designed to protect against thermal overall conditions, this circuitry is not intended to replace proper heat sinking. Continuously running the device into thermal shutdown or above the maximum recommended junction temperature reduces long-term reliability.

Current Limit & Short Circuit Protection

The VS78L00C series has current limiting and short circuit protection mechanism. When the output current of the device is too high, the output of the device will be shut down. When the output of the device is short circuited to ground, the output of the device will also be shut down and the output current will be maintained within a certain range.

9 Application and Implementation

9.1 Risk Alert and Precautions

The VS78L00C series is designed for thermal protection, output SOA protection and built-in current limit. However, like any IC regulator, precautions are necessary to reduce the possibility of accidental damage to the regulator. The following describes the possible causes of unit damage or failure:

Electrostatic Discharge (ESD) and Instantaneous Electrical Surge

Electrostatic discharge (ESD) is a common near-field hazard source. It comes from many sources, such as human body, mechanical equipment and electronic components themselves. ESD can cause phenomena such as high voltage and instantaneous high current in a very short time, resulting in damage or failure of the device due to electric shock.

In some applications, a short duration but high energy spike may occur in the circuit, including peak voltage and surge current. They may cause unstable operation of the regulator, accelerated aging and potential hazards, and even damage or malfunction of the regulator. These peaks are usually more likely to occur in hot-plug, switch inductance, heavy-load, and other types of circuits.

Precautions for ESD and Electrical Surge

In the practical application of the circuit, adopting the following suggestions can reduce the possibility of device failure due to the above reasons to a certain extent.

1. Place a TVS between the IN and GND of the voltage regulator to absorb the peak voltage that may be generated due to ESD or other reasons. As shown in Figure 9-2;
2. Place a resistor with appropriate resistance in series before the IN of the voltage regulator, which can help the voltage regulator share part of the energy in case of surge. The resistance value of the resistance should not be too large. The specific resistance value depends on the application of the circuit. Generally, the resistance value of this resistance does not exceed 20Ω. As shown in Figure 9-3.
3. For the application circuit using the low ESR multilayer ceramic capacitor (MLCC) type input capacitor, the LC resonant voltage spike caused by hot plugging or power transmission line inductance can be suppressed by using RC suppression circuit for parallel connection of the input capacitor. A very simple method is to parallel a suitable electrolytic capacitor to the input capacitor. As shown in Figure 9-4. For most 100μF/25V electrolytic capacitor has an ESR of about 0.2Ω at 100kHz. This can completely suppress the overshoot phenomenon of the input and minimize the possibility of IC damage due to input voltage spikes.

For the VS78L00C series, it is recommended that the peak voltage should not exceed 36V. When the input voltage of the operating circuit may not meet the application conditions described above, it is recommended to adopt the circuit layout shown in Figure 9-2 to 9-4 in the circuit design. During the power supply design process, the designer must inspect the application circuit to ensure that under no circumstances will the IC be damaged due to the reasons mentioned above.

Large Output Capacitance

The VS78L00C series can obtain better transient response with the help of output capacitance. However, if the output capacitor is relatively large, the surge current generated by the charging of the output capacitor will also be large at the moment of power on of the regulator, and the large surge current passing through the regulator may damage the internal circuit. When the output capacitance is large, adopting the circuit design shown in Figure 9-3 will reduce the possibility of damage to the device due to large surge current to a certain extent. It is recommended that the selection of output capacitor should not exceed 20μF. **If the selection of output capacitor exceeds 20μF, it is recommended to adopt the circuit design in Figure 9-3 to reduce the possibility of accidental failure of the device due to large surge current during power on.**