

OP07C

Description

The OP07C is a high precision operational amplifier, offering low offset voltage and long-term stability. The low offset voltage ($150\mu\text{V}$, maximum) and high gain (up to 200V/mV) make OP07C well-suited in the applications related to instrumentation.

The differential input, including a wide input-voltage range ($\pm 13\text{V}$) and high CMRR (100dB), provides outstanding linearity and gain precision in high loop gain circuit.



SOP-8

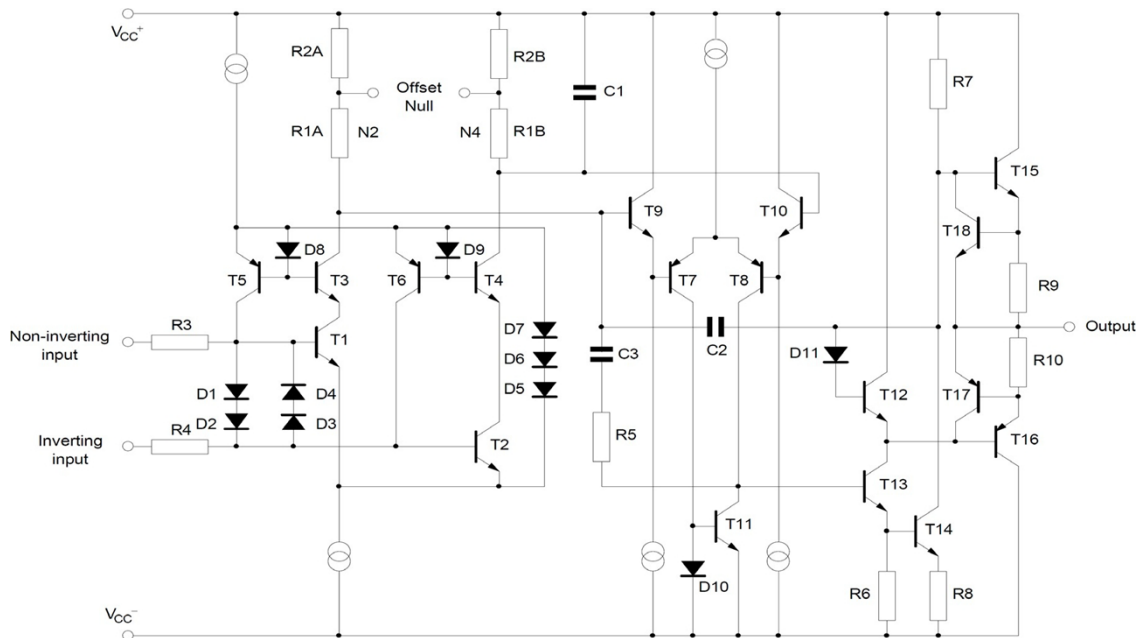
Features

- Low Offset Voltage: $150\mu\text{V}$ (max)
- Low Offset Current: $I_{io}=1.3\text{nA}$ (Typ)
- Wide Input-Voltage Range: $\pm 3\text{V}$ to $\pm 20\text{V}$
- High CMRR: 100dB (min)

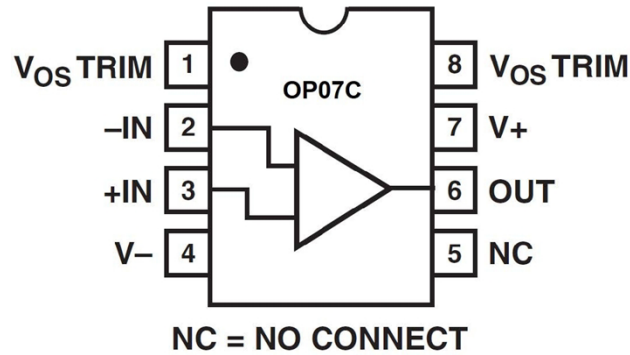
Applications

- Instrumentation
- Precision Filters
- Sensors and Controls

Functional Block Diagram



Pin Configuration



Absolute Maximum Ratings (Note 1)

Parameter	Symbol	Value	Unit
Power Supply Voltage	V_{CC}	± 20	V
Differential Input Voltage	V_{ID}	± 30	V
Input Voltage	V_I	± 18	V
Operating Temperature	T_{OPR}	-10 to 85	°C
Storage Temperature Range	T_{STG}	-40 to 150	°C

Note 1: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device under these conditions is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

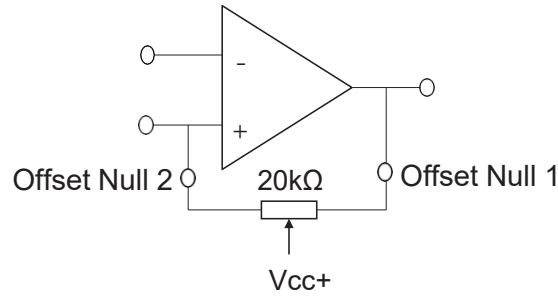
ELECTRICAL CHARACTERISTICS

V_{CC}=±15V, GND=0, T_A=25°C unless otherwise specified.

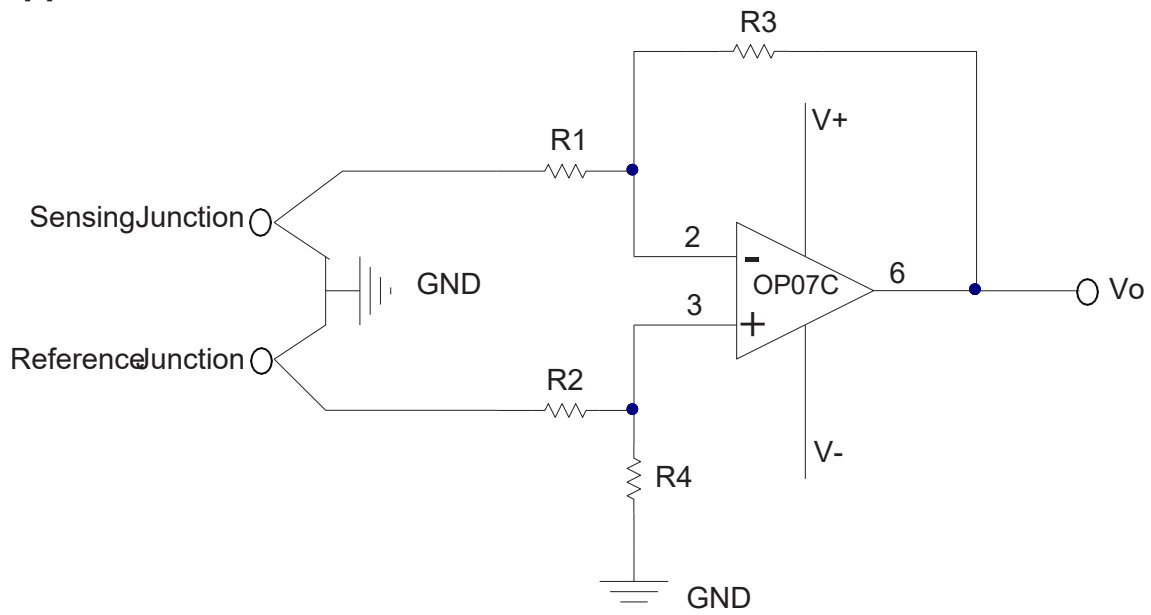
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Input Offset Voltage	V _{IO}	Ta=25°C			150	μV
		0°C<Ta<70°C			250	
Input Bias Current (Note 2)	I _{BIAS}	I _{IN} ⁺ or I _{IN} ⁻ , V _{CM} =0V			28	nA
Input Offset Current	I _{IO}	I _{IN} ⁺ -I _{IN} ⁻ , V _{CM} =0V			8	nA
Peak Output Voltage	V _{OM}	R _L =10KΩ	±12			V
		R _L =2KΩ	±11.5			
Supply Current	I _{CC}	I _{out} =0A		3.8	6	mA
		0°C<Ta<70°C			7	
		V _{CC} =±3V		1	3	
Large Signal Voltage Gain	G _V	V _{CC} =±15V, R _L ≧10KΩ, V _O =±10V	100			V/mV
Common Mode Rejection Ratio	CMRR		100			dB
Power Supply Rejection Ration	PSRR		90			dB
Gain Bandwidth Product	GBP	R _L =2kΩ, C _L ≦100pF, f=100kHz		0.5		MHZ
Input Common Voltage	V _{ICM}		±13	±13.5		V
Temperature coefficient of input offset voltage	DV _{IO}				1.8	μV/°C

Note 2: The direction of the input current is out of the IC due to the PNP input stage. This current is essentially constant, independent of the state of the output so no loading change exists on the input lines.

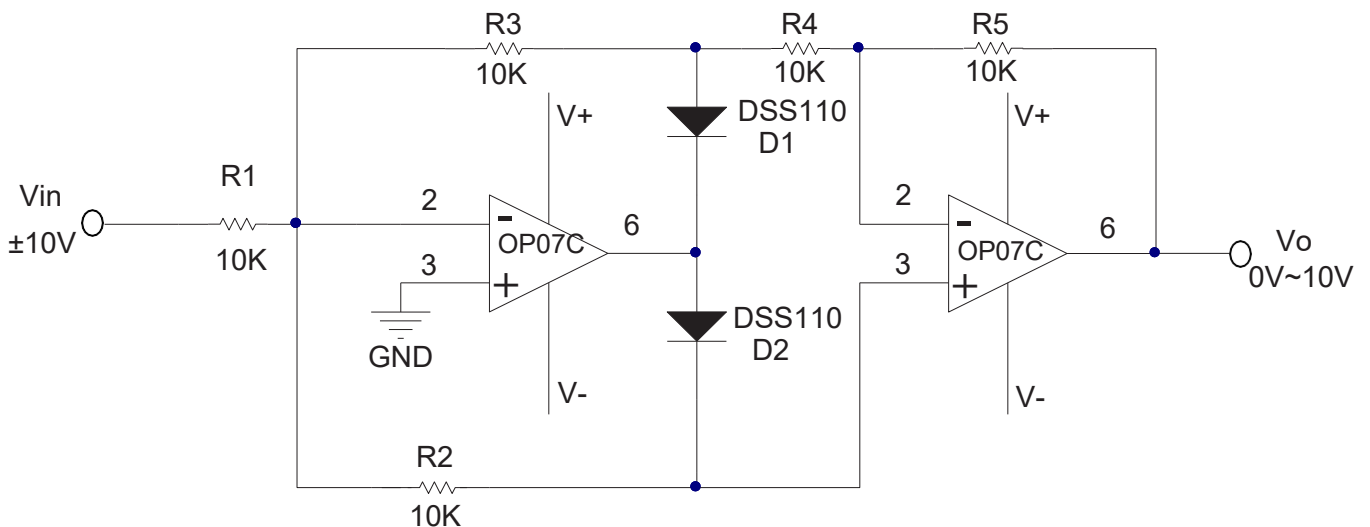
Input Offset-Voltage Null Circuit



Typical Application



Circuit1. High stability thermocouple amplification



Circuit2. Precision absolute value circuit